

FP4332-203-VB Datasheet N-Channel 250 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)	Q _g (Тур)	
250	0.040 at V _{GS} = 10 V	60	95	
230	0.045 at V _{GS} = 6 V	55	90	

FEATURES

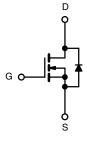
- Trench Power MOSFETS
- 175 °C Junction Temperature
- New Low Thermal Resistance Package
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

Industrial



Top View



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_c = 25$ °C, unless otherwise noted)						
Parameter	Symbol	Limit	Unit			
Drain-Source Voltage	V _{DS}	250	V			
Gate-Source Voltage		V _{GS}			± 30	
Continuous Drain Current (T _{.1} = 175 °C)	T _C = 25 °C	1_	60	А		
Continuous Drain Current $(1) = 175^{\circ}$ C)	T _C = 125 °C		35			
Pulsed Drain Current	I _{DM}	200	A			
Avalanche Current	I _{AR}	35				
Repetitive Avalanche Energy ^a	L = 0.1 mH	E _{AR}	61	mJ		
	T _C = 25 °C	Р	300 ^b	w		
Maximum Power Dissipation ^a	T _A = 25 °C ^c	– P _D –	3.75			
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Limit	Unit		
Junction-to-Ambient (PCB Mount) ^c	R _{thJA}	40	°C/W		
Junction-to-Case (Drain)	R _{thJC}	0.5	°C/W		

Notes:

a. Duty cycle \leq 1 %.

b. See SOA curve for voltage derating.

c. When mounted on 1" square PCB (FR-4 material).

	SPECIFICATIONS ($T_J = 25$	°C, unless c	otherwise noted)				
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	Parameter	Symbol	Test Conditions	Min .	Тур.	Max.	Unit
Gate Threshold Voltage VGS(th) VDS = VGS, ID = 250 µA 2 4 V Gate-Body Leakage IGSS VDS = 0 V, VGS = ± 30 V ± 250 nA Gate-Body Leakage IGSS VDS = 0 V, VGS = ± 30 V ± 250 nA Zero Gate Voltage Drain Current IDSS VDS = 250 V, VGS = 0 V 1 µA On-State Drain Current ^a ID(0) VDS = 250 V, VGS = 0 V, TJ = 125 °C 50 µA Drain-Source On-State Resistance ^a PDS(on) VGS = 10 V, ID = 30 A 0.040 µA VGS = 10 V, ID = 30 A, TJ = 175 °C 0.123	Static	•					
$ \begin{array}{c c c c c c } \mbox{Gate Threshold Voltage} & V_{GS}(m) & V_{GS} = V_{SS}, h_{O} = 250 \mu A & 2 & 4 \\ \hline \mbox{Gate Threshold Voltage} & I_{GSS} & V_{DS} = 0 V, V_{GS} = 30 V & 1 & 1 \\ \hline \mbox{V}_{DS} = 250 V, V_{GS} = 0 V, T_{J} = 125 °C & 0 & 250 \\ \hline \mbox{V}_{DS} = 250 V, V_{GS} = 0 V, T_{J} = 125 °C & 0 & 250 \\ \hline \mbox{V}_{DS} = 250 V, V_{GS} = 0 V, T_{J} = 175 °C & 0 & 250 \\ \hline \mbox{V}_{DS} = 250 V, V_{GS} = 10 V & 70 & A \\ \hline \mbox{V}_{DS} = 250 V, V_{GS} = 10 V & 70 & 0 & A \\ \hline \mbox{V}_{DS} = 10 V, I_{D} = 30 A & T_{J} = 125 °C & 0.091 & 0 \\ \hline \mbox{V}_{GS} = 10 V, I_{D} = 30 A, T_{J} = 175 °C & 0.091 & 0 \\ \hline \mbox{V}_{GS} = 10 V, I_{D} = 30 A, T_{J} = 175 °C & 0.091 & 0 \\ \hline \mbox{V}_{GS} = 10 V, I_{D} = 30 A, T_{J} = 175 °C & 0.091 & 0 \\ \hline \mbox{V}_{GS} = 0 V, V_{DS} = 15 V, I_{D} = 30 A & 70 & 0 & S \\ \hline \mbox{Dyname}^{b} & & & & & & & & & & & & & & & & & & &$	Drain-Source Breakdown Voltage	V _{DS}	$V_{DS} = 0 V, I_{D} = 250 \mu A$	250			V
$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	2		4	v
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Gate-Body Leakage	I _{GSS}	V_{DS} = 0 V, V_{GS} = ± 30 V			± 250	nA
$\begin{tabular}{ c c c c c c } \hline V_{DS} = $250 V, V_{GS} = 0 V, T_J = $175 °C$ & 250 & A \\ \hline V_{DS} = $250 V, V_{GS} = $10 V$ & 70 & A \\ \hline V_{GS} = $10 V, I_{D} = $30 A$ & 0.040 & V_{GS} = $10 V, I_{D} = $30 A$ & 0.040 & V_{GS} = $10 V, I_{D} = $30 A$ & 0.040 & V_{GS} = $10 V, I_{D} = $30 A$ & 0.040 & V_{GS} = $10 V, I_{D} = $30 A$ & 0.045 & V_{GS} = $10 V, I_{D} = $30 A$, T_{J} = $125 °C$ & 0.091 & V_{GS} = $10 V, I_{D} = $30 A$ & 70 & S \\ \hline V_{GS} = $10 V, I_{D} = $30 A$ & 70 & S & D \\ \hline V_{GS} = $10 V, I_{D} = $30 A$ & 70 & S & D \\ \hline V_{GS} = $10 V, I_{D} = $30 A$ & 70 & S & D \\ \hline V_{GS} = $0 V, V_{DS} = $15 V, I_{D} = $30 A$ & 70 & S & D \\ \hline D uput Capacitance & C_{iss} & V_{GS} = $0 V, V_{DS} = $25 V, f = $1 MHz$ & 300 & P & P & 140 & 0 \\ \hline $Cate Drain Charge^{C} & Q_{g} & V_{DS} = $125 V, V_{GS} = $10 V, I_{D} = $45 A$ & 28 & n & $$			$V_{DS} = 250 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	μΑ
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 250 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$			50	
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $			$V_{DS} = 250 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$			250	
$ \begin{array}{ c c c c c c } \mbox{Prime} Prime$	On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	70			А
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $			V _{GS} = 10 V, I _D = 30 A		0.040		- Ω
$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$		Р	V_{GS} = 10 V, I _D = 30 A, T _J = 125 °C		0.091		
Forward Transconductance ^a g_{fs} $V_{DS} = 15$ V, $I_D = 30$ A 70 S Dynamic ^b $V_{DS} = 15$ V, $I_D = 30$ A 70 S Input Capacitance C_{iss} $V_{GS} = 0$ V, $V_{DS} = 25$ V, $f = 1$ MHz 5000 pF Output Capacitance C_{css} $V_{GS} = 0$ V, $V_{DS} = 25$ V, $f = 1$ MHz 300 pF Reverse Transfer Capacitance C_{rss} $V_{DS} = 125$ V, $V_{GS} = 10$ V, $I_D = 45$ A 28 nC Gate-Source Charge ^c Q_{gd} $f = 1$ MHz 1.6 Ω Gate Resistance R_g $f = 1$ MHz 1.6 Ω Turn-On Delay Time ^c t_f $V_{DD} = 100$ V, $R_I = 2.78 \Omega$ 222 330 ns Rise Time ^c t_f $V_{DD} = 100$ V, $R_g = 2.5 \Omega$ 400 60 ns Source-Drain Diode Ratings and Characteristics ($T_C = 25$ °C) ^b 45 A A Continuous Current I_S $I_F = 45$ A, $V_{GS} = 0$ V 1 1.5 V Pulsed Current I_S $I_F = 45$ A, $di/dt = 100$ A/ μ	Drain-Source On-State Resistance	DS(on)	V_{GS} = 10 V, I _D = 30 A, T _J = 175 °C		0.123		
Dynamic ^b Source Ciss Source Sour			$V_{GS} = 6 V, I_D = 25 A$		0.045		
$ \begin{array}{ c c c c c c } \hline \text{Input Capacitance} & C_{1SS} & & & & & & & & & & & & & & & & & & $	Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 30 A		70		S
$ \begin{array}{c c c c c c c c } \hline Output Capacitance & C_{oss} & V_{GS} = 0 \ V, \ V_{DS} = 25 \ V, \ f = 1 \ MHz & 300 & 1 \\ \hline 0 \ 170 & 1 \\ \hline 180 & 1 \\ \hline 180$	Dynamic ^b	•	•		÷		
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Input Capacitance	C _{iss}			5000		pF
$ \begin{array}{c c c c c c c c c } \hline Total Gate Charge^{c} & Q_{g} & & & & & & & & & & & & & & & & & & &$	Output Capacitance	C _{oss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz		300		
Gate-Source Charge ^c Q_{gs} $V_{DS} = 125 V$, $V_{GS} = 10 V$, $I_D = 45 A$ 28 nC Gate-Drain Charge ^c Q_{gd} 34	Reverse Transfer Capacitance	C _{rss}			170		
$ \begin{array}{c c c c c c c c c c } \hline Gate-Drain Charge^{\circ} & Q_{gd} & & & & & & & & & & & & & & & & & & &$	Total Gate Charge ^c	Qg			95	140	nC
$ \begin{array}{c c c c c c c c } \hline Gate Resistance & R_g & f = 1 \ MHz & 1.6 & \Omega \\ \hline Turn-On Delay Time^{C} & t_{d(on)} \\ \hline Rise Time^{C} & t_r & V_{DD} = 100 \ V, \ R_L = 2.78 \ \Omega \\ \hline I_D \cong 45 \ A, \ V_{GEN} = 10 \ V, \ R_g = 2.5 \ \Omega \\ \hline I_D \cong 45 \ A, \ V_{GEN} = 10 \ V, \ R_g = 2.5 \ \Omega \\ \hline I_1 I Time^{C} & t_f & 145 \ 220 \\ \hline Source-Drain Diode Ratings and Characteristics (T_C = 25 \ ^{\circ}C)^{b} \\ \hline Continuous \ Current & I_S & 40 & 60 \\ \hline Pulsed \ Current & I_{SM} & 70 \\ \hline Forward \ Voltage^{a} & V_{SD} & I_F = 45 \ A, \ V_{GS} = 0 \ V & 1 & 1.5 & V \\ \hline Reverse \ Recovery \ Time & t_{rr} & 150 \ 225 \ ns \\ \hline Peak \ Reverse \ Recovery \ Current & I_{RM(REC)} & I_F = 45 \ A, \ di/dt = 100 \ A/\mus & 12 \ 18 \ A \\ \hline \end{array}$	Gate-Source Charge ^c	Q _{gs}	V_{DS} = 125 V, V_{GS} = 10 V, I_{D} = 45 A		28		
$ \begin{array}{c c c c c c c c c } \hline Turn-On \ Delay \ Time^{C} & t_{d(on)} \\ \hline Rise \ Time^{C} & t_{r} \\ \hline Turn-Off \ Delay \ Time^{C} & t_{d(off)} \\ \hline Turn-Off \ Delay \ Time^{C} & t_{d(off)} \\ \hline Fall \ Time^{C} & t_{f} \\ \hline \end{array} & \begin{array}{c c c c c c c c c c c c c c c c c c c $	Gate-Drain Charge ^c	Q _{gd}			34		
$\begin{array}{c c c c c c c } \hline \text{Rise Time}^{C} & \text{tr} \\ \hline \text{Turn-Off Delay Time}^{C} & t_{d(off)} \\ \hline \text{Fall Time}^{C} & t_{f} \\ \hline \text{Source-Drain Diode Ratings and Characteristics } (T_{C} = 25 \ ^{\circ}\text{C})^{b} \\ \hline \text{Continuous Current} & l_{S} \\ \hline \text{Pulsed Current} & l_{SM} \\ \hline \text{Forward Voltage}^{a} & V_{SD} & l_{F} = 45 \ \text{A}, \ V_{GS} = 0 \ \text{V} \\ \hline \text{Reverse Recovery Time} & t_{rr} \\ \hline \text{Peak Reverse Recovery Current} & l_{\text{RM}(\text{REC})} \\ \hline \text{Reverse Recovery Current} & \text{Reverse Recovery Current} \\ \hline \text{Reverse Recovery Current} & l_{\text{RM}(\text{REC})} \\ \hline \text{Reverse Recovery Current} & \text{Reverse Recovery Current} \\ \hline \text{Reverse Recovery Current} & \text{Reverse Recovery Current} \\ \hline \text{Reverse Recovery Current} & \text{Reverse Recovery Current} \\ \hline \text{Reverse Recovery Current} & \text{Reverse Recovery Current} \\ \hline \text{Reverse Recovery Current} & \text{Reverse Recovery Current} \\ \hline \text{Reverse Recovery Current} & \text{Reverse Recovery Current} \\ \hline \text{Reverse Recovery Current} & \text{Reverse Recovery Current} \\ \hline \text{Reverse Recovery Current} & \text{Reverse Recovery Current} \\ \hline \text{Reverse Recovery Current} & \text{Reverse Recovery Current} \\ \hline \text{Reverse Recovery Current} & \text{Reverse Recovery Current} \\ \hline \text{Reverse Recovery Current} \\ \hline \text{Reverse Recovery Current} & \text{Reverse Recovery Current} \\ \hline \text{Reverse Recovery Current} \\ \hline Reverse Reco$	Gate Resistance	Rg	f = 1 MHz		1.6		Ω
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	Turn-On Delay Time ^c	t _{d(on)}			22	35	
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Rise Time ^c		V_{DD} = 100 V, R_L = 2.78 Ω		220	330	ns -
Source-Drain Diode Ratings and Characteristics $(T_C = 25 \ ^{\circ}C)^b$ Continuous CurrentIs45Pulsed CurrentIs70Forward Voltage ^a V_{SD} I _F = 45 A, $V_{GS} = 0 \ V$ 1Reverse Recovery Time t_{rr} 150225nsPeak Reverse Recovery CurrentI _{RM(REC)} I _F = 45 A, di/dt = 100 A/µs1218A	Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong 45$ A, V_{GEN} = 10 V, R_g = 2.5 Ω		40	60	
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	Fall Time ^c	t _f			145	220	
Pulsed Current I _{SM} 70 A Forward Voltage ^a V_{SD} $I_F = 45 \text{ A}, V_{GS} = 0 \text{ V}$ 1 1.5 V Reverse Recovery Time t_{rr} 150 225 ns Peak Reverse Recovery Current $I_{RM(REC)}$ $I_F = 45 \text{ A}, di/dt = 100 \text{ A}/\mu s$ 12 18 A	Source-Drain Diode Ratings and Cha	aracteristics ($T_{\rm C} = 25 \ {}^{\circ}{\rm C})^{\rm b}$				
Pulsed CurrentI SMToToForward VoltageaV SDI F=45 A, V GS = 0 V11.5VReverse Recovery Time t_{rr} 150225nsPeak Reverse Recovery CurrentI RM(REC)I F=45 A, di/dt = 100 A/µs1218A	Continuous Current	ا _S				45	^
Reverse Recovery Time t_{rr} 150225nsPeak Reverse Recovery Current $I_{RM(REC)}$ $I_F = 45 \text{ A}, di/dt = 100 \text{ A/}\mu\text{s}$ 1218A	Pulsed Current	I _{SM}				70	
Peak Reverse Recovery CurrentI RM(REC)I F = 45 A, di/dt = 100 A/µs1218A	Forward Voltage ^a	V _{SD}	$I_{F} = 45 \text{ A}, V_{GS} = 0 \text{ V}$		1	1.5	V
	Reverse Recovery Time	t _{rr}	I _F = 45 A, di/dt = 100 A/μs		150	225	ns
Reverse Recovery ChargeQ _{rr} 0.92μC	Peak Reverse Recovery Current	I _{RM(REC)}			12	18	А
	Reverse Recovery Charge	Q _{rr}			0.9	2	μC

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

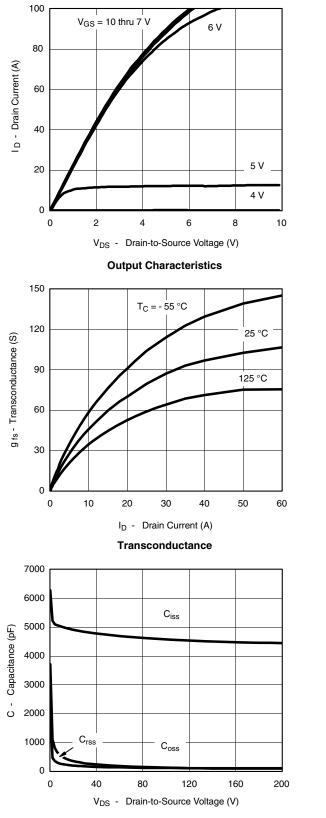
b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

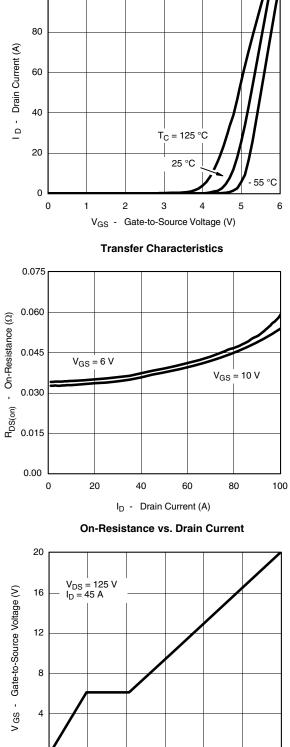
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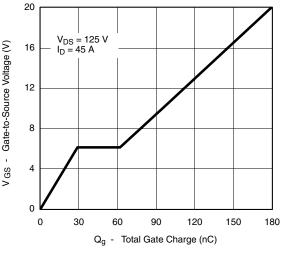


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





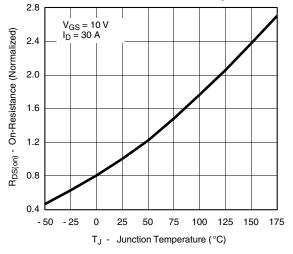
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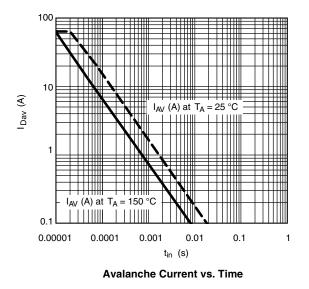
Gate Charge

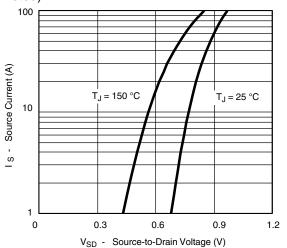


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

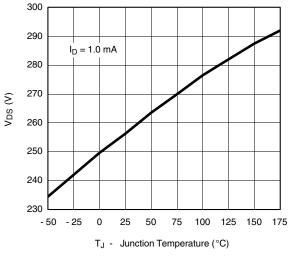


On-Resistance vs. Junction Temperature





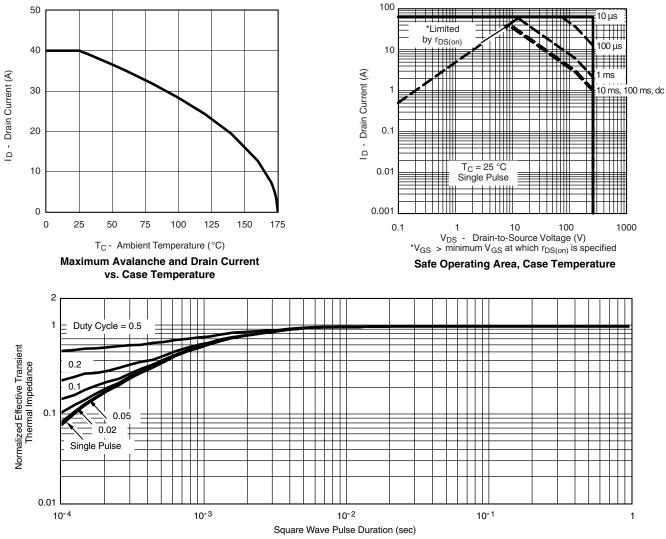
Source-Drain Diode Forward Voltage



Drain Source Breakdown vs. Junction Temperature



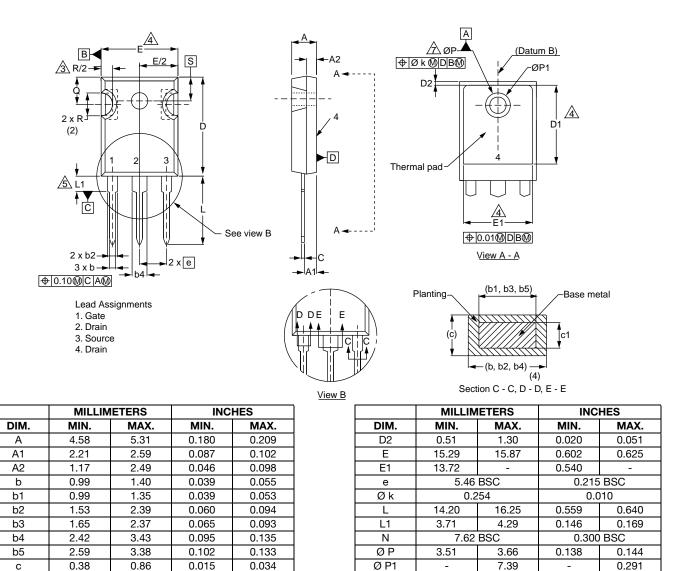
THERMAL RATINGS



Normalized Thermal Transient Impedance, Junction-to-Case







Q

R

S

5.31

4.52

5.51 BSC

5.69

5.49

0.209

0.178

0.217 BSC

0.224

0.216

c1

D

D1

0.38

19.71

13.08

0.76

20.82

-

0.015

0.776

0.515

0.030

0.820

-



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