

## 95N25W-VB Datasheet N-Channel 250 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY							
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω)	I <sub>D</sub> (A)	Q <sub>g</sub> (Тур)				
250	0.040 at V <sub>GS</sub> = 10 V	60	95				
	0.045 at V <sub>GS</sub> = 6 V	55	90				

#### FEATURES

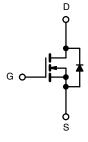
- Trench Power MOSFETS
- 175 °C Junction Temperature
- New Low Thermal Resistance Package
- Compliant to RoHS Directive 2002/95/EC

#### **APPLICATIONS**

Industrial



Top View



N-Channel MOSFET

ABSOLUTE MAXIMUM RATING	<b>S</b> (T <sub>C</sub> = 25 °C, unless oth	erwise noted)			
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V <sub>DS</sub>	250	M		
Gate-Source Voltage	V <sub>GS</sub>	± 30	V		
Continuous Drain Current (T <sub>.1</sub> = 175 °C)	T <sub>C</sub> = 25 °C	1_	60	А	
Continuous Drain Current $(1) = 175^{\circ}$ C)	T <sub>C</sub> = 125 °C		35		
Pulsed Drain Current	I <sub>DM</sub>	200	A .		
Avalanche Current	I <sub>AR</sub>	35			
Repetitive Avalanche Energy <sup>a</sup>	L = 0.1 mH	E <sub>AR</sub>	61	mJ	
Marian David Disainaliana	T <sub>C</sub> = 25 °C	Р	300 <sup>b</sup>	14/	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C <sup>c</sup>	– P <sub>D</sub> –	3.75	W	
Operating Junction and Storage Temperature R	ange	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Limit	Unit	
Junction-to-Ambient (PCB Mount) <sup>c</sup>	R <sub>thJA</sub>	40	°C/W	
Junction-to-Case (Drain)	R <sub>thJC</sub>	0.5	°C/W	

Notes:

a. Duty cycle  $\leq$  1 %.

b. See SOA curve for voltage derating.

c. When mounted on 1" square PCB (FR-4 material).

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	Cymbol			- 76-	mux.	Unit	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>DS</sub> = 0 V, I <sub>D</sub> = 250 μA	250				
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	2		4	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 30 V$			± 250	nA	
		V <sub>DS</sub> = 250 V, V <sub>GS</sub> = 0 V			1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 250 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$			50	μA	
		$V_{DS} = 250 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$			250		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	70			Α	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A		0.040			
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 125 °C		0.091		Ω	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 175 °C		0.123			
		$V_{GS} = 6 V, I_D = 25 A$		0.045			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A		70		S	
Dynamic <sup>b</sup>	+			*			
Input Capacitance	C <sub>iss</sub>			5000		pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 V$ , $V_{DS} = 25 V$ , f = 1 MHz		300			
Reverse Transfer Capacitance	C <sub>rss</sub>			170			
Total Gate Charge <sup>c</sup>	Qg			95	140	nC	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS}$ = 125 V, $V_{GS}$ = 10 V, $I_{D}$ = 45 A		28			
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			34			
Gate Resistance	Rg	f = 1 MHz		1.6		Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			22	35		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD}$ = 100 V, $R_L$ = 2.78 $\Omega$		220	330	ns	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$\text{I}_\text{D}\cong$ 45 A, $\text{V}_\text{GEN}$ = 10 V, $\text{R}_\text{g}$ = 2.5 $\Omega$		40	60	115	
Fall Time <sup>c</sup>	t <sub>f</sub>			145	220		
Source-Drain Diode Ratings and Cha	aracteristics (	$T_{\rm C} = 25 \ {}^{\circ}{\rm C})^{\rm b}$		·			
Continuous Current	ا <sub>S</sub>				45	А	
Pulsed Current	I <sub>SM</sub>				70	7	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_{F} = 45 \text{ A}, V_{GS} = 0 \text{ V}$		1	1.5	V	
Reverse Recovery Time	t <sub>rr</sub>			150	225	ns	
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	I <sub>F</sub> = 45 A, di/dt = 100 A/μs		12	18	A	
Reverse Recovery Charge	Q <sub>rr</sub>			0.9	2	μC	

Notes:

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

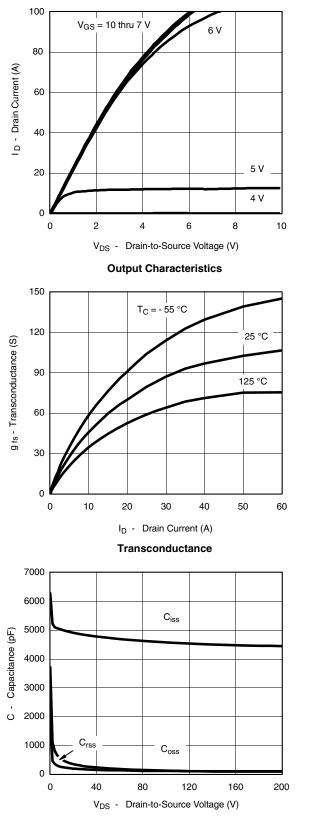
b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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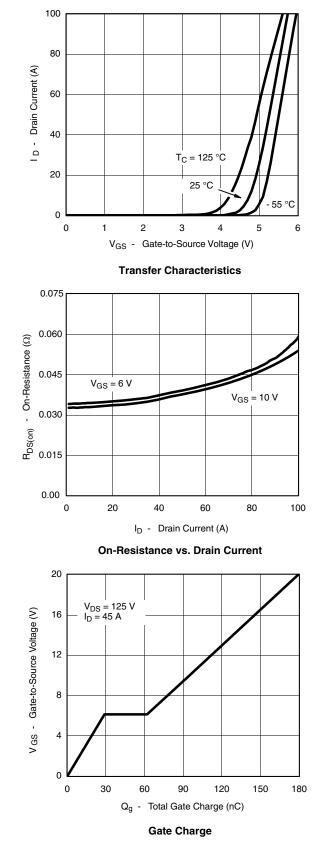




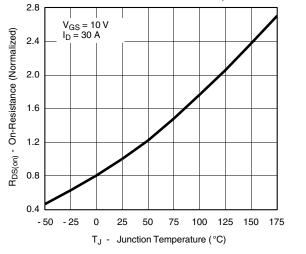
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





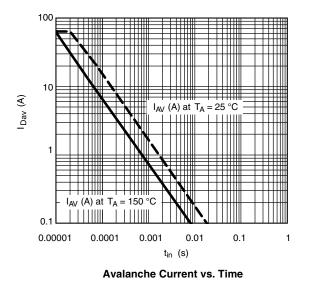


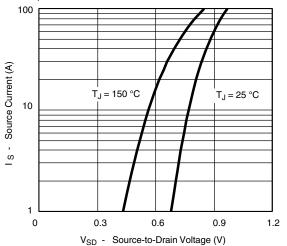




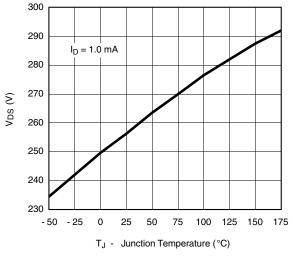
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**On-Resistance vs. Junction Temperature** 





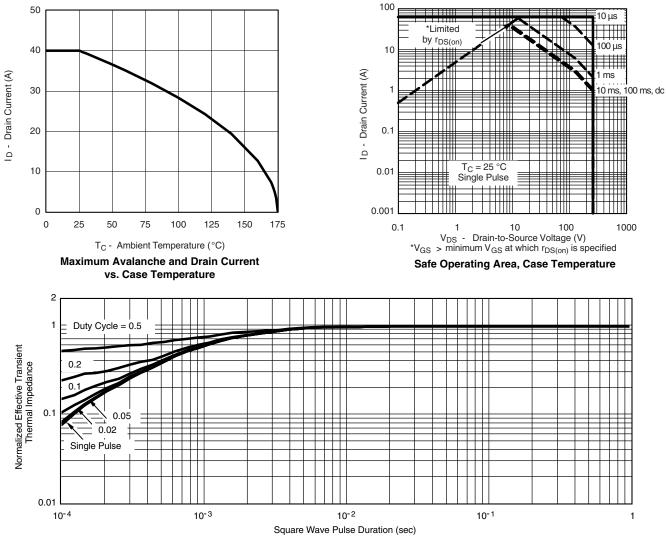
Source-Drain Diode Forward Voltage



Drain Source Breakdown vs. Junction Temperature

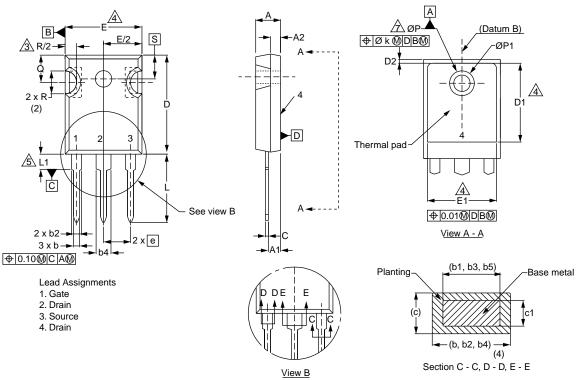


#### THERMAL RATINGS



Normalized Thermal Transient Impedance, Junction-to-Case





## TO-247AC (High Voltage)

DIM.	MILLIMETERS		INCHES			MILLIN	IETERS	INCHES	
	MIN.	MAX.	MIN.	MAX.	DIM.	MIN.	MAX.	MIN.	MAX
А	4.58	5.31	0.180	0.209	D2	0.51	1.30	0.020	0.05
A1	2.21	2.59	0.087	0.102	E	15.29	15.87	0.602	0.62
A2	1.17	2.49	0.046	0.098	E1	13.72	-	0.540	-
b	0.99	1.40	0.039	0.055	е	5.46 BSC		0.215 BSC	
b1	0.99	1.35	0.039	0.053	Øk	0.254		0.010	
b2	1.53	2.39	0.060	0.094	L	14.20	16.25	0.559	0.640
b3	1.65	2.37	0.065	0.093	L1	3.71	4.29	0.146	0.169
b4	2.42	3.43	0.095	0.135	N	7.62 BSC		0.300 BSC	
b5	2.59	3.38	0.102	0.133	ØP	3.51	3.66	0.138	0.144
С	0.38	0.86	0.015	0.034	Ø P1	-	7.39	-	0.29
c1	0.38	0.76	0.015	0.030	Q	5.31	5.69	0.209	0.224
D	19.71	20.82	0.776	0.820	R	4.52	5.49	0.178	0.216
D1	13.08	-	0.515	-	S	5.51 BSC		0.21	7 BSC



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