

TK70A06J1-VB Datasheet N-Channel 60 V (D-S) MOSFET

PRODUCT	SUMMARY	
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A) ^a
60	0.005 at V _{GS} = 10 V	120
00	0.013 at $V_{GS} = 4.5 \text{ V}$	95

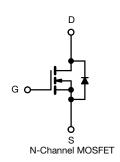
FEATURES

- 175 °C Junction Temperature
- Trench Power MOSFET
- Material categorization:



TO-220 FULLPAK





Parameter		Symbol	Limit	Unit
Gate-Source Voltage		V _{GS}	± 20	V
Continuous Brain Comment /T 475 90\b	T _C = 25 °C		120	
Continuous Drain Current (T _J = 175 °C) ^b	T _C = 100 °C	l _D	95 ^a	
Pulsed Drain Current	<u>.</u>	I _{DM}	300	А
Continuous Source Current (Diode Conduction)		I _S	70 ^a	
Avalanche Current		I _{AS}	50	
Single Avalanche Energy (Duty Cycle ≤ 1 %)	L = 0.1 mH	E _{AS}	125	mJ
Maritimus Britania Pitalia dia	T _C = 25 °C	P _D 136		W
Maximum Power Dissipation	T _A = 25 °C	'D	3 ^b , 8.3 ^{b, c}]
Operating Junction and Storage Temperature Range	•	T _J , T _{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^a	t ≤ 10 sec	R _{thJA}	15	18	°C/W		
Maximum Junction-to-Ambient	Steady State		40	50			
Maximum Junction-to-Case		R_{thJC}	0.85	1.1			

Notes:

- a. Package limited.
- b. Surface mounted on 1" x 1" FR4 board.
- $c.\ t \leq 10\ s.$

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SPECIFICATIONS ($T_J = 25$	°C, unless o	otherwise noted)					
Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit	
Static	•						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	60			\/	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1	2	3	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V _{DS} = 60 V, V _{GS} = 0 V			1		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V, T _J = 125 °C			50	μA	
		$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 ^{\circ}\text{C}$			250		
On-State Drain Current ^b	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	60			Α	
		V _{GS} = 10 V, I _D = 20 A		0.005			
Desir Occurs Oc Otata Basistanah	P	V _{GS} = 10 V, I _D = 20 A, T _J = 125 °C		0.010		V nA μA	
Drain-Source On-State Resistance ^b	R _{DS(on)}	V _{GS} = 10 V, I _D = 20 A, T _J = 175 °C		0.015			
		V _{GS} = 4.5 V, I _D = 15 A		0.013			
Forward Transconductance ^b	9 _{fs}	V _{DS} = 15 V, I _D = 20 A		60			
Dynamic							
Input Capacitance	C _{iss}			5650			
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		1120		pF	
Reverse Transfer Capacitance	C _{rss}			525			
Total Gate Charge ^c	Q_g			47	70		
Gate-Source Charge ^c	Q_{gs}	$V_{DS} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 50 \text{ A}$		10		nC	
Gate-Drain Charge ^c	Q_{gd}			12			
Turn-On Delay Time ^c	t _{d(on)}			10	20		
Rise Time ^c	t _r	$V_{DD} = 30 \text{ V, R}_{L} = 0.6 \Omega$		15	25	ns	
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong 50 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5 \Omega$		35	50		
Fall Time ^c	t _f			20	30		
Source-Drain Diode Ratings and Cha	aracteristics (T _C = 25 °C)					
Pulsed Current	I _{SM}			3 0 0		Α	
Diode Forward Voltage	V_{SD}	$I_F = 20 \text{ A}, V_{GS} = 0 \text{ V}$		1	1.5	V	
Reverse Recovery Time	t _{rr}	$I_F = 20 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}$		45	100	ns	

Notes:

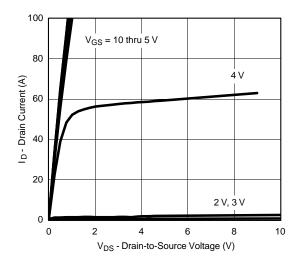
- a. For design aid only; not subject to production testing.
- b. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

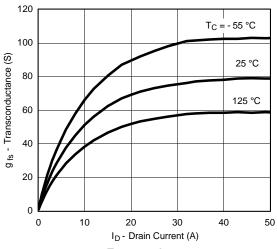
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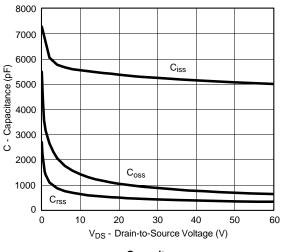
TYPICAL CHARACTERISTICS (25 °C unless noted)



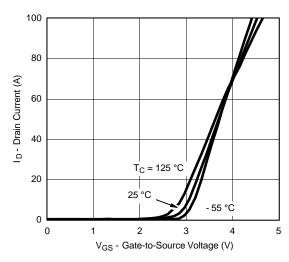
Output Characteristics



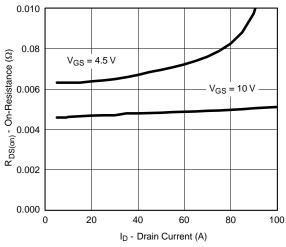
Transconductance



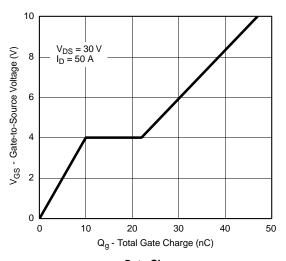
Capacitance



Transfer Characteristics



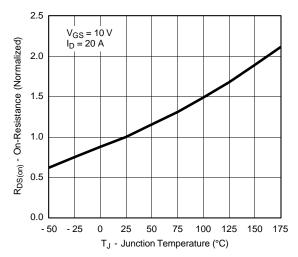
On-Resistance vs. Drain Current



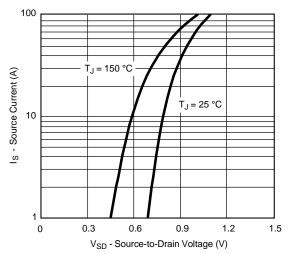
Gate Charge



TYPICAL CHARACTERISTICS (25 °C unless noted)



On-Resistance vs. Junction Temperature

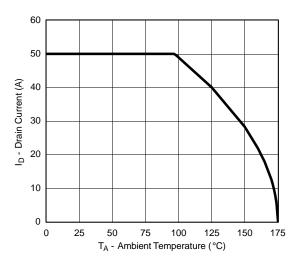


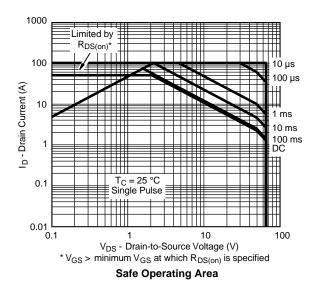
Source-Drain Diode Forward Voltage

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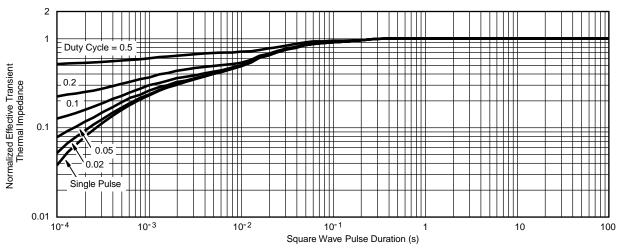


THERMAL RATINGS





Maximum Drain Current vs. Ambient Temperature



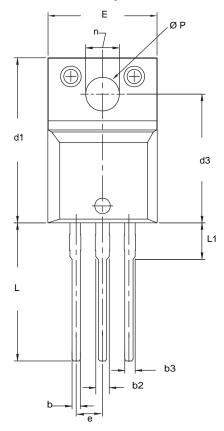
Normalized Thermal Transient Impedance, Junction-to-Case

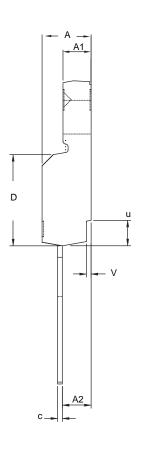
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TO-220 FULLPAK (HIGH VOLTAGE)





DIM.	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
А	4.570	4.830	0.180	0.190
A1	2.570	2.830	0.101	0.111
A2	2.510	2.850	0.099	0.112
b	0.622	0.890	0.024	0.035
b2	1.229	1.400	0.048	0.055
b3	1.229	1.400	0.048	0.055
С	0.440	0.629	0.017	0.025
D	8.650	9.800	0.341	0.386
d1	15.88	16.120	0.622	0.635
d3	12.300	12.920	0.484	0.509
E	10.360	10.630	0.408	0.419
е	2.54	BSC	0.100 BSC	
L	13.200	13.730	0.520	0.541
L1	3.100	3.500	0.122	0.138
n	6.050	6.150	0.238	0.242
ØΡ	3.050	3.450	0.120	0.136
u	2.400	2.500	0.094	0.098
V	0.400	0.500	0.016	0.020

ECN: X09-0126-Rev. B, 26-Oct-09 DWG: 5972

- To be used only for process drawing.
 These dimensions apply to all TO-220, FULLPAK leadframe versions 3 leads.
 All critical dimensions should C meet C_{pk} > 1.33.
 All dimensions include burrs and plating thickness.
 No chipping or package damage.

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