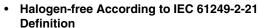


# FQPF17P10-VB Datasheet P-Channel 100 V (D-S) MOSFET

PRODUCT	SUMMARY		
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)
- 100	0.220 at $V_{GS} = -10 \text{ V}$	- 12	11.7
- 100	$0.230$ at $V_{GS} = -4.5 \text{ V}$	- 10	11.7

#### **FEATURES**

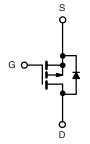




- Trench Power MOSFET
- 100 % R<sub>g</sub> and UIS Tested
- Compliant to RoHS Directive 2002/95/EC

## **APPLICATIONS**

- · Power Switch
- DC/DC Converters



P-Channel MOSFET

G D S
Top View

**TO-220 FULLPAK** 

ABSOLUTE MAXIMUM RATINGS	<b>S</b> $T_C = 25  ^{\circ}C$ , unless other	erwise noted			
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 100	V	
Gate-Source Voltage		V <sub>GS</sub>	± 20	V	
Continuous Drain Current (T <sub>.1</sub> = 150 °C)	T <sub>C</sub> = 25 °C	I-	- 12		
Continuous Diam Current (1) = 150 °C)	T <sub>C</sub> = 70 °C	- I'D	- 8.6		
Pulsed Drain Current	<u>.</u>	I <sub>DM</sub>	- 36	А	
Avalanche Current		I <sub>AS</sub>	- 18		
Single Avalanche Energy <sup>a</sup>	L = 0.1 mH	E <sub>AS</sub>	16.2	mJ	
M	T <sub>C</sub> = 25 °C	В	38.1 <sup>b</sup>	W	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C <sup>c</sup>	- P <sub>D</sub> -	2.5	VV	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Limit	Unit
Junction-to-Ambient (PCB Mount) <sup>c</sup>	R <sub>thJA</sub>	50	°C/W
Junction-to-Case (Drain)	R <sub>thJC</sub>	3.9	C/VV

#### Notes:

- a. Duty cycle  $\leq$  1 %.
- b. See SOA curve for voltage derating.
- c. When Mounted on 1" square PCB (FR-4 material).



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{DS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$	- 100			V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = -250 \mu A$	- 1		- 2.5	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 250	nA	
		V <sub>DS</sub> = - 100 V, V <sub>GS</sub> = 0 V			- 1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			- 50	μΑ	
	V   V   V   V   V   V   V   V   V   V	V <sub>DS</sub> = - 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 150 °C			- 250		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le -10 \text{ V}, V_{GS} = -10 \text{ V}$	- 15			Α	
	В	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 3.6 A		0.220			
Drain-Source On-State Resistance <sup>a</sup>	<sup>n</sup> DS(on)	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 3.4 A		0.230		Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 3.6 A		12		S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			1055		pF	
Output Capacitance	C <sub>oss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = - 50 V, f = 1 MHz		65			
Reverse Transfer Capacitance		1		41			
		V <sub>DS</sub> = - 50 V, V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 3.6 A		23.2	34.8		
Total Gate Charge <sup>c</sup>	Q <sub>g</sub>			11.7	17.6	~^	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = -50 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -3.6 \text{ A}$		3.5		nC	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			4.8			
Gate Resistance	$R_g$	f = 1 MHz	1.2	5.7	11.5	Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			7	14		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = -50 \text{ V}, R_L = 17.2 \Omega$		12	18		
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong$ - 2.9 A, $V_{GEN}$ = - 10 V, $R_g$ = 1 $\Omega$		33	50	ns	
Fall Time <sup>c</sup>	t <sub>f</sub>	]		9	18		
Drain-Source Body Diode Ratings ar	nd Characteri	stics T <sub>C</sub> = 25 °C <sup>b</sup>		•			
Continuous Current	I <sub>S</sub>				- 8.8		
Pulsed Current	I <sub>SM</sub>				- 15	Α	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = - 2.9 A, V <sub>GS</sub> = 0 V		- 0.8	- 1.5	V	
Reverse Recovery Time	t <sub>rr</sub>			50	75	ns	
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	I <sub>F</sub> = - 2.9 A, dl/dt = 100 A/μs		- 4	- 6	Α	
Reverse Recovery Charge	Q <sub>rr</sub>	·		98	147	nC	

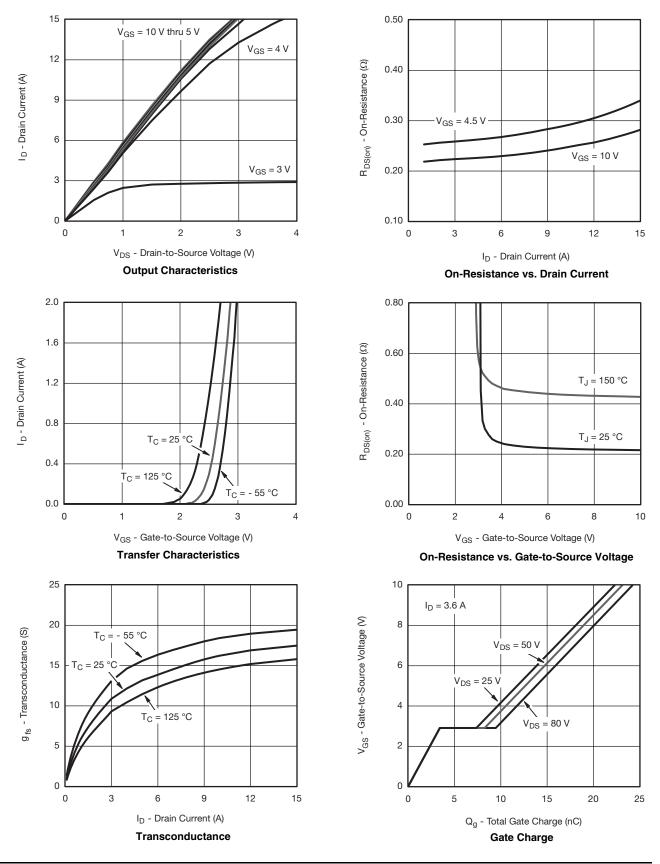
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

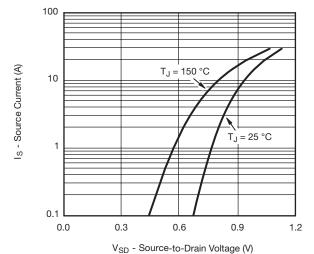


## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

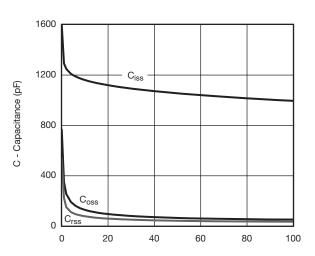




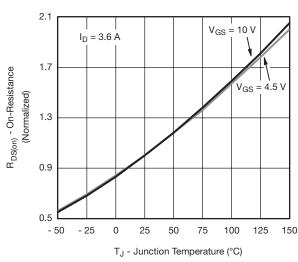
## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Source-Drain Diode Forward Voltage

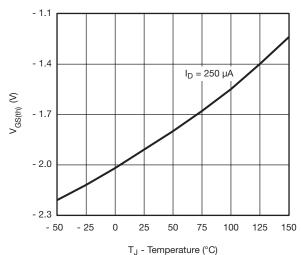


 $V_{DS}$  - Drain-to-Source Voltage (V)  $\label{eq:capacitance}$ 

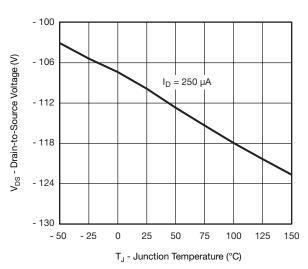


On-Resistance vs. Junction Temperature

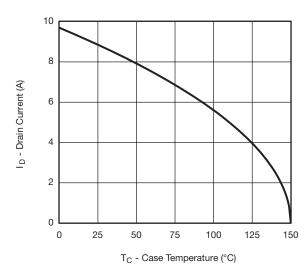
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Threshold Voltage



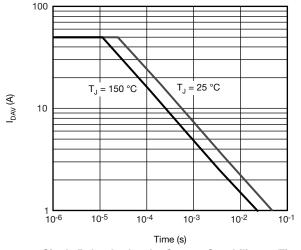
Drain Source Breakdown vs. Junction Temperature

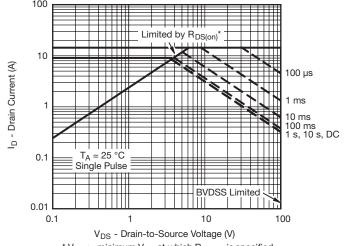


**Current Derating** 



## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

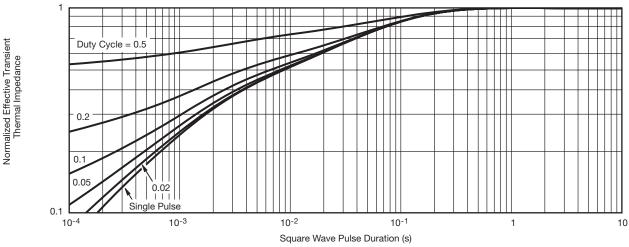




Single Pulse Avalanche Current Capability vs. Time

\*  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified





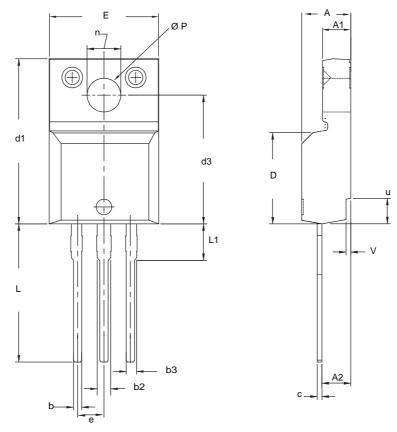
Normalized Thermal Transient Impedance, Junction-to-Case

服务热线:400-655-8788

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## **TO-220 FULLPAK**



DIM.	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
Α	4.570	4.830	0.180	0.190
A1	2.570	2.830	0.101	0.111
A2	2.510	2.850	0.099	0.112
b	0.622	0.890	0.024	0.035
b2	1.229	1.400	0.048	0.055
b3	1.229	1.400	0.048	0.055
С	0.440	0.629	0.017	0.025
D	8.650	9.800	0.341	0.386
d1	15.88	16.120	0.622	0.635
d3	12.300	12.920	0.484	0.509
E	10.360	10.630	0.408	0.419
е	2.54 BSC		0.100 BSC	
L	13.200	13.730	0.520	0.541
L1	3.100	3.500	0.122	0.138
n	6.050	6.150	0.238	0.242
ØΡ	3.050	3.450	0.120	0.136
u	2.400	2.500	0.094	0.098
V	0.400	0.500	0.016	0.020

## Notes

- To be used only for process drawing.
   These dimensions apply to all TO-220, FULLPAK leadframe versions 3 leads.
   All critical dimensions should C meet C<sub>pk</sub> > 1.33.
   All dimensions include burrs and plating thickness.
   No chipping or package damage.



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