

## FI4227-VB Datasheet N-Channel 200-V (D-S) MOSFET

PRODUCT SUMMARY			
V <sub>(BR)DSS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω)	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)
200	0.038 at V <sub>GS</sub> = 15 V	45	57
	0.043 at V <sub>GS</sub> = 10 V	40	57

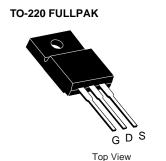
#### FEATURES

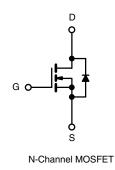
- Trench Power MOSFETS
- 175 °C Junction Temperature
- 100 %  $R_g$  and UIS Tested

### APPLICATIONS

- Power Supply
- Lighting Systems







<b>ABSOLUTE MAXIMUM RATINGS</b>	$T_A = 25 ^{\circ}C$ , unless oth	erwise noted			
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V <sub>DS</sub>	200	v		
Gate-Source Voltage		V <sub>GS</sub>	± 25	v	
Continuous Drain Current ( $T_1 = 175 ^{\circ}C$ )	T <sub>C</sub> = 25 °C	L	45		
Continuous Drain Current $(1) = 175$ C)	T <sub>C</sub> = 100 °C	I_D	26	_	
Pulsed Drain Current		I <sub>DM</sub>	150	— A	
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	20	1	
Single Pulse Avalanche Energy <sup>a</sup>	L = 0.1 1111	E <sub>AS</sub>	20	mJ	
Maximum Power Dissipation <sup>a</sup>	T <sub>C</sub> = 25 °C	Р	55 <sup>b</sup>		
	T <sub>A</sub> = 25 °C <sup>c</sup>	– P <sub>D</sub> –	3.12	W	
Operating Junction and Storage Temperature Ra	T <sub>J</sub> , T <sub>stq</sub>	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Limit	Unit	
Junction-to-Ambient (PCB Mount) <sup>c</sup>	R <sub>thJA</sub>	40	°C/W	
Junction-to-Case (Drain)	R <sub>thJC</sub>	0.75	- °C/W	

Notes:

a. Duty cycle  $\leq$  1 %.

b. See SOA curve for voltage derating.

c. When Mounted on 1" square PCB (FR-4 material).

<b>SPECIFICATIONS</b> (T <sub>J</sub> = $25^{\circ}$	C, unless o	otherwise noted)				
Parameter	Symbol	Test Conditions	Min.	Typ. <sup>a</sup>	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = 250 \mu A$	200			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	2		4	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 200 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	μΑ
		$V_{DS} = 200 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$			50	
		V <sub>DS</sub> = 200 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			250	
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = 5 V, V <sub>GS</sub> = 10 V	40			А
Drain-Source On-State Resistance <sup>b</sup>		$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 3 \text{ A}$		0.048		Ω
	Б	$V_{GS}$ = 10 V, I <sub>D</sub> = 3 A, T <sub>J</sub> = 125 °C		0.050		
	R <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 3 \text{ A}, \text{ T}_{J} = 175 \text{ °C}$		0.070		
		V <sub>GS</sub> = 6 V, I <sub>D</sub> = 3 A		0.092		
Forward Transconductanceb	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 3 A		35		S
Dynamic <sup>a</sup>						
Input Capacitance	C <sub>iss</sub>			2800		pF
Output Capacitance	C <sub>oss</sub>	$V_{GS}$ = 0 V, $V_{DS}$ = 25 V, F = 1 MHz		180		
Reverse Transfer Capacitance	C <sub>rss</sub>			80		
Total Gate Charge <sup>c</sup>	Qg			34	51	nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3 \text{ A}$		8		
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			12		
Gate Resistance	Rg		0.5		2.9	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			15	25	ns
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD}$ = 100 V, R <sub>L</sub> = 5.2 $\Omega$ I <sub>D</sub> $\cong$ 3 A, V <sub>GEN</sub> = 10 V, R <sub>g</sub> = 2.5 $\Omega$		50	75	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			30	45	
Fall Time <sup>c</sup>	t <sub>f</sub>			60	90	
Source-Drain Diode Ratings and Char	acteristics (	Γ <sub>C</sub> = 25 °C)				
Pulsed Current	I <sub>SM</sub>				30	А
Diode Forward Voltage <sup>b</sup>	V <sub>SD</sub>	I <sub>F</sub> = 3 A, V <sub>GS</sub> = 0 V		0.9	1.5	V
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 3 A, dl/dt = 100 A/μs		180	250	ns

Notes:

a. Guaranteed by design, not subject to production testing.

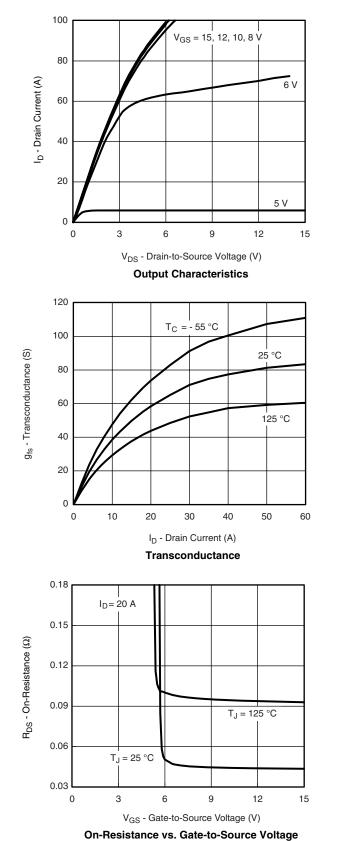
b. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

c. Independent of operating temperature.

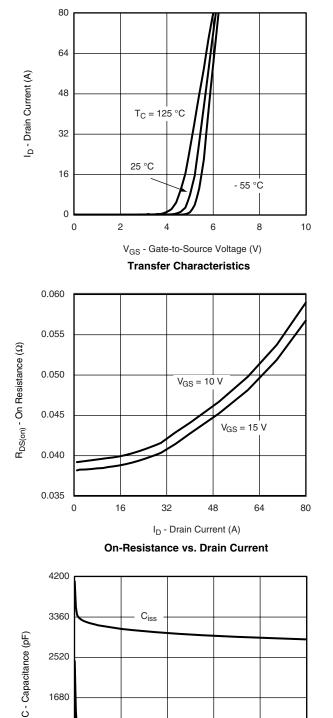
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

VBsemi





#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



840

0

0

Crss

20

40

C<sub>oss</sub>

80

60

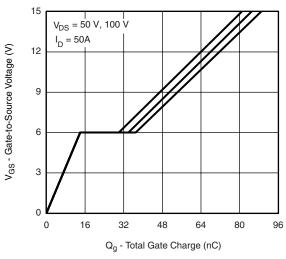
V<sub>DS</sub> - Drain-to-Source Voltage (V)

Capacitance

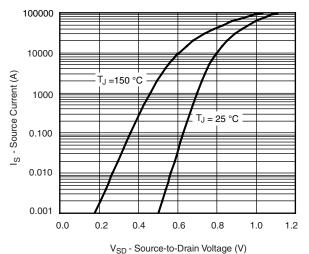
100

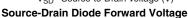


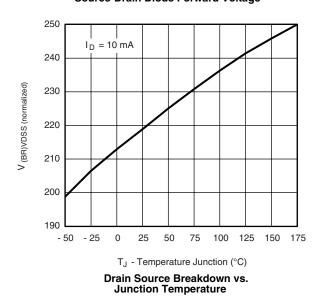


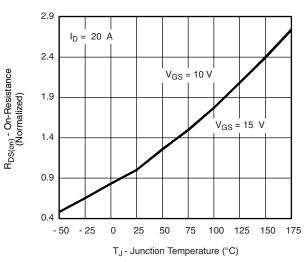




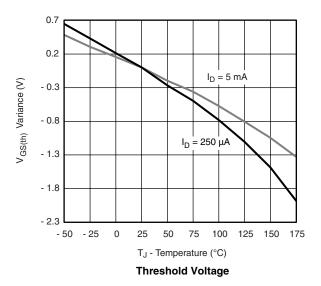


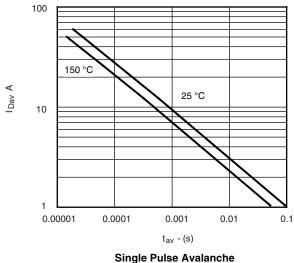


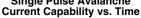




**On-Resistance vs. Junction Temperature** 

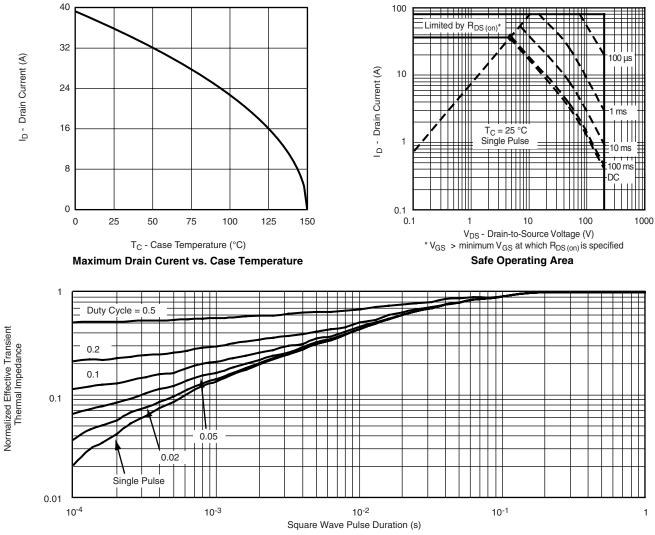








#### THERMAL RATINGS



Normalized Thermal Transient Impedance, Junction-to-Case



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