

AP4407F-VB Datasheet

P-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A) ^a
- 30	0.011 at $V_{GS} = - 10$ V	55
	0.013 at $V_{GS} = - 4.5$ V	50

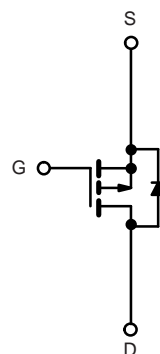
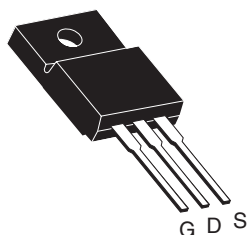
FEATURES

- Compliant to RoHS Directive 2002/95/EC



Available
RoHS*
COMPLIANT

TO-220 FULLPAK



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_C = 25\text{ }^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ($T_J = 175\text{ }^\circ\text{C}$)	I_D	$- 55^a$	A
		$- 45$	
Pulsed Drain Current	I_{DM}	$- 260$	
Avalanche Current	I_{AR}	$- 55$	
Repetitive Avalanche Energy ^b	E_{AR}	190	mJ
Power Dissipation	$T_C = 25\text{ }^\circ\text{C}$ (TO-220F)	45^d	W
	$T_A = 25\text{ }^\circ\text{C}$ (TO-220F) ^c	3.75	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	$- 55$ to 175	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Limit	Unit
Junction-to-Ambient	R_{thJA}	40	$^\circ\text{C/W}$
		62.5	
Junction-to-Case	R_{thJC}	0.8	

Notes:

a. Package limited.

b. Duty cycle $\leq 1\%$.

c. When mounted on 1" square PCB (FR-4 material).

d. See SOA curve for voltage derating.

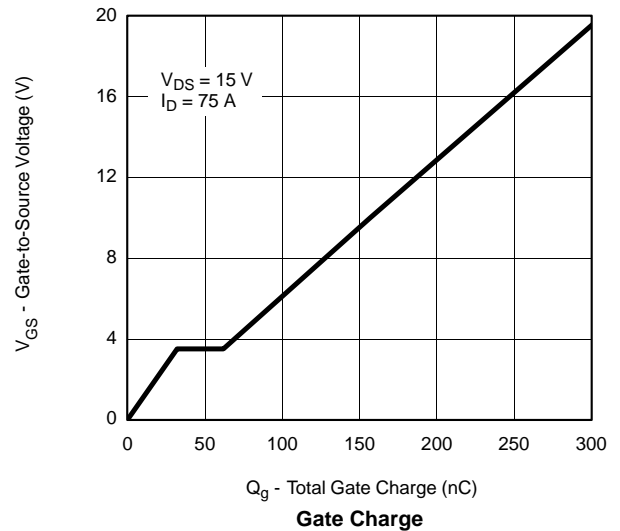
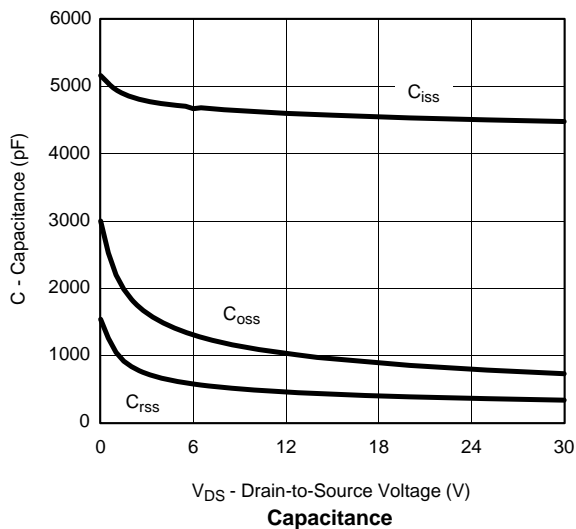
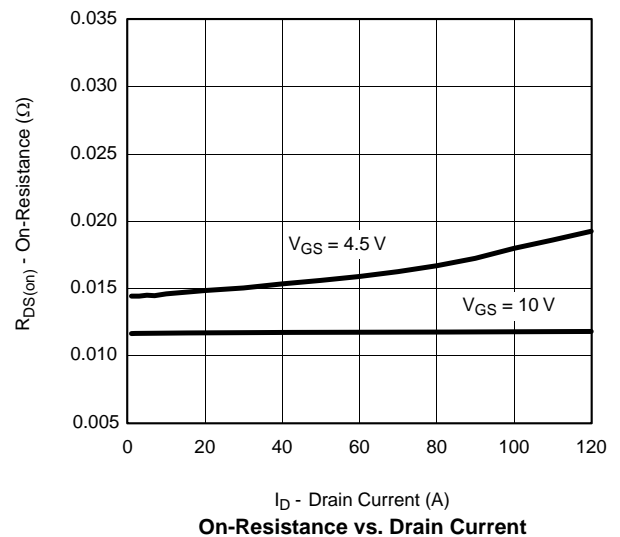
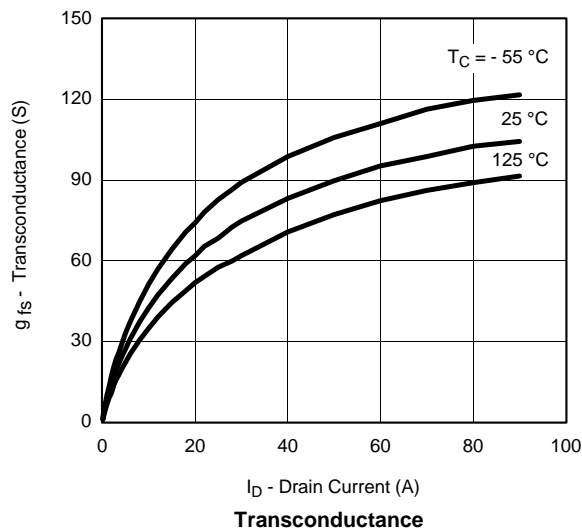
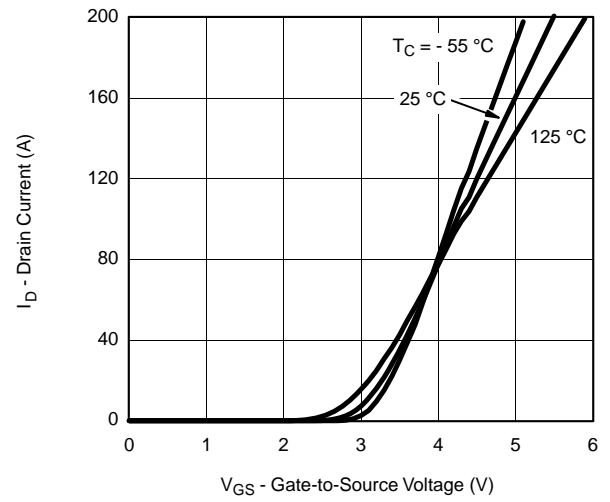
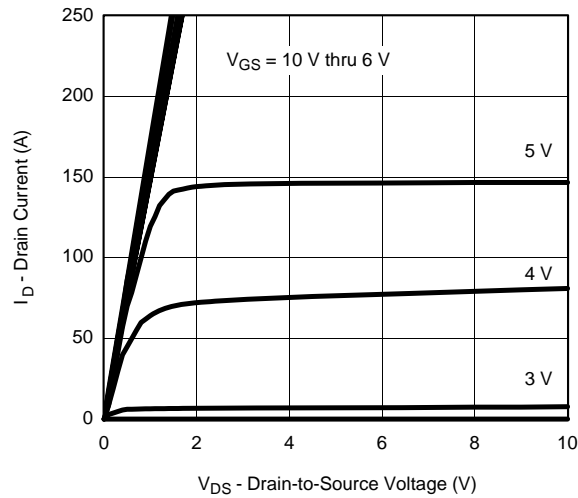
* Pb containing terminations are not RoHS compliant, exemptions may apply.

SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA	- 30			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = - 250 μA	- 1		- 3	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 30 V, V _{GS} = 0 V			- 1	μA
		V _{DS} = - 30 V, V _{GS} = 0 V, T _J = 125 °C			- 50	
		V _{DS} = - 30 V, V _{GS} = 0 V, T _J = 175 °C			- 250	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 10 V	- 120			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 30 A		0.011		Ω
		V _{GS} = - 10 V, I _D = - 30 A, T _J = 125 °C		0.015		
		V _{GS} = - 10 V, I _D = - 30 A, T _J = 175 °C		0.019		
		V _{GS} = - 4.5 V, I _D = - 20 A		0.013		
Forward Transconductance ^a	g _{fs}	V _{DS} = - 15 V, I _D = - 75 A	20			S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = - 25 V, f = 1 MHz		4500		pF
Output Capacitance	C _{oss}			765		
Reversen Transfer Capacitance	C _{rss}			315		
Total Gate Charge ^c	Q _g	V _{DS} = - 15 V, V _{GS} = - 10 V, I _D = - 75 A		80	120	nC
Gate-Source Charge ^c	Q _{gs}			20		
Gate-Drain Charge ^c	Q _{gd}			15		
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = - 15 V, R _L = 0.2 Ω I _D ≡ - 75 A, V _{GEN} = - 10 V, R _g = 2.5 Ω		25	40	ns
Rise Time ^c	t _r			225	360	
Turn-Off Delay Time ^c	t _{d(off)}			150	240	
Fall Time ^c	t _f			210	340	
Source-Drain Diode Ratings and Characteristics ^b (T _C = 25 °C)						
Continuous Current	I _S				- 80	A
Pulsed Current	I _{SM}				- 240	
Forward Voltage ^a	V _{SD}	I _F = - 75 A, V _{GS} = 0 V		- 1.2	- 1.5	V
Reverse Recovery Time	t _{rr}	I _F = - 75 A, dI/dt = 100 A/μs		55	100	ns
Peak Reverse Recovery Current	I _{RM(REC)}			2.5	5	A
Reverse Recovery Charge	Q _{rr}			0.07	0.25	μC

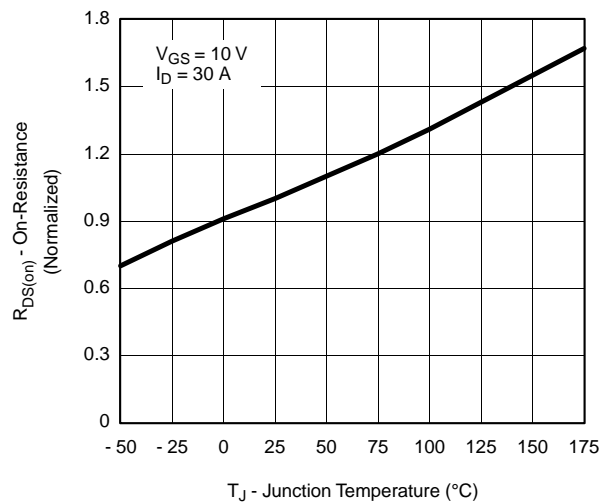
Notes:

- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
 b. Guaranteed by design, not subject to production testing.
 c. Independent of operating temperature.

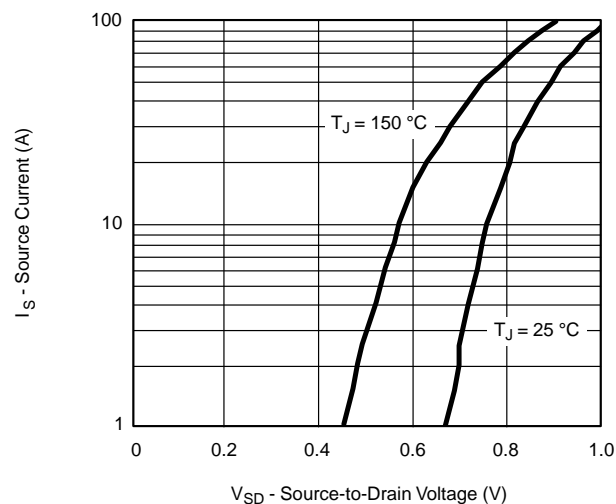
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)


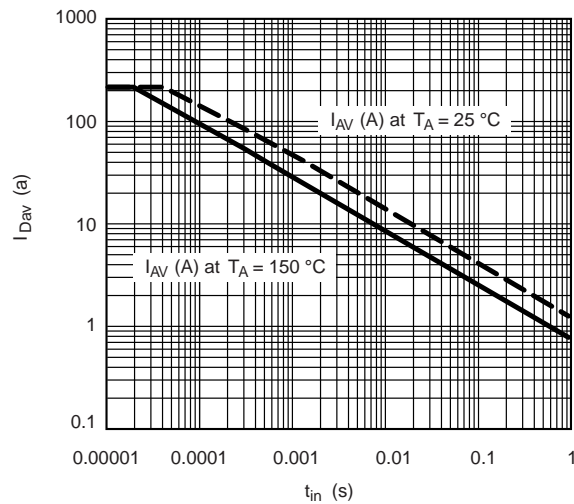
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



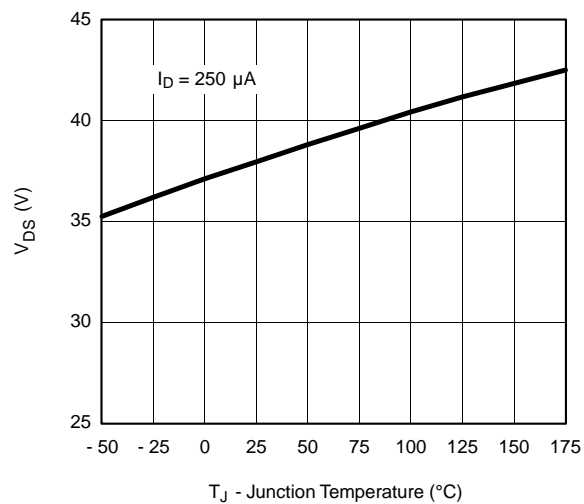
On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage

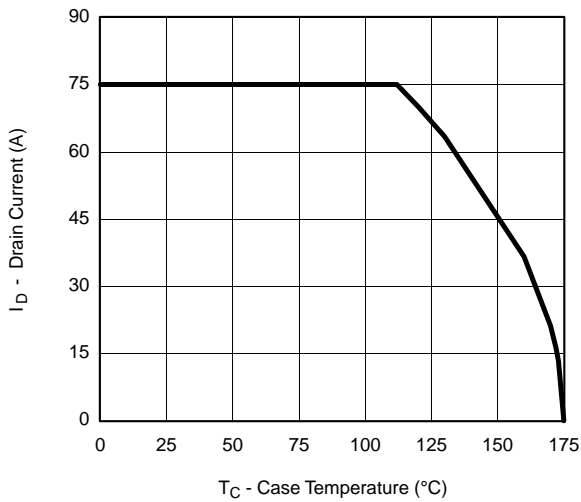


Avalanche Current vs. Time

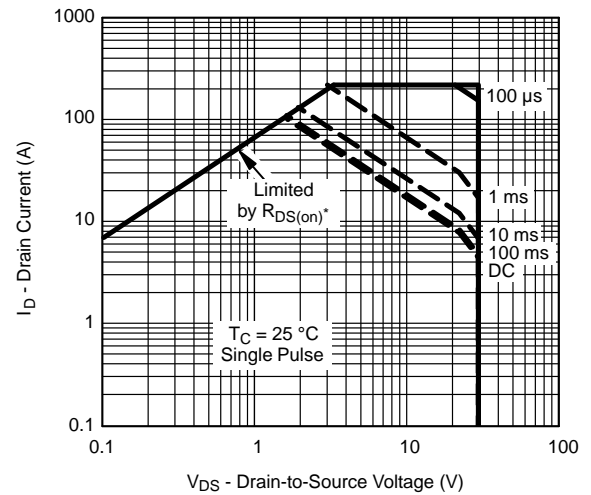


Drain Source Breakdown vs. Junction Temperature

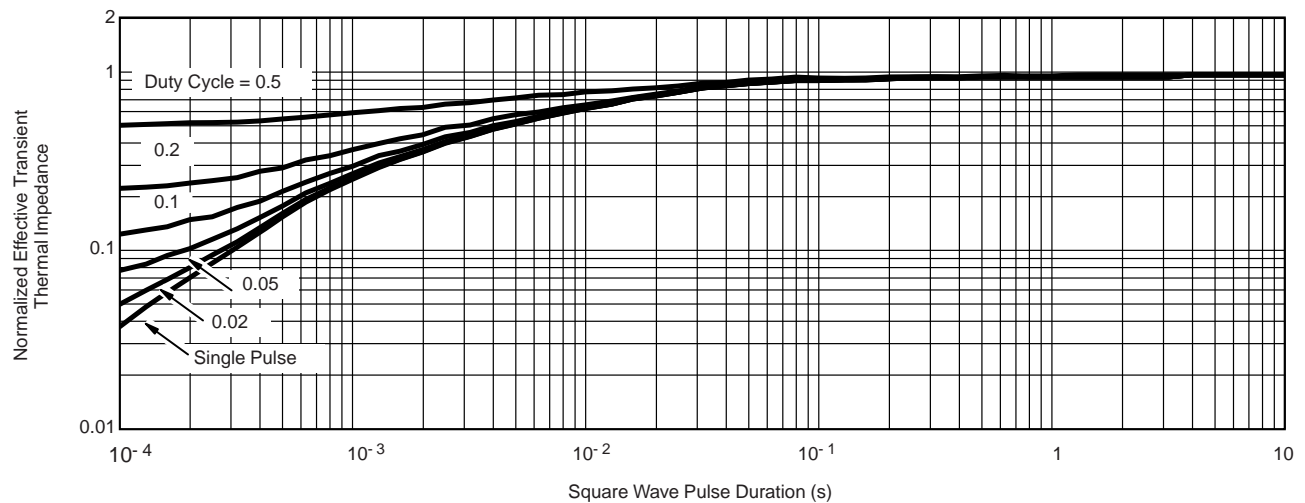
THERMAL RATINGS



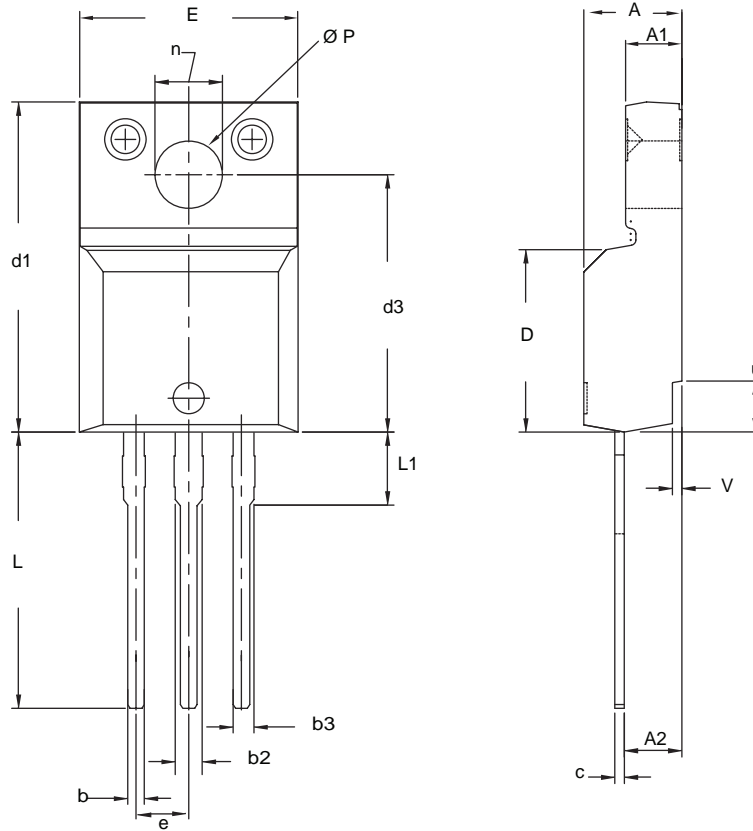
**Maximum Avalanche and Drain Current
vs. Case Temperature**



*** $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified
Safe Operating Area**



Normalized Thermal Transient Impedance, Junction-to-Case

TO-220 FULLPAK

DIM.	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	4.570	4.830	0.180	0.190
A1	2.570	2.830	0.101	0.111
A2	2.510	2.850	0.099	0.112
b	0.622	0.890	0.024	0.035
b2	1.229	1.400	0.048	0.055
b3	1.229	1.400	0.048	0.055
c	0.440	0.629	0.017	0.025
D	8.650	9.800	0.341	0.386
d1	15.88	16.120	0.622	0.635
d3	12.300	12.920	0.484	0.509
E	10.360	10.630	0.408	0.419
e	2.54 BSC		0.100 BSC	
L	13.200	13.730	0.520	0.541
L1	3.100	3.500	0.122	0.138
n	6.050	6.150	0.238	0.242
Ø P	3.050	3.450	0.120	0.136
u	2.400	2.500	0.094	0.098
v	0.400	0.500	0.016	0.020

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 DWG: 5972

Notes

1. To be used only for process drawing.
2. These dimensions apply to all TO-220, FULLPAK leadframe versions 3 leads.
3. All critical dimensions should C meet $C_{pk} > 1.33$.
4. All dimensions include burrs and plating thickness.
5. No chipping or package damage.

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