

40T03GI-VB Datasheet

N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A) ^{a, e}	Q _g (Typ)			
30	0.010 at $V_{GS} = 10 \text{ V}$	68	82 nC			
	0.012 at V _{GS} = 4.5 V	62				

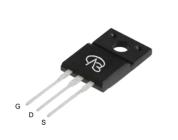
FEATURES

- Trench Power MOSFET
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2011/65/EU

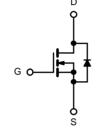


APPLICATIONS

- OR-ing
- Server
- DC/DC



TO-220F



Top View

N-Channel MOSFET

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	30		
Gate-Source Voltage		V _{GS}	± 20	
	T _C = 25 °C		68 ^{a, e}	
Continuous Drain Current (T. – 175 °C)	T _C = 70 °C		62 ^e	
Continuous Drain Current (T _J = 175 °C)	T _A = 25 °C	I _D	68.8 ^{b, c}	Α
	T _A = 70 °C		57 ^{b, c}	_ ^
Pulsed Drain Current	I _{DM}	90		
Avalanche Current Pulse	L = 0.1 mH	I _{AS}	36	
Single Pulse Avalanche Energy	L=0.11IIII	E _{AS}	64.8	V
Ocationary Course Busin Binds Course	T _C = 25 °C	I _S	90 ^{a, e}	А
Continuous Source-Drain Diode Current	T _A = 25 °C	'S	3.13 ^{b, c}	^
	T _C = 25 °C		250 ^a	
Mayimum Payar Dissination	T _C = 70 °C	P _D	175	W
Maximum Power Dissipation	T _A = 25 °C	-D	3.75 ^{b, c}	VV
	T _A = 70 °C		2.63 ^{b, c}	
Operating Junction and Storage Temperature R	ange	T _J , T _{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Тур.	Max.	Unit
Maximum Junction-to-Ambient ^{b, d}	$t \le 10 \text{ sec}$	R _{thJA}	32	40	°C/W
Maximum Junction-to-Case	Steady State	R _{thJC}	0.5	0.6	C/VV

Notes:

- a. Based on T_C = 25 °C.
 b. Surface mounted on 1" x 1" FR4 board.
- c. t = 10 sec.
 d. Maximum under steady state conditions is 90 °C/W.
- e. Calculated based on maximum junction temperature. Package limitation current is 90 A.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = 250 μA		35		mc\ //04	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_{J}$	I _D = 250 μA		- 7.5		mV/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.5		2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
7 0		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V, T _J = 55 °C			10	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	90			Α	
		$V_{GS} = 10 \text{ V}, I_D = 28.8 \text{ A}$		0.010		_	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 27 \text{ A}$		0.012		Ω	
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 28.8 A		160		S	
Dynamic ^b			'			•	
Input Capacitance	C _{iss}			1400			
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		1200		pF	
Reverse Transfer Capacitance	C _{rss}			970			
Total Gate Charge	0	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 28.8 \text{ A}$		171	257	257 123 nC	
	Q_g			81.5	123		
Gate-Source Charge	Q_{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 28.8 \text{ A}$		34			
Gate-Drain Charge	Q_{gd}			29			
Gate Resistance	R _g	f = 1 MHz		1.4	2.1	Ω	
Turn-On Delay Time	t _{d(on)}			18	27		
Rise Time	t _r	V_{DD} = 15 V, R_L = 0.625 Ω		11	17	- -	
Turn-Off Delay Time	t _{d(off)}	$I_D\cong 24$ A, $V_{GEN}=10$ V, $R_g=1$ Ω		70	105		
Fall Time	t _f			10	15	1	
Turn-On Delay Time	t _{d(on)}			55	83	ns ns	
Rise Time	t _r	V_{DD} = 15 V, R_L = 0.67 Ω		180	270		
Turn-Off Delay Time	t _{d(off)}	$I_D\cong 22.5$ A, V_{GEN} = 4.5 V, R_g = 1 Ω		55	83		
Fall Time	t _f			12	18		
Drain-Source Body Diode Characteristic	s		'				
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			90	۸	
Pulse Diode Forward Current ^a	I _{SM}				90	A	
Body Diode Voltage	V _{SD}	I _S = 22 A		0.8	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			52	78	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	L = 20 A di/dt = 100 A/vs T = 25 °C		70.2	105	nC	
Reverse Recovery Fall Time	t _a	$I_F = 20 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		27			
Reverse Recovery Rise Time	t _b	\dashv		25		ns	

Notes:

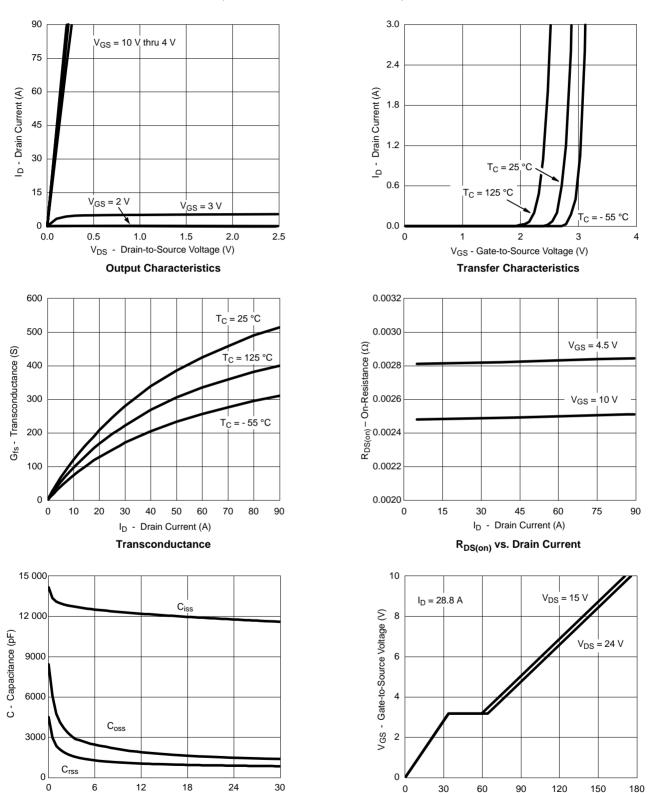
- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



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V_{DS} - Drain-to-Source Voltage (V)

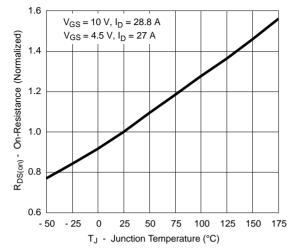
Capacitance

Q_g - Total Gate Charge (nC)

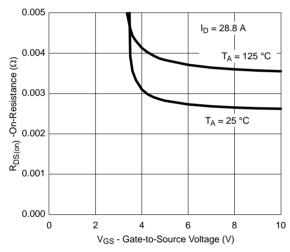
Gate Charge



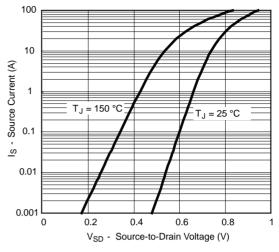
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



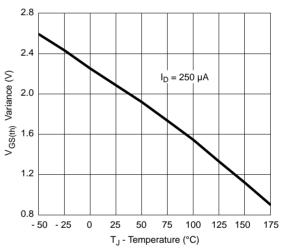
On-Resistance vs. Junction Temperature



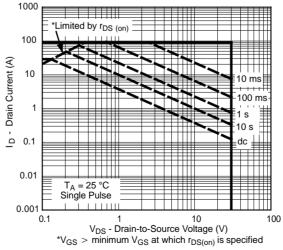
 $R_{DS(on)}$ vs. V_{GS} vs. Temperature



Forward Diode Voltage vs. Temperature



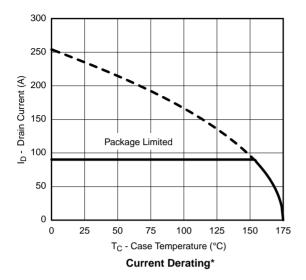
Threshold Voltage

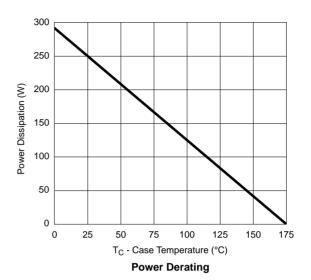


Safe Operating Area, Junction-to-Ambient

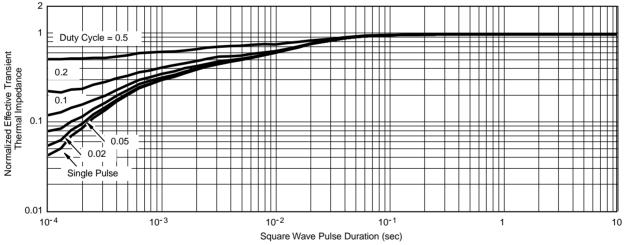


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





*The power dissipation P_D is based on $T_{J(max)}$ = 175 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

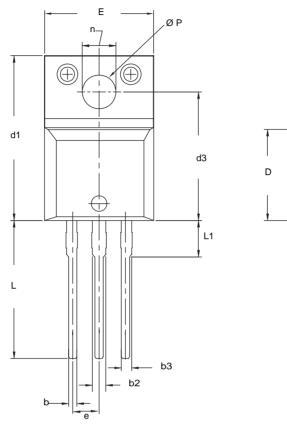


Normalized Thermal Transient Impedance, Junction-to-Case

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TO-220 FULLPAK (HIGH VOLTAGE)



	MILL	METERS	INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
Α	4.570	4.830	0.180	0.190
A1	2.570	2.830	0.101	0.111
A2	2.510	2.850	0.099	0.112
b	0.622	0.890	0.024	0.035
b2	1.229	1.400	0.048	0.055
b3	1.229	1.400	0.048	0.055
С	0.440	0.629	0.017	0.025
D	8.650	9.800	0.341	0.386
d1	15.88	16.120	0.622	0.635
d3	12.300	12.920	0.484	0.509
E	10.360	10.630	0.408	0.419
е	2.54 BSC		0.100 BSC	
L	13.200	13.730	0.520	0.541
L1	3.100	3.500	0.122	0.138
n	6.050	6.150	0.238	0.242
Ø P	3.050	3.450	0.120	0.136
u	2.400	2.500	0.094	0.098
V	0.400	0.500	0.016	0.020
ECN: X09-0126-Rev. B, 2 DWG: 5972	6-Oct-09			

- To be used only for process drawing.
 These dimensions apply to all TO-220, FULLPAK leadframe versions 3 leads.
 All critical dimensions should C meet C_{pk} > 1.33.
 All dimensions include burrs and plating thickness.
 No chipping or package damage.

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