

## UF9Z24-VB Datasheet

### P-Channel 60-V (D-S) MOSFET

#### PRODUCT SUMMARY

$V_{DS}$	-60	V
$R_{DS(on)}$ $V_{GS} = 10\text{ V}$	62	$m\Omega$
$R_{DS(on)}$ $V_{GS} = 4.5\text{ V}$	74	$m\Omega$
$I_D$	-40	A
Configuration	Single	

#### FEATURES

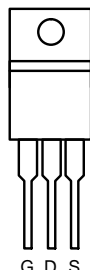
- Trench Power MOSFET
- 100 % UIS Tested

#### APPLICATIONS

- Load Switch


**RoHS**  
 COMPLIANT

TO-220AB



Top View



P-Channel MOSFET

#### ABSOLUTE MAXIMUM RATINGS $T_C = 25\text{ }^\circ\text{C}$ , unless otherwise noted

Parameter		Symbol	Limit	Unit
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Continuous Drain Current ( $T_J = 175\text{ }^\circ\text{C}$ )	$T_C = 25\text{ }^\circ\text{C}$	$I_D$	-40	A
	$T_C = 100\text{ }^\circ\text{C}$		-30	
Pulsed Drain Current		$I_{DM}$	- 90	
Continuing Source Current (Diode Conduction)		$I_S$	- 30	
Avalanche Current		$I_{AS}$	- 28	
Single Pulse Avalanche Energy	$L = 0.1\text{ mH}$	$E_{AS}$	7.2	mJ
Maximum Power Dissipation	$T_C = 25\text{ }^\circ\text{C}$	$P_D$	60 <sup>a</sup>	W
	$T_A = 25\text{ }^\circ\text{C}$		2 <sup>b</sup>	
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	- 55 to 175	$^\circ\text{C}$

#### THERMAL RESISTANCE RATINGS

Parameter		Symbol	Typical	Maximum	Unit
Junction-to-Ambient <sup>b</sup>	$t \leq 10\text{ sec}$	$R_{thJA}$	20	25	$^\circ\text{C/W}$
	Steady State		62	75	
Junction-to-Case		$R_{thJC}$	5	6	

Notes:

a. See SOA curve for voltage derating.

b. Surface Mounted on 1" x 1" FR-4 board.

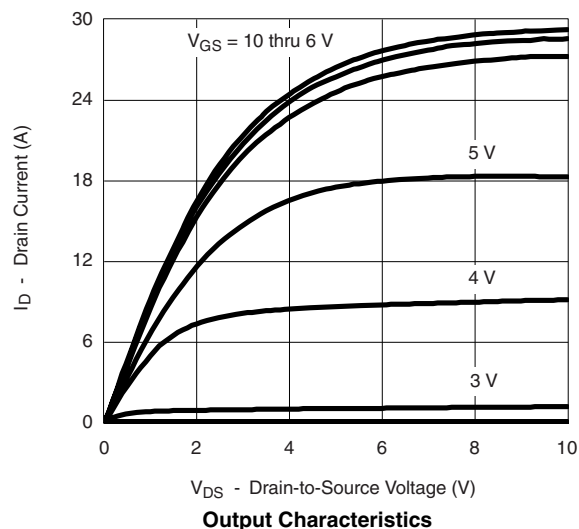
SPECIFICATIONS T <sub>J</sub> = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min	Typ <sup>a</sup>	Max	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = - 250 μA	- 60			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = - 250 μA	- 1.0		- 3.0	
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 60 V, V <sub>GS</sub> = 0 V			- 1	μA
		V <sub>DS</sub> = - 60 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			- 50	
		V <sub>DS</sub> = - 60 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			- 150	
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = - 5 V, V <sub>GS</sub> = - 10 V	- 10			A
Drain-Source On-State Resistance <sup>b</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 5 A		62		mΩ
		V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 5 A, T <sub>J</sub> = 125 °C		80		
		V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 5 A, T <sub>J</sub> = 175 °C		110		
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 2 A		74		
Forward Transconductance <sup>b</sup>	g <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 5 A		8		S
Dynamic						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = - 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz		1300		pF
Output Capacitance	C <sub>oss</sub>			120		
Reverse Transfer Capacitance	C <sub>rss</sub>			90		
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 8.4 A		13		nC
Gate-Source Charge	Q <sub>gs</sub>			2.3		
Gate-Drain Charge	Q <sub>gd</sub>			3.2		
Gate Resistance	R <sub>g</sub>	f = 1 MHz		8.0		Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>	V <sub>DD</sub> = - 30 V, R <sub>L</sub> = 3.57 Ω I <sub>D</sub> ≅ - 8.4 A, V <sub>GEN</sub> = - 10 V, R <sub>G</sub> = 2.5 Ω		5	10	ns
Rise Time <sup>c</sup>	t <sub>r</sub>			14	25	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			15	25	
Fall Time <sup>c</sup>	t <sub>f</sub>			7	12	
Source-Drain Diode Ratings and Characteristics (T <sub>C</sub> = 25 °C) <sup>b</sup>						
Pulsed Current	I <sub>SM</sub>			- 20		A
Forward Voltage <sup>b</sup>	V <sub>SD</sub>	I <sub>F</sub> = - 2 A, V <sub>GS</sub> = 0 V		- 0.9	- 1.3	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 8 A, di/dt = 100 A/μs		50	80	ns
Reverse Recovery Time	Q <sub>rr</sub>			80	120	nC

Notes:

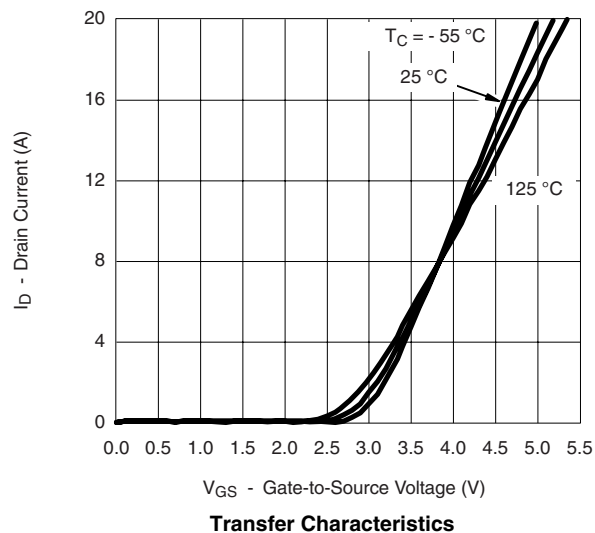
- a. Guaranteed by design, not subject to production testing.  
 b. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .  
 c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

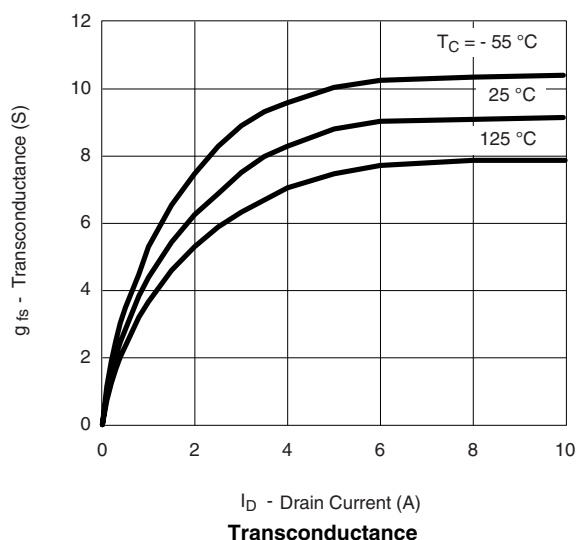
**TYPICAL CHARACTERISTICS** 25 °C unless noted



**Output Characteristics**



**Transfer Characteristics**



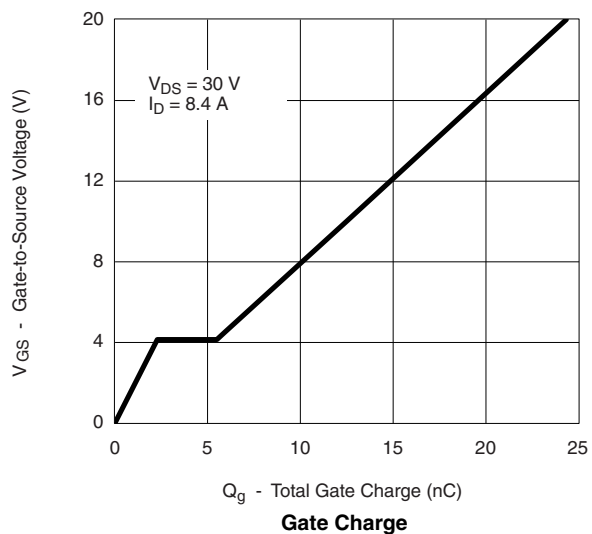
**Transconductance**



**On-Resistance vs. Drain Current**

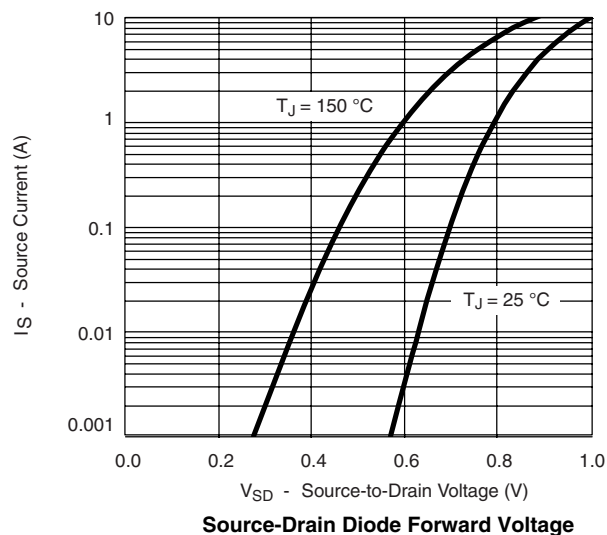
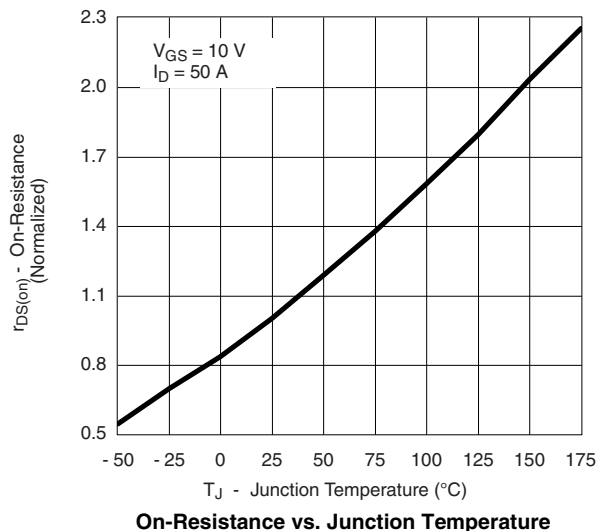


**Capacitance**

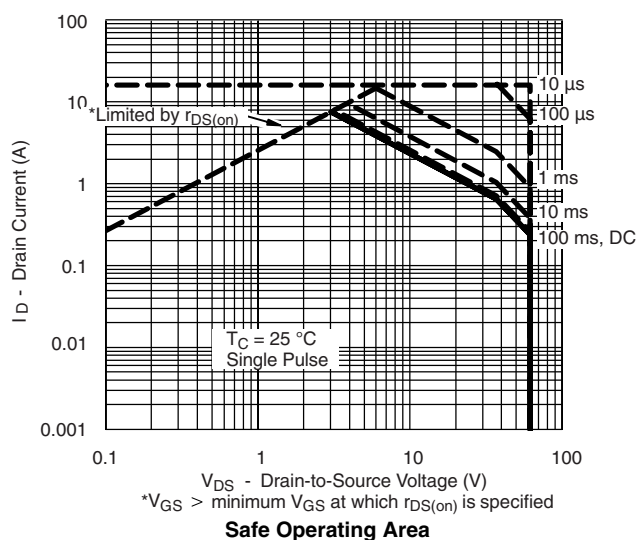
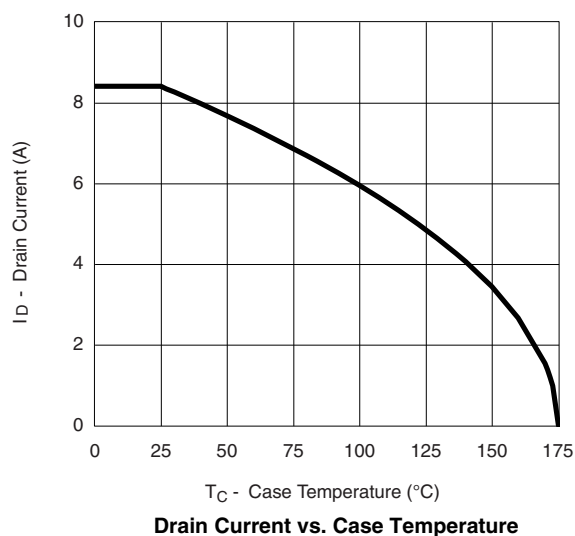


**Gate Charge**

# **TYPICAL CHARACTERISTICS** 25 °C unless noted



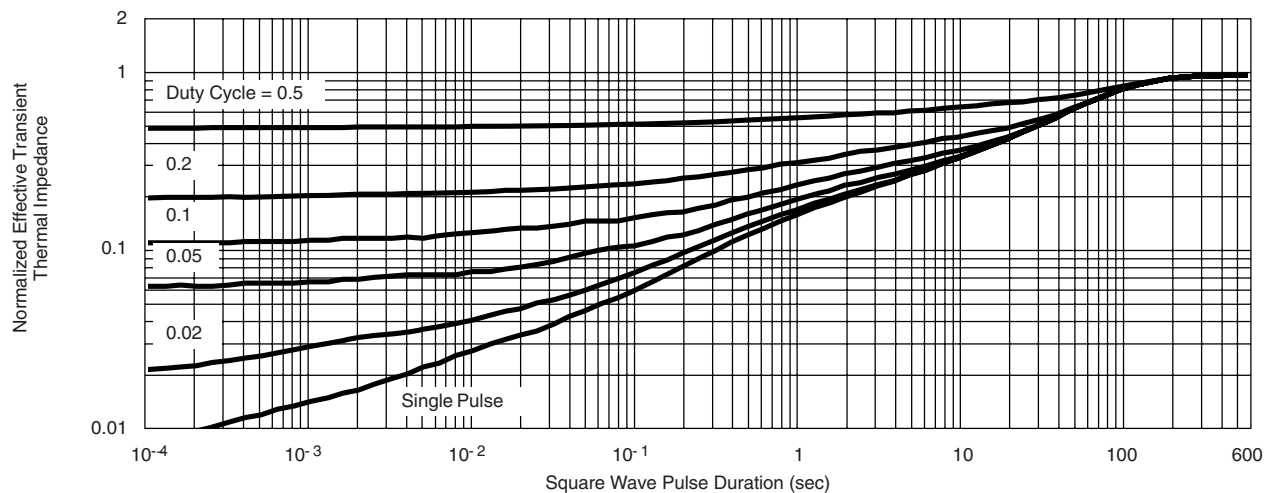
# **THERMAL RATINGS**



## THERMAL RATINGS

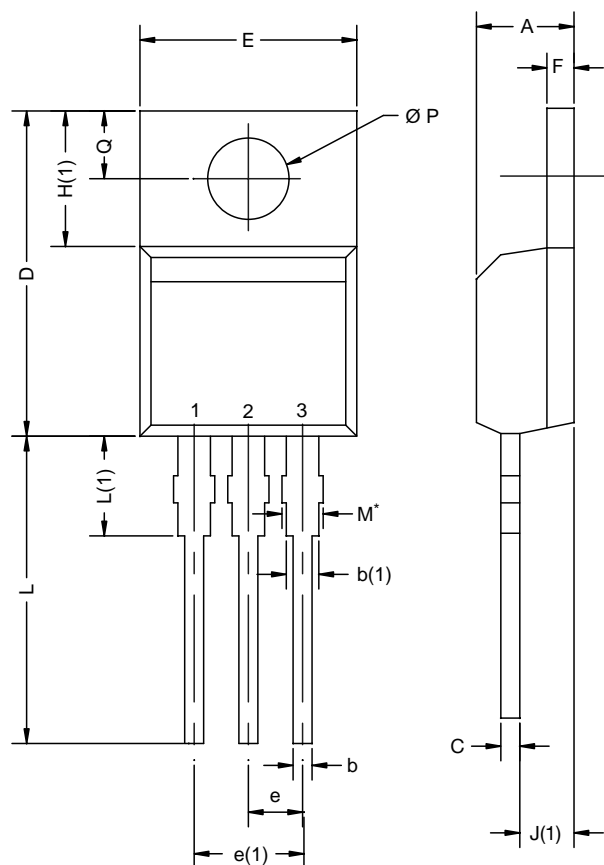


Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

## TO-220AB



DIM.	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	4.25	4.65	0.167	0.183
b	0.69	1.01	0.027	0.040
b(1)	1.20	1.73	0.047	0.068
c	0.36	0.61	0.014	0.024
D	14.85	15.49	0.585	0.610
E	10.04	10.51	0.395	0.414
e	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.09	6.48	0.240	0.255
J(1)	2.41	2.92	0.095	0.115
L	13.35	14.02	0.526	0.552
L(1)	3.32	3.82	0.131	0.150
Ø P	3.54	3.94	0.139	0.155
Q	2.60	3.00	0.102	0.118

ECN: X12-0208-Rev. N, 08-Oct-12  
DWG: 5471

### Notes

\* M = 1.32 mm to 1.62 mm (dimension including protrusion)  
Heatsink hole for HVM

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