

## SUP75P03-07-E3-VB Datasheet

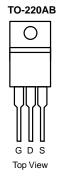
P-Channel 30 V (D-S) MOSFET

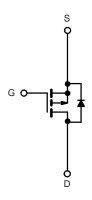
PRODUCT SUMMARY				
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a</sup>		
- 30	0.004 at $V_{GS}$ = - 10 V	-100		
- 30	0.005 at V <sub>GS</sub> = - 4.5 V	-90		

### FEATURES

Compliant to RoHS Directive 2002/95/EC







P-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_C = 25 \text{ °C}$ , unless otherwise noted)					
Parameter	Symbol Limit		Unit		
Gate-Source Voltage	V <sub>GS</sub>	± 20	V		
Continuous Drain Current (T $= 175$ °C)	T <sub>C</sub> = 25 °C	1	- 100		
Continuous Drain Current ( $T_J = 175 \ ^\circ C$ )	T <sub>C</sub> = 125 °C	I <sub>D</sub>	- 80	A	
Pulsed Drain Current	I <sub>DM</sub>	- 300	A		
Avalanche Current	I <sub>AR</sub>	- 80	1		
Repetitive Avalanche Energy <sup>b</sup>	L = 0.1 mH	E <sub>AR</sub>	180	mJ	
Power Dissinction	T <sub>C</sub> = 25 °C (TO-220AB and TO-263)	D	187 <sup>d</sup>	W	
Power Dissipation	T <sub>A</sub> = 25 °C (TO-263) <sup>c</sup>	P <sub>D</sub>	3.75	VV	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Limit	Unit	
Junction-to-Ambient	PCB Mount (TO-263) <sup>c</sup>	Р	40		
Junction-to-Ambient	Free Air (TO-220AB)	– R <sub>thJA</sub>	62.5	°C/W	
Junction-to-Case		R <sub>thJC</sub>	0.8		

Notes:

a. Package limited.

b. Duty cycle  $\leq$  1 %.

c. When mounted on 1" square PCB (FR-4 material).

d. See SOA curve for voltage derating.

\* Pb containing terminations are not RoHS compliant, exemptions may apply.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static		· · · · · ·					
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = -250 \mu A$	- 30		N/		
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1		- 3	- V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
		$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ = - 30 V, $V_{GS}$ = 0 V, $T_{J}$ = 125 °C			- 50	μA	
		$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 \text{ °C}$			- 250		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = -5 V, V_{GS} = -10 V$	- 120			А	
		V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 30 A		0.004			
Drain-Source On-State Resistance <sup>a</sup>	Р	$V_{GS}$ = - 10 V, I <sub>D</sub> = - 30 A, T <sub>J</sub> = 125 °C		0.006		0	
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	$V_{GS}$ = - 10 V, I <sub>D</sub> = - 30 A, T <sub>J</sub> = 175 °C		0.008		Ω	
		$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -20 \text{ A}$		0.005			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 75 A	20			S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			8000		pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS}$ = 0 V, $V_{DS}$ = - 25 V, f = 1 MHz		1565			
Reversen Transfer Capacitance	C <sub>rss</sub>			715			
Total Gate Charge <sup>c</sup>	Qg			160	240	nC	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS}$ = - 15 V, $V_{GS}$ = - 10 V, $I_{D}$ = - 75 A		32			
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			30		1	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			25	40		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = -15 \text{ V}, \text{ R}_{L} = 0.2 \Omega$		225	360	ns	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong$ - 75 Å, $V_{GEN}$ = - 10 V, $R_g$ = 2.5 $\Omega$		150	240		
Fall Time <sup>c</sup>	t <sub>f</sub>			210	340		
Source-Drain Diode Ratings and Cha	racteristics <sup>b</sup>	(T <sub>C</sub> = 25 °C)					
Continuous Current	ا <sub>S</sub>				- 80	^	
Pulsed Current	I <sub>SM</sub>				- 240	A	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = - 75 A, V <sub>GS</sub> = 0 V - 1.2		- 1.2	- 1.5	V	
Reverse Recovery Time	t <sub>rr</sub>			55	100	ns	
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	I <sub>F</sub> = - 75 A, dl/dt = 100 A/μs		2.5	5	Α	
Reverse Recovery Charge	Q <sub>rr</sub>	1		0.07	0.25	μC	

Notes:

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

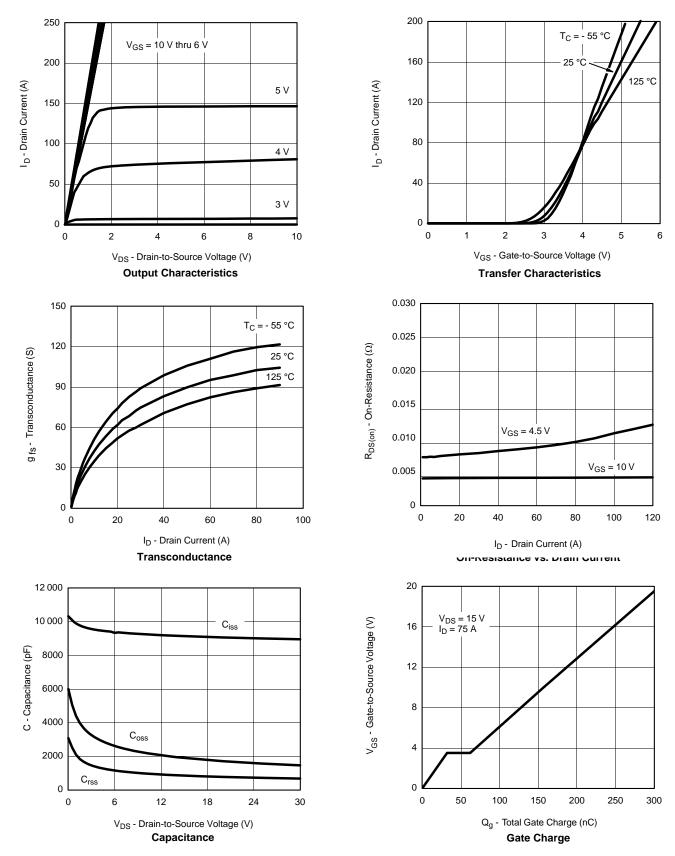
b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

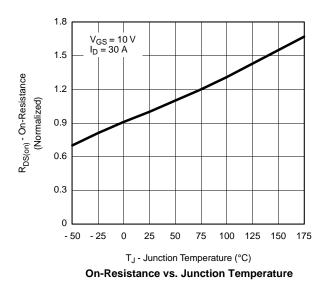


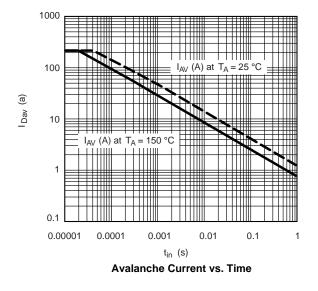


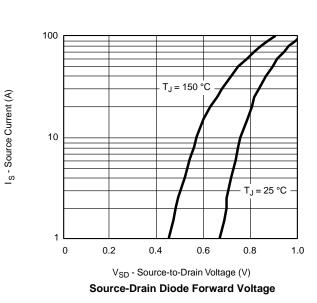


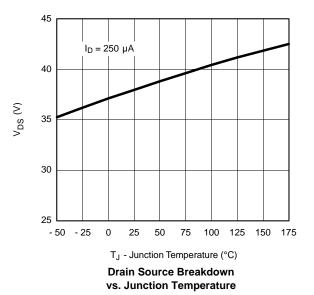


## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





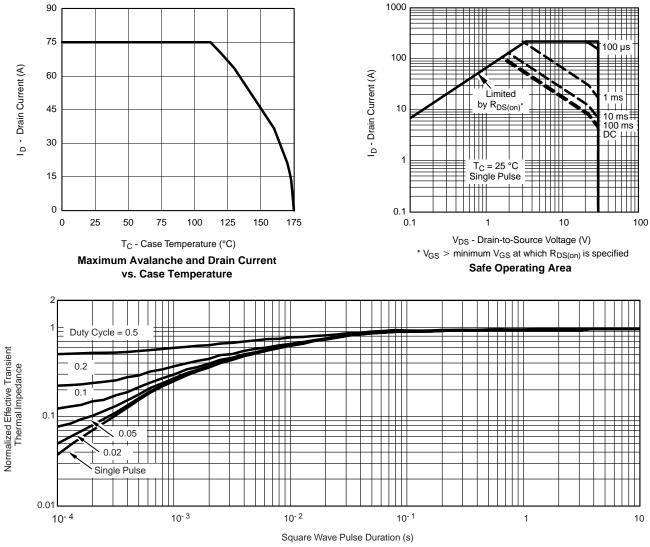




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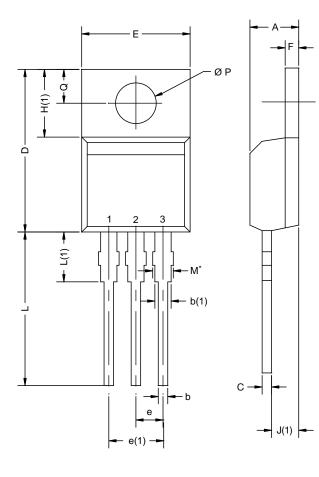
#### **THERMAL RATINGS**



Normalized Thermal Transient Impedance, Junction-to-Case



## **TO-220AB**



	MILLIMETERS		INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
А	4.25	4.65	0.167	0.183
b	0.69	1.01	0.027	0.040
b(1)	1.20	1.73	0.047	0.068
С	0.36	0.61	0.014	0.024
D	14.85	15.49	0.585	0.610
Е	10.04	10.51	0.395	0.414
е	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.09	6.48	0.240	0.255
J(1)	2.41	2.92	0.095	0.115
L	13.35	14.02	0.526	0.552
L(1)	3.32	3.82	0.131	0.150
ØΡ	3.54	3.94	0.139	0.155
Q	2.60	3.00	0.102	0.118
ECN: X12-0 DWG: 5471	0208-Rev. N,	08-Oct-12		

#### Notes

 $^{\star}$  M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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