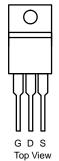


STP434S-VB Datasheet N-Channel 40-V (D-S) MOSFET

PRODUCT SUMMARY		
V _{DS}	40	V
R _{DS(on)} V _{GS} = 10 V	6	mΩ
I _D	110	А
Configuration	Sin	gle

TO-220AB

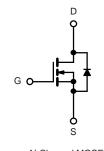


FEATURES

- Trench Power MOSFET
- 100 % R_g and UIS Tested

APPLICATIONS

- Synchronous Rectification
- Power Supplies



N-Channel MOSFET

ABSOLUTE MAXIMUM RATING	$5 1_{A} = 25 \mathbf{C}, \text{ unless}$		ju	r
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V _{DS}	40	V
Gate-Source Voltage		V _{GS}	± 20	v
	T _C = 25 °C		110 ^{a, c}	
Continuous Drain Current (T $= 175 ^{\circ}\text{C}$)	T _C = 70 °C		90 ^c	
Continuous Drain Current ($T_J = 175 \text{ °C}$)	T _A = 25 °C	I _D	31 ^b	Α
	T _A = 70 °C		25 ^b	
Pulsed Drain Current		I _{DM}	270	
Avalanche Current Pulse	L = 0.1 mH	I _{AS}	85	
Single Pulse Avalanche Energy		E _{AS}	320	V
Continuous Source Drain Diade Current	T _C = 25 °C	I _S	110 ^{a, c}	Α
Continuous Source-Drain Diode Current	T _A = 25 °C	is	2.6 ^b	A
	T _C = 25 °C		312 ^a	
Maximum Dawar Dissinction	T _C = 70 °C	PD	200	w
Maximum Power Dissipation	T _A = 25 °C	1 D	3.13 ^b	V
	T _A = 70 °C	= 70 °C 2.0 ^b	2.0 ^b	
Operating Junction and Storage Temperature R	ange	T _J , T _{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS	5					
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^b	Steady State	R _{thJA}	32	40	°C/W	
Maximum Junction-to-Case	Steady State	R _{thJC}	0.33	0.4	C/W	

Notes:

a. Based on $T_C = 25$ °C.

b. Surface Mounted on 1" x 1" FR4 board.

c. Calculated based on maximum junction temperature. Package limitation current is 110 A.

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static	1 - 1					
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = 250 \mu A$	40			V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	L = 250 uA		41		
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 8		mV/°C
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1.2		2.5	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zene Ceta Maltana Drain Correct		$V_{DS} = 40 \text{ V}, V_{GS} = 0 \text{ V}$			1	
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 40 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			10	μΑ
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5$ V, V_{GS} = 10 V	120			Α
Drain-Source On-State Resistance ^a	Б	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 30 \text{ A}$		6		
	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 20 \text{ A}$		7		1 mΩ
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 30 A		180		S
Dynamic ^b						
Input Capacitance	C _{iss}			2900		mΩ
Output Capacitance	C _{oss}	V_{DS} = 20 V, V_{GS} = 0 V, f = 1 MHz		750		
Reverse Transfer Capacitance	C _{rss}			310		
Total Gate Charge	Qg			130		
Gate-Source Charge	Q _{gs}	$V_{DS} = 20$ V, $V_{GS} = 10$ V, $I_{D} = 20$ A		20		nC
Gate-Drain Charge	Q _{gd}			32		
Gate Resistance	Rg	f = 1 MHz		0.85	1.3	Ω
Turn-On Delay Time	t _{d(on)}			20	30	
Rise Time	t _r	$\label{eq:VDD} \begin{array}{l} V_{DD} = 20 \ V, \ R_{L} = 1.0 \ \Omega \\ I_{D} \cong 20 \ A, \ V_{GEN} = 10 \ V, \ R_{g} = 1 \ \Omega \end{array}$		11	17	-
Turn-Off Delay Time	t _{d(off)}			77	115	
Fall Time	t _f			10	15	
Turn-On Delay Time	t _{d(on)}			102	155	- ns
Rise Time	t _r	V_{DD} = 20 V, R_{L} = 1.0 Ω		62	95	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 20$ A, V_{GEN} = 4.5 V, R_g = 1 Ω		180	270	
Fall Time	t _f			60	90	
Drain-Source Body Diode Characteristic	s			•		•
Continuous Source-Drain Diode Current	ا _S	$T_{C} = 25 \ ^{\circ}C$			110	^
Pulse Diode Forward Current ^a	I _{SM}				200	
Body Diode Voltage	V _{SD}	I _S = 20 A		0.8	1.2	V
Body Diode Reverse Recovery Time	t _{rr}			50	75	ns
Body Diode Reverse Recovery Charge	Q _{rr}	L = 20.4 di/dt = 100.4 fm = 100.2 fm		70	105	nC
Reverse Recovery Fall Time	t _a	I _F = 20 A, di/dt = 100 A/µs, T _J = 25 °C		30		
Reverse Recovery Rise Time	t _b			20		ns

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

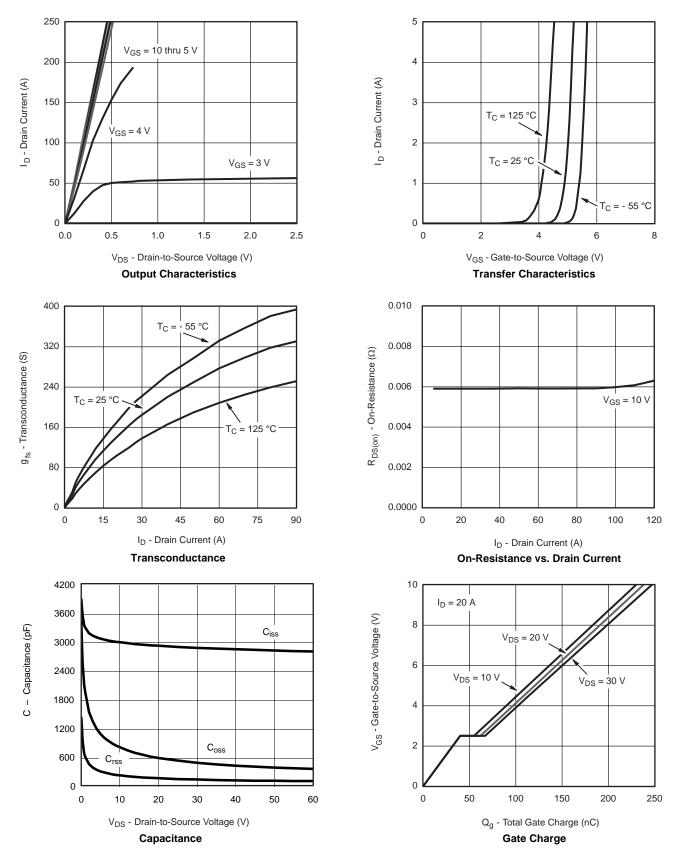
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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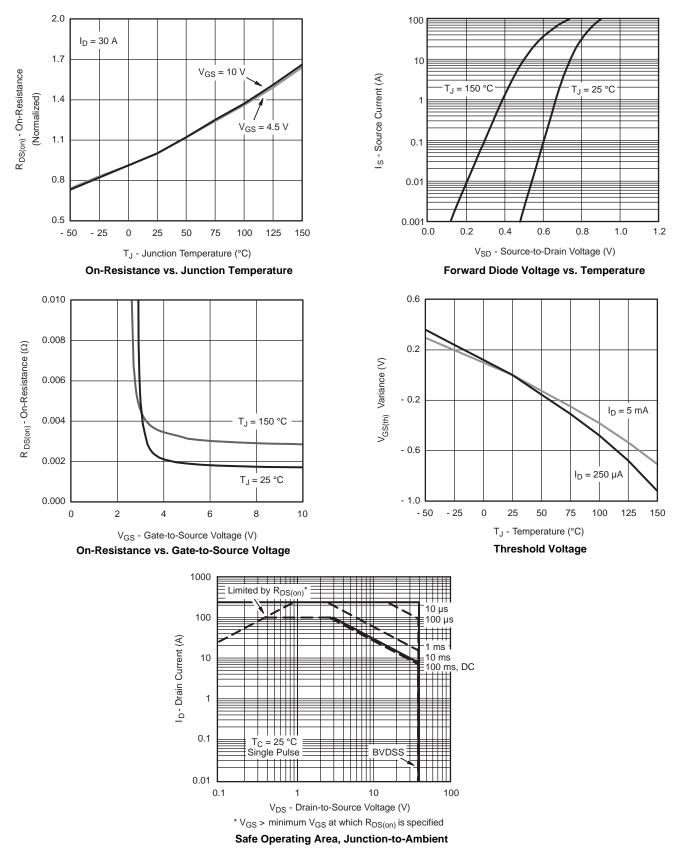
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

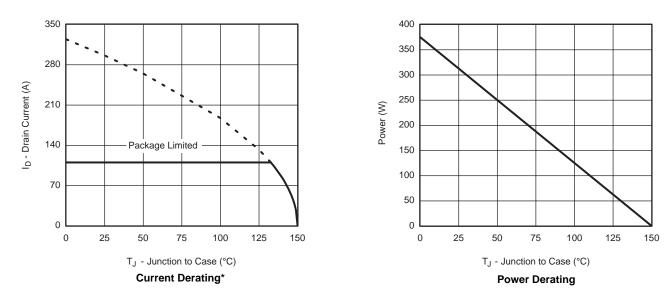


服务热线:400-655-8788



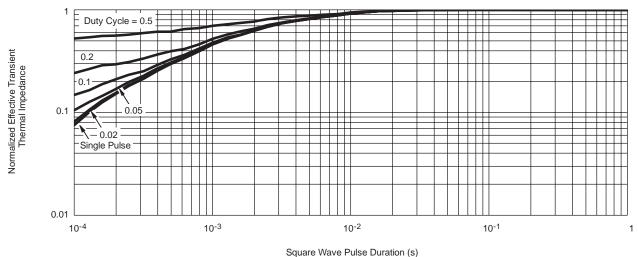
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

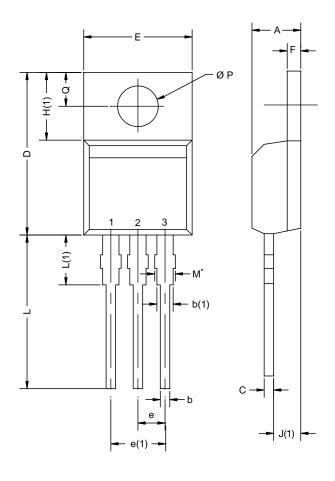


Normalized Thermal Transient Impedance, Junction-to-Case

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TO-220AB



MIN.	MAX.		
		MIN.	MAX.
4.25	4.65	0.167	0.183
0.69	1.01	0.027	0.040
1.20	1.73	0.047	0.068
0.36	0.61	0.014	0.024
14.85	15.49	0.585	0.610
10.04	10.51	0.395	0.414
2.41	2.67	0.095	0.105
4.88	5.28	0.192	0.208
1.14	1.40	0.045	0.055
6.09	6.48	0.240	0.255
2.41	2.92	0.095	0.115
13.35	14.02	0.526	0.552
3.32	3.82	0.131	0.150
3.54	3.94	0.139	0.155
2.60	3.00	0.102	0.118
	1.20 0.36 14.85 10.04 2.41 4.88 1.14 6.09 2.41 13.35 3.32 3.54 2.60	1.20 1.73 0.36 0.61 14.85 15.49 10.04 10.51 2.41 2.67 4.88 5.28 1.14 1.40 6.09 6.48 2.41 2.92 13.35 14.02 3.32 3.82 3.54 3.94	1.20 1.73 0.047 0.36 0.61 0.014 14.85 15.49 0.585 10.04 10.51 0.395 2.41 2.67 0.095 4.88 5.28 0.192 1.14 1.40 0.045 6.09 6.48 0.240 2.41 2.92 0.095 13.35 14.02 0.526 3.32 3.82 0.131 3.54 3.94 0.139 2.60 3.00 0.102

Notes

* M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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