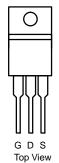


## PSMN4R5-40PS-VB Datasheet N-Channel 40-V (D-S) MOSFET

PRODUCT SUMMARY		
V <sub>DS</sub>	40	V
R <sub>DS(on)</sub> V <sub>GS</sub> = 10 V	6	mΩ
I <sub>D</sub>	110	А
Configuration	Sing	gle

### TO-220AB

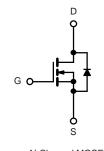


#### FEATURES

- Trench Power MOSFET
- 100 % R<sub>g</sub> and UIS Tested

#### APPLICATIONS

- Synchronous Rectification
- Power Supplies



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	<b>T</b> <sub>A</sub> = 25 °C, unle	ss otherwise no	ted	
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V <sub>DS</sub>	40	V
$\begin{tabular}{ c c c c } \hline Drain-Source Voltage & & & \\ \hline Gate-Source Voltage & & & \\ \hline T_C = 25 \ ^{\circ}C & \\ \hline T_C = 70 \ ^{\circ}C & \\ \hline T_A = 25 \ ^{\circ}C & \\ \hline T_A = 70 \ ^{\circ}C & \\ \hline \hline T_A = 70 \ ^{\circ}C & \\ \hline \hline T_A = 70 \ ^{\circ}C & \\ \hline \hline T_A = 70 \ ^{\circ}C & \\ \hline \hline T_A = 70 \ ^{\circ}C & \\ \hline \hline T_A = 70 \ ^{\circ}C & \\ \hline \hline T_A = 25 \ ^{\circ}C & \\ \hline \hline T_A = 25 \ ^{\circ}C & \\ \hline \hline T_A = 25 \ ^{\circ}C & \\ \hline \hline T_A = 25 \ ^{\circ}C & \\ \hline \hline T_C = 25 \ ^{\circ}C & \\ \hline \hline \hline T_C = 25 \ ^{\circ}C & \\ \hline \hline \hline T_C = 25 \ ^{\circ}C & \\ \hline \hline \hline \hline T_C = 25 \ ^{\circ}C & \\ \hline \hline \hline \hline \hline T_C = 25 \ ^{\circ}C & \\ \hline \hline$		V <sub>GS</sub>	± 20	v
	T <sub>C</sub> = 25 °C		110 <sup>a, c</sup>	
Continuous Drain Current (T = $175 ^{\circ}$ C)	T <sub>C</sub> = 70 °C		90 <sup>c</sup>	
Continuous Drain Current $(T_j = 175 \text{ C})$	T <sub>A</sub> = 25 °C	I <sub>D</sub>	31 <sup>b</sup>	A
	T <sub>A</sub> = 70 °C		25 <sup>b</sup>	A
Pulsed Drain Current	·	I <sub>DM</sub>	270	
Avalanche Current Pulse	$T_{C} = 25 \text{ °C}$ $T_{C} = 70 \text{ °C}$ $T_{A} = 25 \text{ °C}$ $T_{A} = 70 \text{ °C}$ $L = 0.1 \text{ mH}$ $T_{C} = 25 \text{ °C}$ $T_{A} = 25 \text{ °C}$ $T_{C} = 25 \text{ °C}$ $T_{C} = 70 \text{ °C}$ $T_{C} = 70 \text{ °C}$ $T_{A} = 25 \text{ °C}$ $T_{A} = 70 \text{ °C}$	I <sub>AS</sub>	85	
Single Pulse Avalanche Energy	L = 0.1 IIIH	E <sub>AS</sub>	320	V
Continuous Source Drain Diode Current	T <sub>C</sub> = 25 °C	I <sub>S</sub>	110 <sup>a, c</sup>	Α
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	15	2.6 <sup>b</sup>	A
	T <sub>C</sub> = 25 °C		312 <sup>a</sup>	
Maximum Power Dissipation	T <sub>C</sub> = 70 °C	PD	200	w
Maximum Fower Dissipation	T <sub>A</sub> = 25 °C 3.13 <sup>b</sup>	vv		
	T <sub>A</sub> = 70 °C		2.0 <sup>b</sup>	
Operating Junction and Storage Temperature Ra	ange	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>b</sup>	Steady State	R <sub>thJA</sub>	32	40	°C/W
Maximum Junction-to-Case	Steady State	R <sub>thJC</sub>	0.33	0.4	0,00

Notes:

a. Based on T<sub>C</sub> = 25 °C.

b. Surface Mounted on 1" x 1" FR4 board.

c. Calculated based on maximum junction temperature. Package limitation current is 110 A.



SPECIFICATIONS $T_J = 25 \ ^{\circ}C$ ,	unless other	wise noted				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = 250 \mu A$	40			V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = 250 μA		41		mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	$I_D = 230 \mu A$		- 8		mv/°C
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1.2		2.5	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zara Cata Valtaga Drain Current	1	$V_{DS} = 40 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 40 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 \text{ °C}$			10	μA
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	120			А
	Р	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A		6		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_{D} = 20 \text{ A}$		7		mΩ
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A		180		S
Dynamic <sup>b</sup>	•			•	•	•
Input Capacitance	C <sub>iss</sub>			2900		
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ = 20 V, $V_{GS}$ = 0 V, f = 1 MHz		750		pF
Reverse Transfer Capacitance	C <sub>rss</sub>	$ \begin{array}{c c} C_{oss} & V_{DS} = 20 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz} & 750 \\ \hline C_{rss} & & 310 \\ \hline Q_g & & 130 \\ \hline Q_{gs} & V_{DS} = 20 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_D = 20 \text{ A} & 20 \\ \hline \end{array} $		1		
Total Gate Charge				130		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 20$ V, $V_{GS} = 10$ V, $I_{D} = 20$ A		20		nC
Gate-Drain Charge	Q <sub>gd</sub>			32		
Gate Resistance	Rg	f = 1 MHz		0.85	1.3	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			20	30	
Rise Time	t <sub>r</sub>	$V_{DD} = 20 \text{ V}, \text{ R}_{L} = 1.0 \Omega$		11	17	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ 20 A, $V_{GEN}$ = 10 V, $R_g$ = 1 $\Omega$		77	115	1
Fall Time	t <sub>f</sub>			10	15	
Turn-On Delay Time	t <sub>d(on)</sub>			102	155	ns
Rise Time	t <sub>r</sub>	$V_{DD}$ = 20 V, $R_L$ = 1.0 $\Omega$		62	95	-
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 20$ A, $V_{GEN}$ = 4.5 V, $R_g$ = 1 $\Omega$		180	270	
Fall Time	t <sub>f</sub>			60	90	
Drain-Source Body Diode Characteristic	s					
Continuous Source-Drain Diode Current	۱ <sub>S</sub>	T <sub>C</sub> = 25 °C			110	٨
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				200	A
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 20 A		0.8	1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>			50	75	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	L = 20.4  di/dt = 100.4  hz = 25.90		70	105	nC
Reverse Recovery Fall Time	t <sub>a</sub>	I <sub>F</sub> = 20 A, di/dt = 100 A/μs, T <sub>J</sub> = 25 °C		30		
Reverse Recovery Rise Time	t <sub>b</sub>			20		ns

Notes:

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

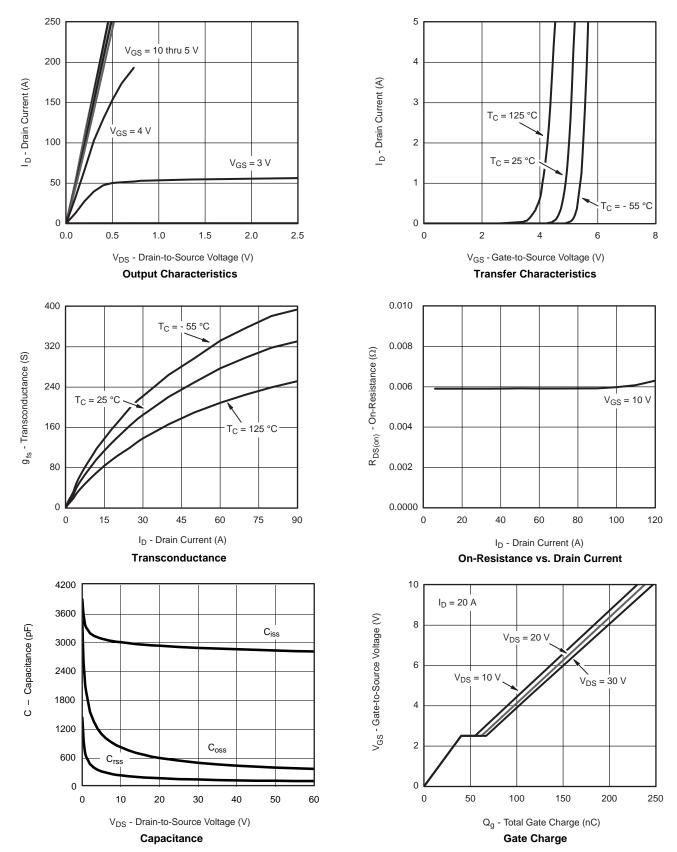
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

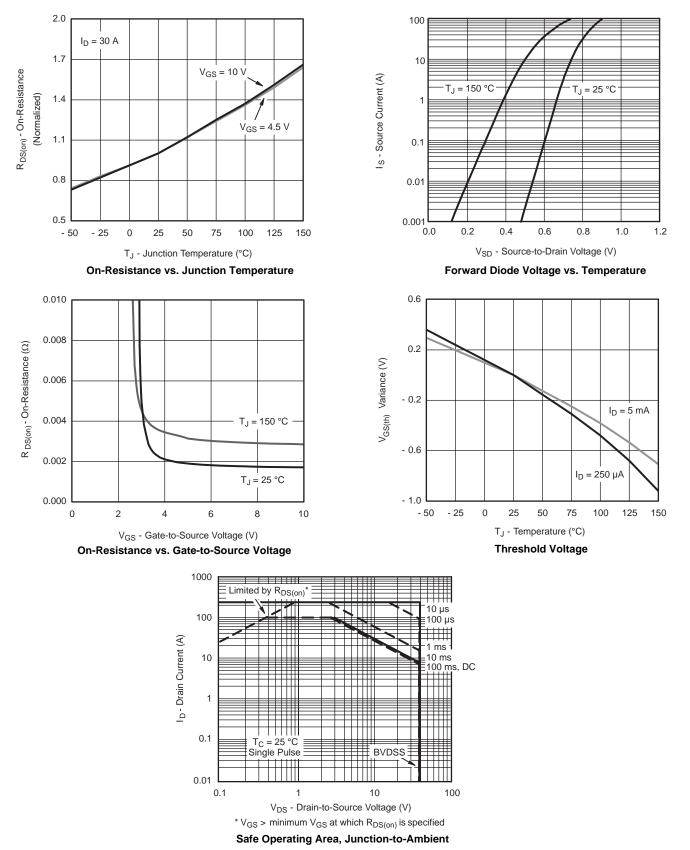


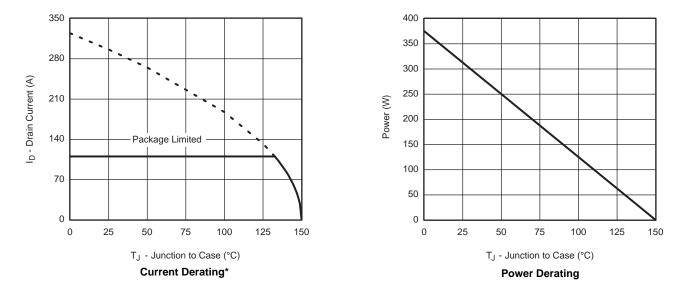
服务热线:400-655-8788

## PSMN4R5-40PS-VB



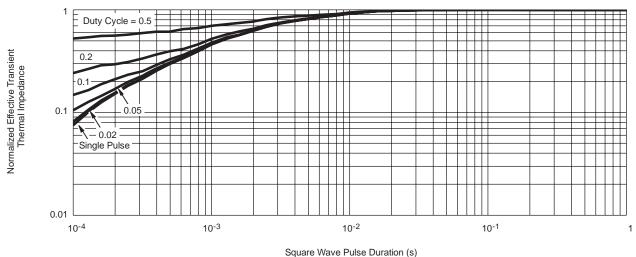
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

\* The power dissipation  $P_D$  is based on  $T_{J(max)}$  = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

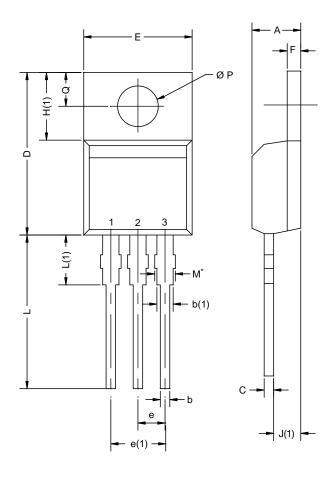


Normalized Thermal Transient Impedance, Junction-to-Case

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## **TO-220AB**



	MILLIN	IETERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
А	4.25	4.65	0.167	0.183	
b	0.69	1.01	0.027	0.040	
b(1)	1.20	1.73	0.047	0.068	
С	0.36	0.61	0.014	0.024	
D	14.85	15.49	0.585	0.610	
E	10.04	10.51	0.395	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.09	6.48	0.240	0.255	
J(1)	2.41	2.92	0.095	0.115	
L	13.35	14.02	0.526	0.552	
L(1)	3.32	3.82	0.131	0.150	
ØΡ	3.54	3.94	0.139	0.155	
Q	2.60	3.00	0.102	0.118	

#### Notes

\* M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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