

# NCEP85T25-VB Datasheet N-Channel 80 V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}$ ( $\Omega$ ) MAX. $I_D$ (A)		Q <sub>g</sub> (TYP.)		
80	0.0028 at V <sub>GS</sub> = 10 V	195	94		
	0.0030 at V <sub>GS</sub> = 7.5 V	185	94		



## **FEATURES**

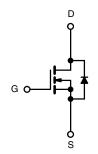
- Trench power MOSFET
- Maximum 175 °C junction temperature
- Very low Q<sub>gd</sub> reduces power loss from passing through V<sub>plateau</sub>
- 100 % R<sub>g</sub> and UIS tested



FREE

#### **APPLICATIONS**

- Power supply
  - Secondary synchronous rectification
- DC/DC converter
- Power tools
- Motor drive switch
- DC/AC inverter
- Battery management



N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>C</sub> = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	LIMIT	UNIT			
Drain-Source Voltage	V <sub>DS</sub>	80	V			
Gate-Source Voltage	V <sub>GS</sub>	± 20	V			
Continuous Durin Comment /T., 150 °C)	T <sub>C</sub> = 25 °C		195	۸		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 70 °C	I <sub>D</sub>	120 <sup>d</sup>			
Pulsed Drain Current (t = 100 μs)	I <sub>DM</sub>	600	A			
Avalanche Current		I <sub>AS</sub>	70			
Single Avalanche Energy <sup>a</sup>	L = 0.1 mH	E <sub>AS</sub>	245	mJ		
Mariana Dama Dissination 2	T <sub>C</sub> = 25 °C	В	375 b	W		
Maximum Power Dissipation <sup>a</sup>	T <sub>C</sub> = 125 °C	$ P_D$	125 <sup>b</sup>			
Operating Junction and Storage Temperature Ra	nge	T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C		

THERMAL RESISTANCE RATINGS						
PARAMETER	SYMBOL	LIMIT	UNIT			
Junction-to-Ambient (PCB Mount) <sup>c</sup>	R <sub>thJA</sub>	40	°C ///			
Junction-to-Case (Drain)	R <sub>thJC</sub>	0.4	°C/W			

#### Notes

- a. Duty cycle  $\leq$  1 %.
- b. See SOA curve for voltage derating.
- c. When mounted on 1" square PCB (FR4 material).
- d. Package limited.



PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static			•				
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	80	-	-	W	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2	-	4	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 250	nA	
		V <sub>DS</sub> = 80 V, V <sub>GS</sub> = 0 V	-	-	1	μА	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 80 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C	-	-	150		
		V <sub>DS</sub> = 80 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C	-	-	5	mA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$	120	-	-	Α	
Drain-Source On-State Resistance a	В	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A	-	0.0028	-	Ω	
Drain-Source On-State Resistance 4	R <sub>DS(on)</sub>	V <sub>GS</sub> = 7.5 V, I <sub>D</sub> = 20 A	-	0.0030	-		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A	-	82	-	S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 40 V, f = 1 MHz	-	7910	-	pF	
Output Capacitance	C <sub>oss</sub>		-	3250	-		
Reverse Transfer Capacitance	C <sub>rss</sub>		-	348	-		
Total Gate Charge <sup>c</sup>	Qg		-	94	141	nC	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 40 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$	-	31	-		
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>		-	10	-		
Gate Resistance	$R_g$	f = 1 MHz	0.28	1.4	2.8	Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>		-	24	40		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 40 \text{ V}, \text{ R}_{L} = 4 \Omega$	-	24	40	20	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 10 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$	-	34	60	ns	
Fall Time <sup>c</sup>	t <sub>f</sub>		-	14	28		
Drain-Source Body Diode Ratings ar	nd Characteris	stics <sup>b</sup> (T <sub>C</sub> = 25 °C)					
Pulsed Current (t = 100 μs)	I <sub>SM</sub>		-	-	250	Α	
Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>F</sub> = 10 A, V <sub>GS</sub> = 0 V	-	0.8	1.5	V	
Reverse Recovery Time	t <sub>rr</sub>		-	126	190	ns	
Peak Reverse Recovery Charge	I <sub>RM(REC)</sub>	$I_F = 34 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}$	-	5	10	Α	
Reverse Recovery Charge	Q <sub>rr</sub>		_	0.315	0.475	иC	

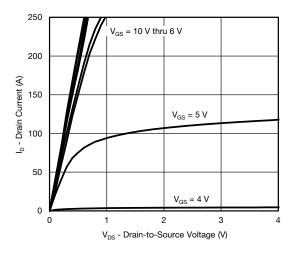
#### Notes

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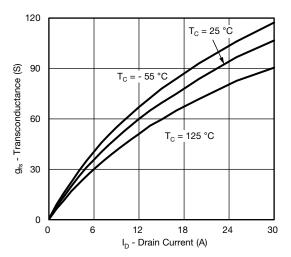
- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %. b. Guaranteed by design, not subject to production testing. c. Independent of operating temperature.



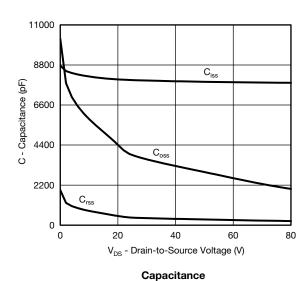
## **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)

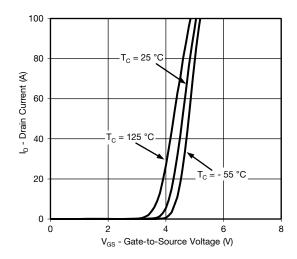


### **Output Characteristics**

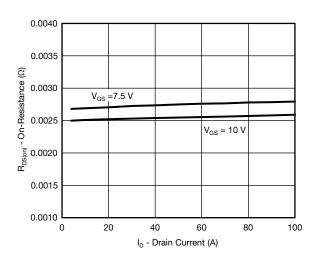


## Transconductance

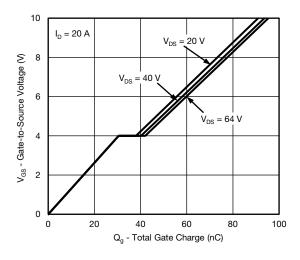




#### **Transfer Characteristics**



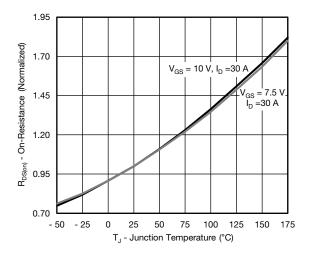
On-Resistance vs. Drain Current



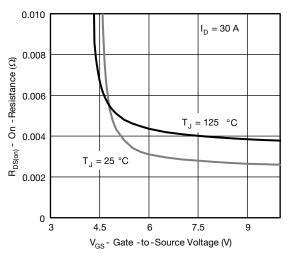
**Gate Charge** 



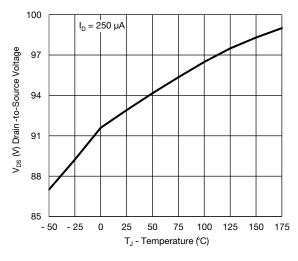
## TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise noted)



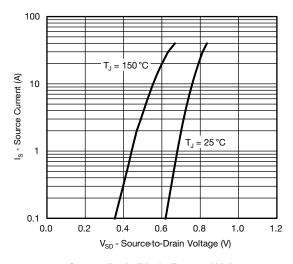
On-Resistance vs. Junction Temperature



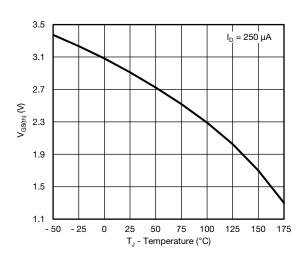
On-Resistance vs. Gate-to-Source Voltage



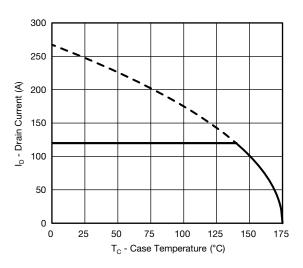
Drain Source Breakdown vs. Junction Temperature



**Source Drain Diode Forward Voltage** 



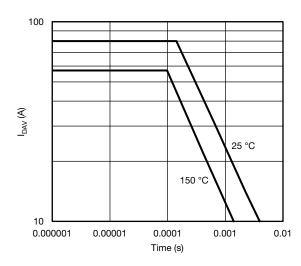
Threshold Voltage



**Current De-rating** 

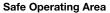


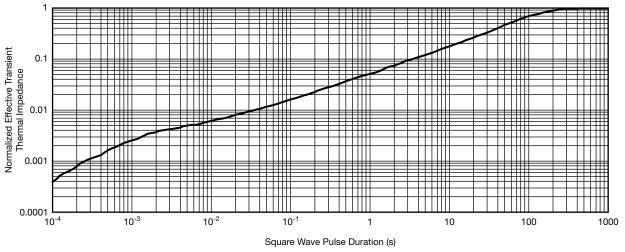
## **THERMAL RATINGS** ( $T_A = 25$ °C, unless otherwise noted)



1000 I<sub>DM</sub> Limited 10 µs 100 I<sub>D</sub> - Drain Current (A) 100 μs 10 Limited by Bsid 1 1 ms 111111 0.1 T<sub>C</sub> = 25°C Single Pulse 10 ms **BVDSS** Limited 0.01  $\begin{array}{c} 1 & 10 \\ V_{DS}\text{-} Drain-to-Source Voltage (V) \\ ^*V_{GS}> minimum \ V_{GS} \ at \ which \ R_{DS(on)} \ is \ specified \end{array}$ 0.1 100

Single Pulse Avalanche Current Capability vs. Time





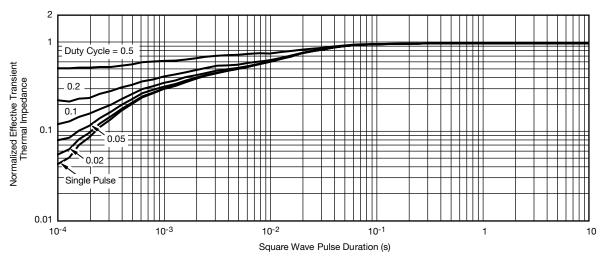
Normalized Thermal Transient Impedance, Junction-to-Ambient

服务热线:400-655-8788

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## **THERMAL RATINGS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



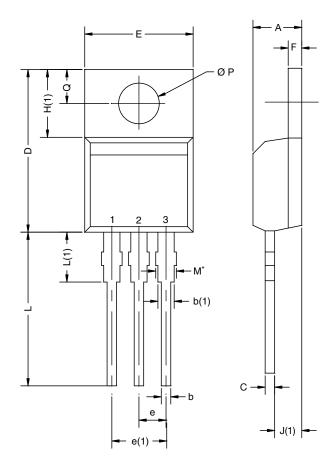
Normalized Thermal Transient Impedance, Junction-to-Case

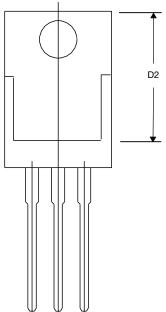
#### Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction to Case (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.



# **TO-220AB**





	MILLIN	IETERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
А	4.25	4.65	0.167	0.183	
b	0.69	1.01	0.027	0.040	
b(1)	1.20	1.73	0.047	0.068	
С	0.36	0.61	0.014	0.024	
D	14.85	15.49	0.585	0.610	
D2	12.19	12.70	0.480	0.500	
Е	10.04	10.51	0.395	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.09	6.48	0.240	0.255	
J(1)	2.41	2.92	0.095	0.115	
L	13.35	14.02	0.526	0.552	
L(1)	3.32	3.82	0.131	0.150	
ØΡ	3.54	3.94	0.139	0.155	
Q	2.60	3.00	0.102	0.118	
ECN: T14-0413-Rev. P, 16-Jun-14 DWG: 5471					

## Note

 $<sup>^{\</sup>star}$  M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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