

COMPLIANT

# F630M-VB Datasheet N-Channel 200 V (D-S) MOSFET

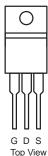
PRODUCT SUMMARY					
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)			
200	0.270 at V <sub>GS</sub> = 10 V	10			

#### **FEATURES**

- DT-Trench Power MOSFET
- 175 °C Junction Temperature
- PWM Optimized
- 100 % R<sub>g</sub> Tested
  Compliant to RoHS Directive 2002/95/EC

#### **APPLICATIONS**

• Primary Side Switch



# D G N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25 °C, unless otherwise noted) Limit Symbol Unit Parameter **Drain-Source Voltage** V<sub>DS</sub> 200 V ± 20 Gate-Source Voltage  $V_{GS}$ T<sub>C</sub> = 25 °C 10 Continuous Drain Current (T<sub>J</sub> = 175 °C)<sup>b</sup>  $I_D$ T<sub>C</sub> = 125 °C 6 **Pulsed Drain Current**  $I_{DM}$ А 38 Continuous Source Current (Diode Conduction)  $I_S$ 12 Avalanche Current  $I_{AS}$ 10

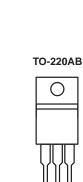
Single Pulse Avalanche Energy	L = 0.1 mH	E <sub>AS</sub>	18	mJ
Maximum Power Dissipation	T <sub>C</sub> = 25 °C	P <sub>D</sub>	121 <sup>b</sup>	w
	T <sub>A</sub> = 25 °C	·D	2 <sup>a</sup>	vv
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C
THERMAL RESISTANCE RATINGS				

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
hunstien to Ambienta	$t \le 10 \text{ s}$	R <sub>thJA</sub>	15	18	°C/W
Junction-to-Ambient <sup>a</sup>	Steady State		40	50	
Junction-to-Case (Drain)		R <sub>thJC</sub>	0.85	1.1	

Notes:

a. Surface mounted on 1" x 1" FR4 board.

b. See SOA curve for voltage derating.



### **F630M-VB**



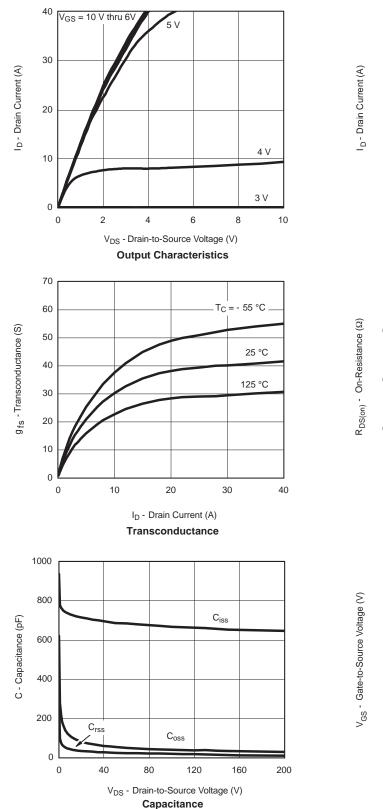
<b>SPECIFICATIONS</b> ( $T_J = 25$ °	C, unless o	otherwise noted)					
Parameter	Symbol	Test Conditions	Min.	Typ. <sup>a</sup>	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = 250 \mu A$	200			V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	2		4	v	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
	I <sub>DSS</sub>	V <sub>DS</sub> = 200 V, V <sub>GS</sub> = 0 V	1		1		
Zero Gate Voltage Drain Current		$V_{DS} = 200 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$			50	μA	
		$V_{DS} = 200 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$			250		
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = 5 V, V <sub>GS</sub> = 10 V	40			А	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5 A		0.270			
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5 A, T <sub>J</sub> = 125 °C		0.320		Ω	
Drain-Source On-State Resistance <sup>b</sup>		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5 A, T <sub>J</sub> = 175 °C		0.410			
		$V_{GS}$ = 4.5 V, I <sub>D</sub> = 5 A		0.310		1	
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 19 A		35		S	
Dynamic <sup>a</sup>							
Input Capacitance	C <sub>iss</sub>			800			
Output Capacitance	C <sub>oss</sub>	$V_{GS}$ = 0 V, $V_{DS}$ = 25 V, F = 1 MHz		110		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			80			
Total Gate Charge <sup>c</sup>	Qg			30			
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 19 \text{ A}$		8		nC	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			12			
Gate Resistance	Rg		0.5		2.9	Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			15	25		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD}$ = 100 V, R <sub>L</sub> = 5.2 $\Omega$		50	75		
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$\rm I_D \cong 19$ A, $\rm V_{GEN}$ = 10 V, $\rm R_g$ = 2.5 $\Omega$		30	45	ns	
Fall Time <sup>c</sup>	t <sub>f</sub>	]		60	90		
Source-Drain Diode Ratings and Char	acteristics (	Г <sub>С</sub> = 25 °С)					
Pulsed Current	I <sub>SM</sub>				40	А	
Diode Forward Voltage <sup>b</sup>	V <sub>SD</sub>	I <sub>F</sub> = 19 A, V <sub>GS</sub> = 0 V		0.9	1.5	V	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 19 A, dl/dt = 100 A/μs		180	250	ns	

Notes:

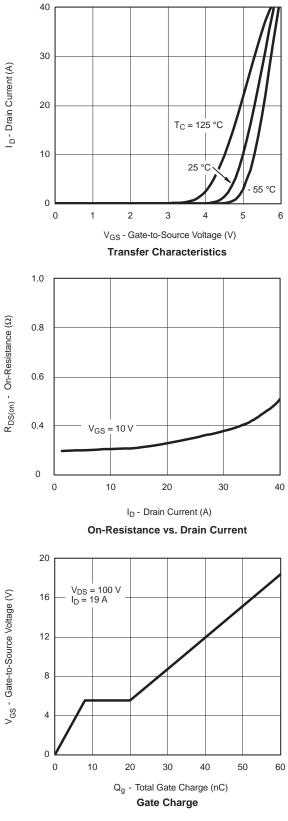
a. Guaranteed by design, not subject to production testing. b. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %. c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

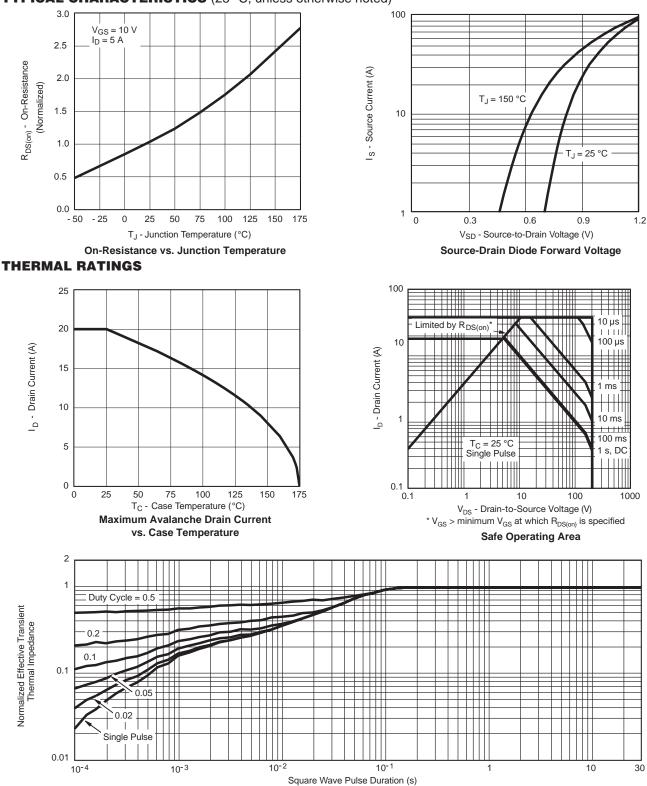




#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





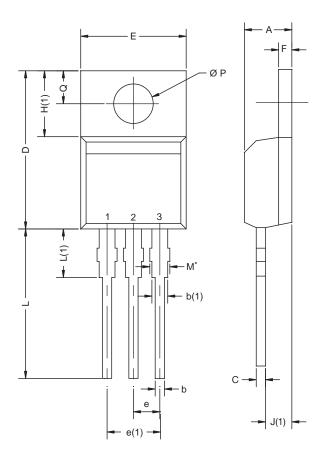


#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

Normalized Thermal Transient Impedance, Junction-to-Case



## **TO-220AB**



MIN.        4.25        0.69        1.20        0.36        14.85        10.04        2.41	MAX. 4.65 1.01 1.73 0.61 15.49 10.51	MIN. 0.167 0.027 0.047 0.014 0.585 0.395	MAX. 0.183 0.040 0.068 0.024 0.610 0.414
0.69 1.20 0.36 14.85 10.04	1.01 1.73 0.61 15.49 10.51	0.027 0.047 0.014 0.585	0.040 0.068 0.024 0.610
1.20    0.36    14.85    10.04	1.73 0.61 15.49 10.51	0.047 0.014 0.585	0.068 0.024 0.610
0.36 14.85 10.04	0.61 15.49 10.51	0.014 0.585	0.024 0.610
14.85 10.04	15.49 10.51	0.585	0.610
10.04	10.51		
		0.395	0.414
2.41			0.414
	2.67	0.095	0.105
4.88	5.28	0.192	0.208
1.14	1.40	0.045	0.055
6.09	6.48	0.240	0.255
2.41	2.92	0.095	0.115
13.35	14.02	0.526	0.552
3.32	3.82	0.131	0.150
3.54	3.94	0.139	0.155
2.60	3.00	0.102	0.118
	1.14      6.09      2.41      13.35      3.32      3.54      2.60	1.14  1.40    6.09  6.48    2.41  2.92    13.35  14.02    3.32  3.82    3.54  3.94	1.141.400.0456.096.480.2402.412.920.09513.3514.020.5263.323.820.1313.543.940.1392.603.000.102

#### Notes

 $^{\star}$  M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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