

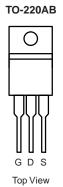
# BUK7540-100A-VB Datasheet N-Channel 100-V (D-S) MOSFET

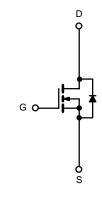
PRODUCT SUMMARY					
V <sub>DS</sub> (V)	100				
$R_{DS(on)} (\Omega)$ at $V_{GS} = 10 V$	0. 036				
I <sub>D</sub> (A) <sup>a</sup>	55				
Configuration	Single				

## FEATURES

- Trench Power MOSFETS
- 175 °C Junction Temperature
- Low Thermal Resistance Package







N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T <sub>C</sub> = 25 °C, unless oth	erwise noted			
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V <sub>DS</sub>	100	V		
Gate-Source Voltage	V <sub>GS</sub>	± 20	v		
Continuous Drain Current (T <sub>J</sub> = 175 °C)	T <sub>C</sub> = 25 °C	1-	55		
	T <sub>C</sub> = 125 °C	I <sub>D</sub>	40	•	
Pulsed Drain Current	I <sub>DM</sub>	135	- A		
Avalanche Current	I <sub>AR</sub>	35	1		
Repetitive Avalanche Energy <sup>a</sup>	L = 0.1 mH	E <sub>AR</sub>	61	mJ	
Maximum Power Dissipation <sup>a</sup>	T <sub>C</sub> = 25 °C	Р	127 <sup>b</sup>	10/	
	T <sub>A</sub> = 25 °C <sup>c</sup>	- P <sub>D</sub> -	3.75	W	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS	5			
Parameter		Symbol	Limit	Unit
Junction-to-Ambient	(PCB Mount) <sup>c</sup>	R <sub>thJA</sub>	40	°C/W
Junction-to-Case (Drain)		R <sub>thJC</sub>	1.4	5

Notes:

a. Duty cycle  $\leq$  1 %.

b. See SOA curve for voltage derating.

c. When Mounted on 1" square PCB (FR-4 material).

\* Pb containing terminations are not RoHS compliant, exemptions may apply.

<b>SPECIFICATIONS</b> $T_J = 25^{\circ}$	C, unless of	herwise noted					
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	•						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{SS} = 0 V, I_D = 250 \mu A$	100			V	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1		3	v	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
		$V_{DS} = 100 \ W_{GS} = 0 \ V$			1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 80 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 125 \text{ °C}$			50		
		$V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 \text{ °C}$			250		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	75			А	
		$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 5 \text{ A}$		0.036		1	
	_	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 3 \text{ A}$		0.038		-	
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	$V_{GS}$ = 10 V, I <sub>D</sub> = 5 A, T <sub>J</sub> = 125 °C		0.050		Ω	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3 A, T <sub>J</sub> = 175 °C		0.065			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 15 A	10			S	
Dynamic <sup>b</sup>	•		4				
Input Capacitance	C <sub>iss</sub>			4500		pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 V, V_{DS} = 25 V, f = 1 MHz$		270			
Reverse Transfer Capacitance	C <sub>rss</sub>			90			
Total Gate Charge <sup>c</sup>	Qg			35	60	nC	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 40 \text{ A}$		11			
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			9			
Gate Resistance	R <sub>G</sub>			1.7		Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			11	20		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD}$ = 50 V, $R_L$ = 1.25 $\Omega$		12	20	ns	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong$ 40 A, $V_{GEN}$ = 10 V, $R_G$ = 2.5 $\Omega$		30	45		
Fall Time <sup>c</sup>	t <sub>f</sub>			12	20		
Source-Drain Diode Ratings and Cha	aracteristics 7	$C = 25 \ ^{\circ}C^{b}$	1				
Continuous Current	ا <sub>S</sub>	-			40	•	
Pulsed Current	I <sub>SM</sub>				120	120 A	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 30 A, V <sub>GS</sub> = 0 V		1.0	1.5	V	
Reverse Recovery Time	t <sub>rr</sub>			60	100	ns	
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	I <sub>F</sub> = 30 A, di/dt = 100 A/µs		5	8	А	
Reverse Recovery Charge	Q <sub>rr</sub>			0.15	0.4	μC	

Notes:

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

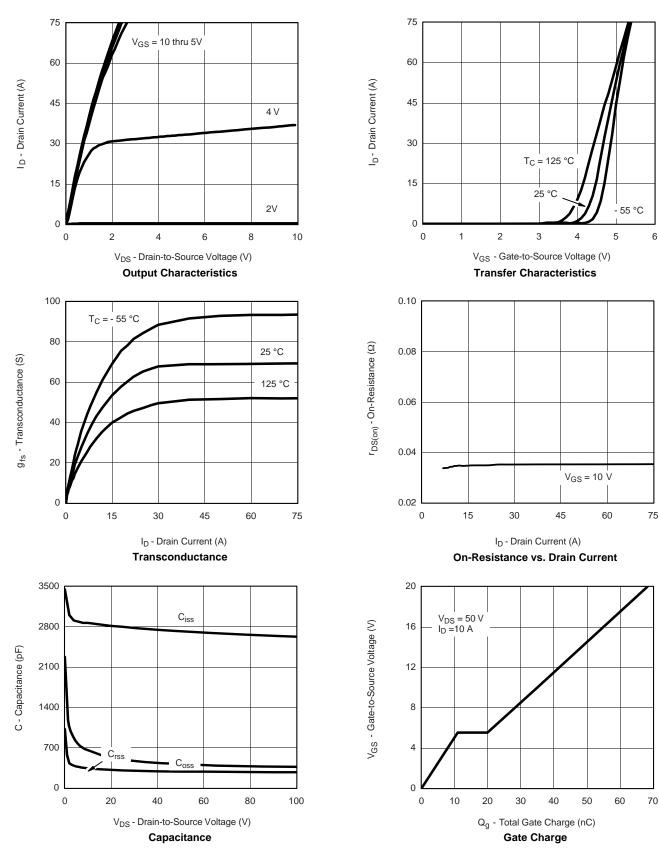
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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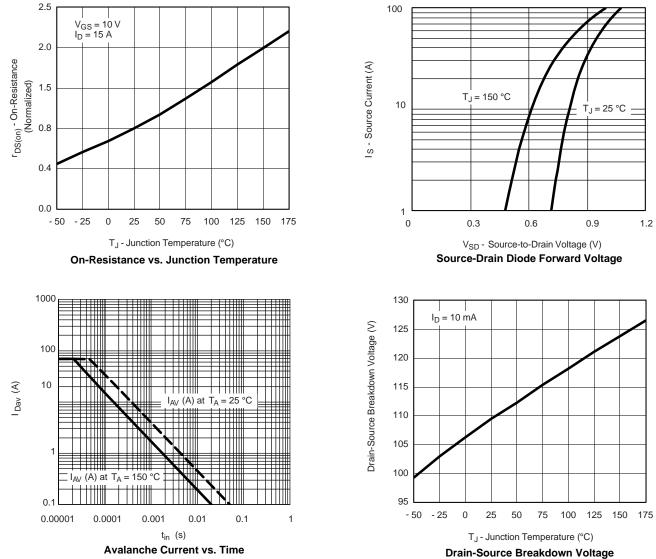


### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





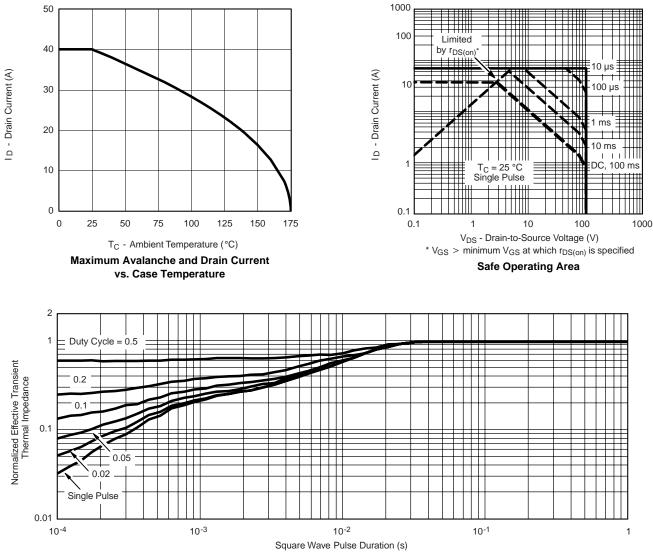
## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



vs. Junction Temperature



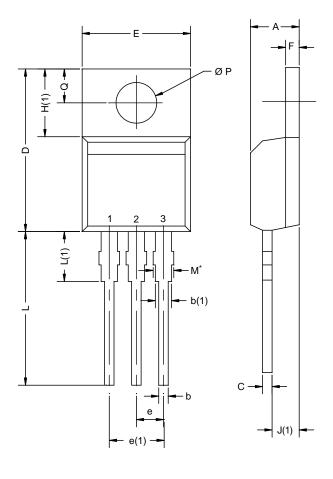
#### THERMAL RATINGS



Normalized Thermal Transient Impedance, Junction-to-Case



## **TO-220AB**



	MILLIMETERS INCI			HES
DIM.	MIN.	MAX.	MIN.	MAX.
А	4.25	4.65	0.167	0.183
b	0.69	1.01	0.027	0.040
b(1)	1.20	1.73	0.047	0.068
С	0.36	0.61	0.014	0.024
D	14.85	15.49	0.585	0.610
Е	10.04	10.51	0.395	0.414
е	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.09	6.48	0.240	0.255
J(1)	2.41	2.92	0.095	0.115
L	13.35	14.02	0.526	0.552
L(1)	3.32	3.82	0.131	0.150
ØΡ	3.54	3.94	0.139	0.155
Q	2.60	3.00	0.102	0.118
ECN: X12-0208-Rev. N, 08-Oct-12 DWG: 5471				

#### Notes

 $^{\star}$  M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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