

# AP4407GP-VB Datasheet P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub>	-30	V		
$R_{DS(on)} V_{GS} = 10 V$	8	mΩ		
$R_{DS(on)}$ $V_{GS} = 4.5 \text{ V}$	11	mΩ		
I <sub>D</sub>	-70	Α		
Configuration	Single			

# **FEATURES**

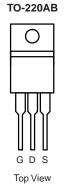
- Halogen-free
- Trench Power MOSFET
- 100 % R<sub>g</sub> Tested
- 100 % UIS Tested

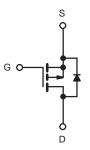


RoHS

## **APPLICATIONS**

- Load Switch
- Notebook Adaptor Switch





P-Channel MOSFET

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V <sub>DS</sub>	- 30	V
Gate-Source Voltage		$V_{GS}$	± 20	
	T <sub>C</sub> = 25 °C		- 70	
Continuous Prain Current (T. – 150 °C)	T <sub>C</sub> = 70 °C		- 55	
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>A</sub> = 25 °C	l <sub>D</sub>	- 55	
	T <sub>A</sub> = 70 °C		-45	Λ.
Pulsed Drain Current		I <sub>DM</sub>	- 200	A
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C		- 50	
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	- I <sub>S</sub>	- 50	
Avalanche Current	1 04	I <sub>AS</sub>	- 20	
Single-Pulse Avalanche Energy	L = 0.1 mH	E <sub>AS</sub>	20	mJ
Maximum Power Dissipation	T <sub>C</sub> = 25 °C		50	
	T <sub>C</sub> = 70 °C		32	w
	T <sub>A</sub> = 25 °C	P <sub>D</sub>	7 <sup>a, b</sup>	VV
	T <sub>A</sub> = 70 °C		5 <sup>a, b</sup>	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a, c</sup>	t ≤ 10 s	$R_{thJA}$	38	46	°C/W
Maximum Junction-to-Foot	Steady State	$R_{thJF}$	20	25	C/VV

## Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. t = 10 s.
- c. Maximum under Steady State conditions is 85 °C/W.
- d. Based on  $T_C$  = 25 °C.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static				l l		ı	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 30			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	In = - 250 µA		- 34		mV/	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = - 250 μΑ		5.3		°C	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.0		- 2.5	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 25 \text{ V}$			± 100	nA	
Zana Oata Vallana Brain Oamani		V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V			- 1		
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 5	μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge -10 \text{ V}, V_{GS} = -10 \text{ V}$	- 30			Α	
	, ,	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 10 A		8	8		
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 8 A		11		mΩ	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 10 A		28		S	
Dynamic <sup>b</sup>						ı	
Input Capacitance	C <sub>iss</sub>			3950			
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz		455		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			390			
Total Gate Charge		V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 10 A		57	86	1	
	Q <sub>g</sub> V <sub>DS</sub> = -13 v, v <sub>GS</sub> = -10 v, I <sub>D</sub> = -10 A		29.5	45			
Gate-Source Charge	Q <sub>qs</sub>	$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -10 \text{ A}$		8		nC	
Gate-Drain Charge	$Q_{gd}$			22			
Gate Resistance	R <sub>g</sub>	f = 1 MHz	0.5	2.2	4.4	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			13	25		
Rise Time	t <sub>r</sub>	$V_{DD} = -15 \text{ V}, R_{L} = 1.5 \Omega$		12	24		
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_{D} \cong -10 \text{ A}, V_{GEN} = -10 \text{ V}, R_{q} = 1 \Omega$		40	70		
Fall Time	t <sub>f</sub>	Ű		9	18		
Turn-On Delay Time	t <sub>d(on)</sub>			48	80	ns	
Rise Time	t <sub>r</sub>	$V_{DD} = -15 \text{ V}, R_{L} = 1.5 \Omega$		92	160		
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D \cong -10 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$		34	60	1	
Fall Time	t <sub>f</sub>	Ů		19	35		
Drain-Source Body Diode Characteris							
Continous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 4.1	_	
Pulse Diode Forward Current	I <sub>SM</sub>	-			- 60	Α	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 3 A, V <sub>GS</sub> = 0 V		- 0.75	- 1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>	3 33		27	45	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	1 40 A 11/1/ 400 A/ T 55 50		16	27	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = -10 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		12			
Reverse Recovery Rise Time	t <sub>b</sub>			15		ns	

#### Notes:

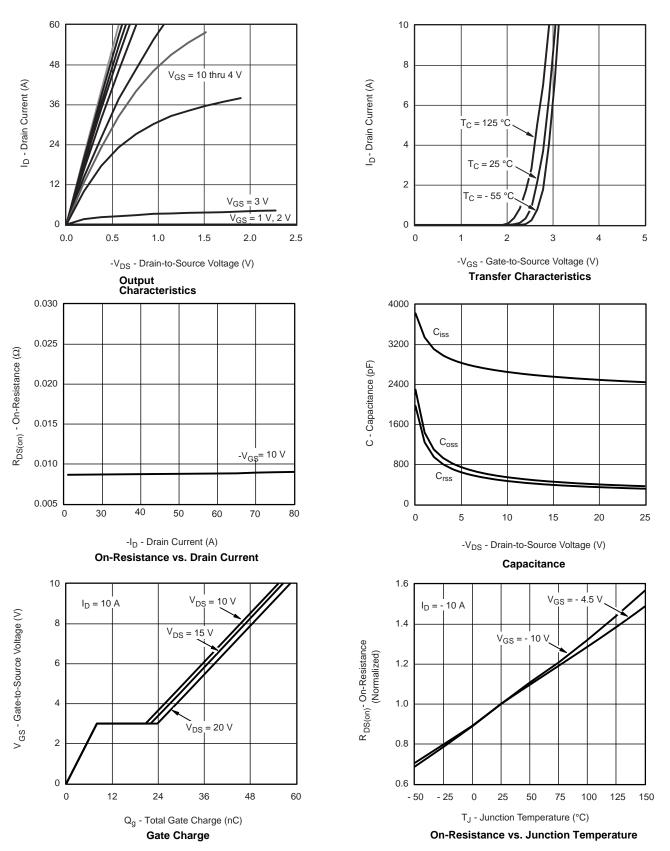
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$ 

b. Guaranteed by design, not subject to production testing.

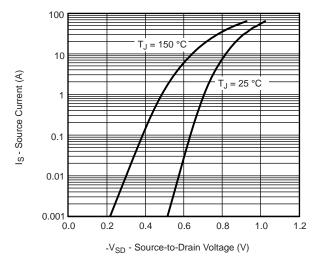


# TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

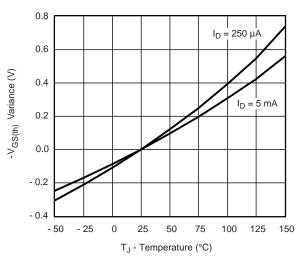




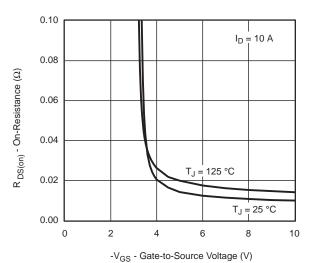
# TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



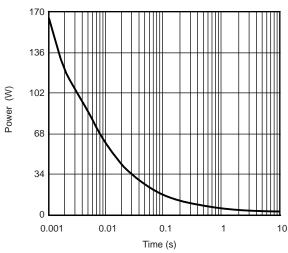
Source-Drain Diode Forward Voltage



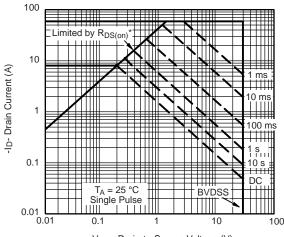
**Threshold Voltage** 



On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient



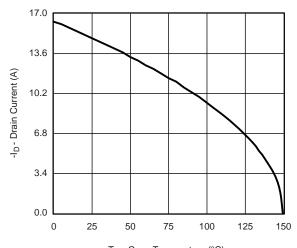
-V<sub>DS</sub> - Drain-to-Source Voltage (V)

Safe Operating Area

<sup>\*</sup>  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

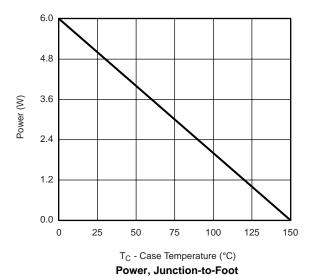


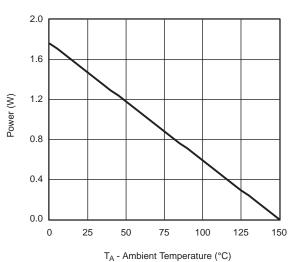
# MOSFET TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



 $T_{\mbox{\scriptsize C}}$  - Case Temperature (°C)

# **Current Derating\***



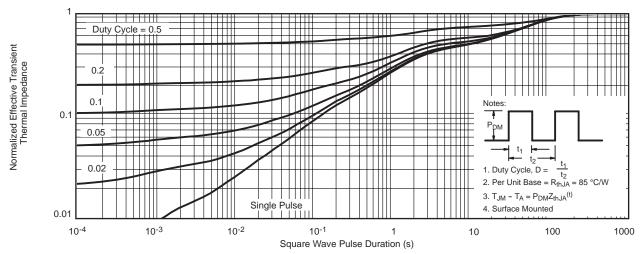


Power Derating, Junction-to-Ambient

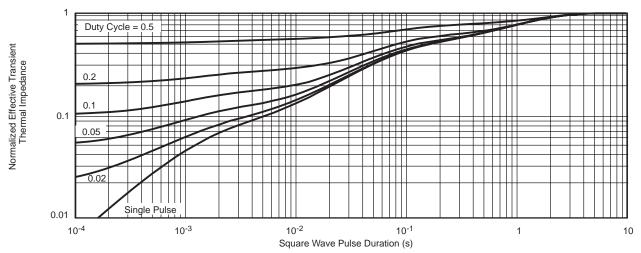
<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



# TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



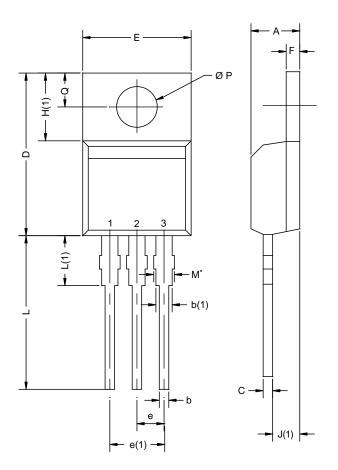
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot



# **TO-220AB**



•	MILLIN	METERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
Α	4.25	4.65	0.167	0.183	
b	0.69	1.01	0.027	0.040	
b(1)	1.20	1.73	0.047	0.068	
С	0.36	0.61	0.014	0.024	
D	14.85	15.49	0.585	0.610	
Е	10.04	10.51	0.395	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.09	6.48	0.240	0.255	
J(1)	2.41	2.92	0.095	0.115	
L	13.35	14.02	0.526	0.552	
L(1)	3.32	3.82	0.131	0.150	
ØР	3.54	3.94	0.139	0.155	
Q	2.60	3.00	0.102	0.118	

DWG: 5471

### Notes

 $^{\star}$  M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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