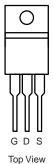


## **AOT424CU-VB Datasheet**

N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	30				
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 V$	0. 0020				
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 V$	0. 0028				
I <sub>D</sub> (A)	140				
Configuration	Single				

#### TO-220AB

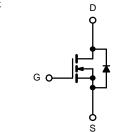


**FEATURES** • DT-Trench Power MOSFET

- 100 % R<sub>g</sub> and UIS Tested
- Compliant to RoHS Directive 2011/65/EU

#### **APPLICATIONS**

- OR-ing
- Server
- DC/DC •



N-Channel MOSFET

ABSOLUTE MAXIMUM RATING	Symbol	Limit	Unit		
Drain-Source Voltage		V <sub>DS</sub>	30		
Gate-Source Voltage		V <sub>GS</sub>	± 20	V	
	T <sub>C</sub> = 25 °C		140 <sup>a, e</sup>		
Continuous Drain Current (T 175 °C)	T <sub>C</sub> = 70 °C		110 <sup>e</sup>		
Continuous Drain Current (T <sub>J</sub> = 175 °C)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	39 <sup>b, c</sup>	A	
	T <sub>A</sub> = 70 °C		28 <sup>b, c</sup>	A .	
Pulsed Drain Current	I <sub>DM</sub>	370			
Avalanche Current Pulse	L = 0.1 mH	I <sub>AS</sub>	39		
Single Pulse Avalanche Energy	L = 0.1 mm	E <sub>AS</sub>	375	mJ	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	I <sub>S</sub>	90 <sup>a, e</sup>	A	
	T <sub>A</sub> = 25 °C	'S	3.13 <sup>b, c</sup>	A	
	T <sub>C</sub> = 25 °C		250 <sup>a</sup>		
Maximum Power Dissipation	T <sub>C</sub> = 70 °C	P <sub>D</sub>	175	w	
	T <sub>A</sub> = 25 °C	۲D	3.75 <sup>b, c</sup>	vv	
	T <sub>A</sub> = 70 °C		2.63 <sup>b, c</sup>		
Operating Junction and Storage Temperature Ra	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Тур.	Max.	Unit	
Maximum Junction-to-Ambient <sup>b, d</sup>	$t \le 10$ sec	R <sub>thJA</sub>	32	40	°C/W	
Maximum Junction-to-Case	Steady State	R <sub>thJC</sub>	0.5	0.6	·C/W	

Notes: a. Based on  $T_C = 25 \ ^{\circ}C$ . b. Surface mounted on 1" x 1" FR4 board.

c. t = 10 sec. d. Maximum under steady state conditions is 90 °C/W.

e. Calculated based on maximum junction temperature. Package limitation current is 90 A.



<b>SPECIFICATIONS</b> ( $T_J = 25 \text{ °C}$ , Parameter	Symbol	Test Conditions	Min.	Typ	Max.	Unit
Static	Symbol	Test Conditions	WIIII.	Тур.	IVIAX.	Unit
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 µA	30			V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$			35		- mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	ΔV <sub>GS(th)</sub> /T <sub>J</sub>	I <sub>D</sub> = 250 μA		- 7.5		
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \ \mu A$	1.0		3.0	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$	1.0		± 100	nA
	-033	$V_{\rm DS} = 30 \ V \ V_{\rm GS} = 0 \ V$			1	- μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 24 \text{ V } V_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 \text{ °C}$			10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 V, V_{GS} = 10 V$	90			A
	$V_{GS} = 0.01, U_{DS} = 0.00, U_{SS} = 0.00, U_{S$			0.0020		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 37 \text{ A}$		0.0028		Ω
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 38.8 A		160		S
Dynamic <sup>b</sup>	-10					
Input Capacitance	C <sub>iss</sub>			8400		pF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz		1725		
Reverse Transfer Capacitance	C <sub>rss</sub>			970		
	$V_{DS} = 15 V$ , $V_{CS} = 10 V$ , $I_{D} = 38.8 A$	$V_{DS}$ = 15 V, $V_{GS}$ = 10 V, $I_{D}$ = 38.8 A		171	257	nC
Total Gate Charge	Qg			81.5	123	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = 15 V, $V_{GS}$ = 4.5 V, $I_{D}$ = 28.8 A		34		
Gate-Drain Charge	Q <sub>gd</sub>			29		
Gate Resistance	R <sub>g</sub>	f = 1 MHz		1.4	2.1	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			18	27	ns
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 0.625 $\Omega$		11	17	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ 24 A, $V_{GEN}$ = 10 V, $R_g$ = 1 $\Omega$		70	105	
Fall Time	t <sub>f</sub>			10	15	
Turn-On Delay Time	t <sub>d(on)</sub>			55	83	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 0.67 $\Omega$		180	270	
Turn-Off Delay Time	t <sub>d(off)</sub>	$\text{I}_\text{D}\cong\text{22.5}$ A, $\text{V}_\text{GEN}$ = 4.5 V, $\text{R}_\text{g}$ = 1 $\Omega$		55	83	
Fall Time	t <sub>f</sub>			12	18	
Drain-Source Body Diode Characteristic	s					
Continuous Source-Drain Diode Current	۱ <sub>S</sub>	T <sub>C</sub> = 25 °C		140		^
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>			370		A
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 22 A		0.8	1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>			52	78	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 20 A, di/dt = 100 A/μs, Τ <sub>.I</sub> = 25 °C		70.2	105	nC
Reverse Recovery Fall Time	t <sub>a</sub>	$r_F = 20 \text{ A}, \text{ al/at} = 100 \text{ A/}\text{µs}, r_J = 25 \text{ °C}$		27		
Reverse Recovery Rise Time	t <sub>b</sub>			25		ns

Notes:

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

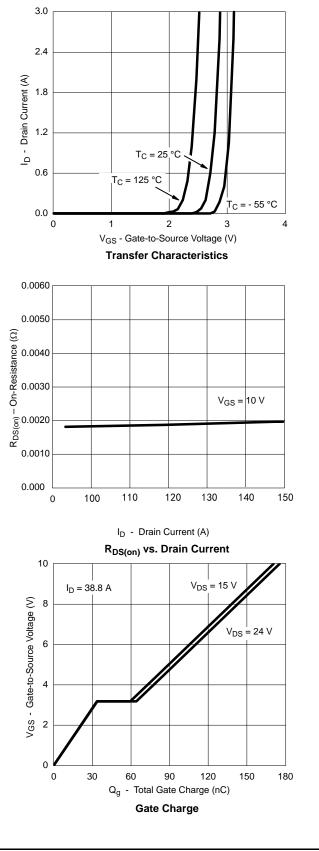
VBsemi Bsemi.com



#### 90 $V_{GS} = 10 \text{ V}$ thru 4 V 75 60 I<sub>D</sub> - Drain Current (A) 45 30 15 $V_{IGS} = 2 V$ $V_{GS} = 3 V$ 0 2.0 2.5 0.0 0.5 1.0 1.5 V<sub>DS</sub> - Drain-to-Source Voltage (V) **Output Characteristics** 600 T<sub>C</sub> = 25 °C 500 G<sub>fs</sub> - Transconductance (S) T<sub>C</sub> = 125 °C 400 300 T<sub>C</sub> = - 55 °C 200 100 0 0 10 20 30 70 80 90 40 50 60 $I_{\mathsf{D}}$ - Drain Current (A) Transconductance 8000 Ciss 6000 C - Capacitance (pF) 4000 2000 Coss 1000 C<sub>rss</sub> 0 0 6 12 18 24 30 V<sub>DS</sub> - Drain-to-Source Voltage (V)

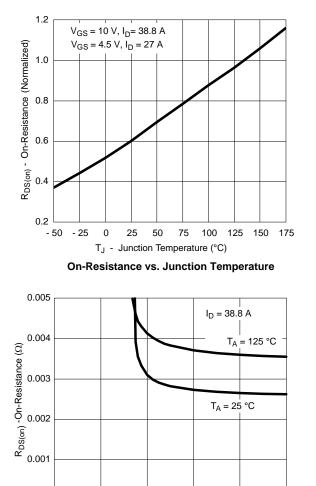
Capacitance

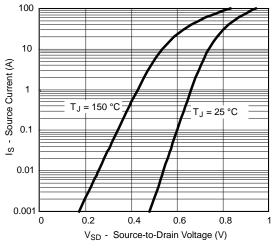
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



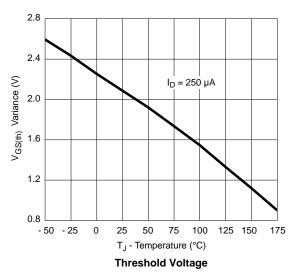


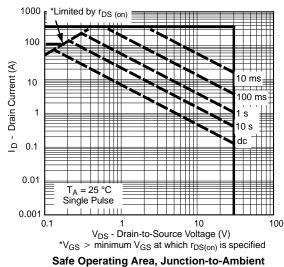
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





Forward Diode Voltage vs. Temperature





0.000

2

4

6

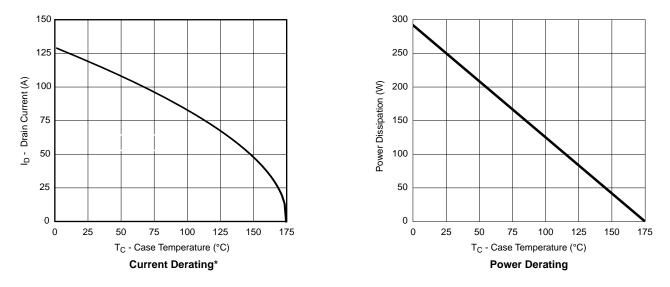
 $V_{GS}$  - Gate-to-Source Voltage (V)

R<sub>DS(on)</sub> vs. V<sub>GS</sub> vs. Temperature

8

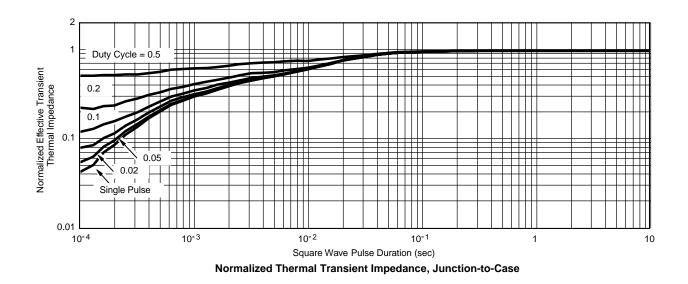
10





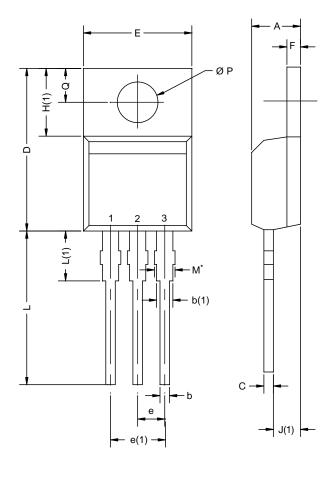
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

\*The power dissipation  $P_D$  is based on  $T_{J(max)} = 175$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





# **TO-220AB**



	MILLIN	IETERS	INC	HES	
DIM.	MIN.	MAX.	MIN.	MAX.	
А	4.25	4.65	0.167	0.183	
b	0.69	1.01	0.027	0.040	
b(1)	1.20	1.73	0.047	0.068	
С	0.36	0.61	0.014	0.024	
D	14.85	15.49	0.585	0.610	
E	10.04	10.51	0.395	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.09	6.48	0.240	0.255	
J(1)	2.41	2.92	0.095	0.115	
L	13.35	14.02	0.526	0.552	
L(1)	3.32	3.82	0.131	0.150	
ØР	3.54	3.94	0.139	0.155	
Q	2.60	3.00	0.102	0.118	
ECN: X12-0208-Rev. N, 08-Oct-12 DWG: 5471					

#### Notes

\* M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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