

K2168-VB Datasheet

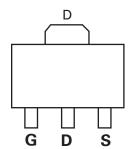
N-Channel 200 V (D-S) MOSFET

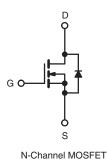
PRODUCT SUMMARY						
V _{DS} (V)	200					
$R_{DS(on)}(\Omega)$	V _{GS} = 10 V	1.6				
Q _g (Max.) (nC)	8.2					
Q _{gs} (nC)	1.8					
Q _{gd} (nC)	4.5					
Configuration	Single					

FEATURES

- Available in tape and reel
- Dynamic dV/dt rating
- Repetitive avalanche rated
- · Fast switching
- Ease of paralleling
- Simple drive requirements







ABSOLUTE MAXIMUM RATINGS ($T_{\rm C}$	= 25 °C, unl	ess otherwis	se noted)			
PARAMETER	SYMBOL	LIMIT	UNIT			
Drain-Source Voltage			V _{DS}	200	V	
Gate-Source Voltage			V_{GS}	± 20	v	
Continuous Drain Current	\/ at 10 \/	T _C = 25 °C	1-	1.0		
Continuous Drain Current	V _{GS} at 10 V	$T_C = 25 ^{\circ}C$ $T_C = 100 ^{\circ}C$	ID	0.6	Α	
Pulsed Drain Current ^a			I _{DM}	3.7		
Linear Derating Factor		0.025	W/°C			
Linear Derating Factor (PCB Mount) e	<u> </u>	0.017] W/ C			
Single Pulse Avalanche Energy b		E _{AS}	50	mJ		
Repetitive Avalanche Current ^a		I _{AR}	0.96	А		
Repetitive Avalanche Energy ^a			E _{AR}	0.31	mJ	
Maximum Power Dissipation	Maximum Power Dissipation $T_C = 25 ^{\circ}C$			3.1		
Maximum Power Dissipation (PCB Mount) e	T _A =	25 °C	P _D	2.0	W	
Peak Diode Recovery dV/dt c	dV/dt	5.0	V/ns			
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C			
Soldering Recommendations (Peak Temperature) d		300				

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b. V_{DD} = 50 V, starting T_J = 25 °C, L = 81 mH, R_G = 25 Ω , I_{AS} = 0.96 A (see fig. 12). c. $I_{SD} \le 3.3$ A, $dI/dt \le 70$ A/µs, $V_{DD} \le V_{DS}$, $T_J \le 150$ °C.
- d. 1.6 mm from case.
- e. When mounted on 1" square PCB (FR-4 or G-10 material).



THERMAL RESISTANCE RATINGS							
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Maximum Junction-to-Ambient (PCB Mount) ^a	R _{thJA}	-	-	40	°C/W		
Maximum Junction-to-Case (Drain)	R _{thJC}	-	1	60			

Note

a. When mounted on 1" square PCB (FR-4 or G-10 material).

PARAMETER	SYMBOL	TEST CONDITIONS			TYP.	MAX.	UNIT
Static						•	
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} :	= 0 V, I _D = 250 μA	200	-	-	V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference	ce to 25 °C, I _D = 1 mA	-	0.30	-	V/°C
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	= V _{GS} , I _D = 250 μA	2.0	-	4.0	V
Gate-Source Leakage	I _{GSS}		V _{GS} = ± 20 V	-	-	± 100	nA
Zava Cata Valtaga Dvain Cuwant	I _{DSS}	V _{DS} =	V _{DS} = 200 V, V _{GS} = 0 V			25	
Zero Gate Voltage Drain Current		V _{DS} = 160 \	/, V _{GS} = 0 V, T _J = 125 °C	-	-	250	μA
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = 10 V	I _D = 0.58 A ^b	-	1.6	-	Ω
Forward Transconductance	9 _{fs}	V _{DS} =	= 50 V, I _D = 0.58 A	0.51	-	-	S
Dynamic							
Input Capacitance	C _{iss}		V _{GS} = 0 V,	-	140	-	pF
Output Capacitance	C _{oss}	1	$V_{DS} = 25 \text{ V},$	-	53	-	
Reverse Transfer Capacitance	C _{rss}	f = 1	f = 1.0 MHz, see fig. 5		15	-	
Total Gate Charge	Qg			-	-	8.2	nC
Gate-Source Charge	Q _{gs}	V _{GS} = 10 V	$V_{GS} = 10 \text{ V}$ $I_D = 3.3 \text{ A}, V_{DS} = 160 \text{ V},$ see fig. 6 and 13 b		-	1.8	
Gate-Drain Charge	Q _{gd}	1	See lig. 6 and 16	-	-	4.5	1
Turn-On Delay Time	t _{d(on)}				8.2	-	- ns
Rise Time	t _r	V_{DD} = 100 V, I_{D} = 3.3 A, R_{g} = 24 Ω , R_{D} = 30 Ω , see fig. 10 b		-	17	-	
Turn-Off Delay Time	t _{d(off)}			-	14	-	
Fall Time	t _f	1	-	8.9	-		
Internal Drain Inductance	L _D	6 mm (0.25")	Between lead, 6 mm (0.25") from			-	nH
Internal Source Inductance	L _S	package and die contact	-	6.0	-	ווח	
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	I _S	MOSFET symbol showing the		-	-	0.96	Α
Pulsed Diode Forward Current ^a	I _{SM}	integral revers p - n junction	-	-	7.7		
Body Diode Voltage	V _{SD}	T _J = 25 °C,	-	-	2.0	V	
Body Diode Reverse Recovery Time	t _{rr}	- T _J = 25 °C, I _F = 3.3 A, dl/dt = 100 A/μs b		-	150	310	ns
Body Diode Reverse Recovery Charge	Q _{rr}	$I_J = 25 \text{ °C, I}_F$	-	0.60	1.4	μC	
Forward Turn-On Time	t _{on}	Intrinsic tu	ırn-on time is negligible (turn	-on is dor	ninated b	y L _S and	L _D)

Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b. Pulse width $\leq 300~\mu s$; duty cycle $\leq 2~\%$.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

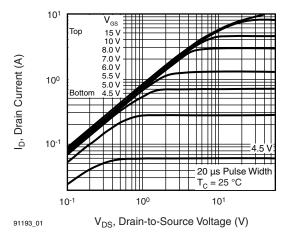


Fig. 1 - Typical Output Characteristics, T_C = 25 °C

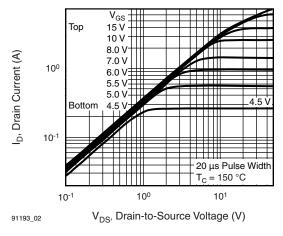


Fig. 2 - Typical Output Characteristics, T_C = 150 °C

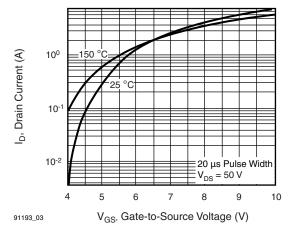


Fig. 3 - Typical Transfer Characteristics

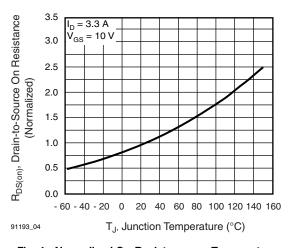


Fig. 4 - Normalized On-Resistance vs. Temperature

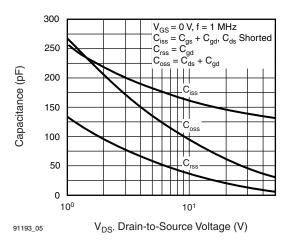


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

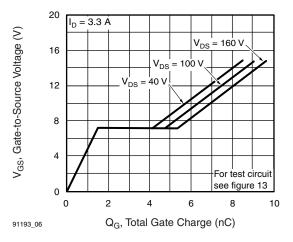


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage



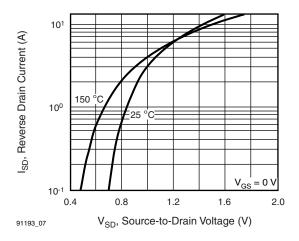


Fig. 7 - Typical Source-Drain Diode Forward Voltage

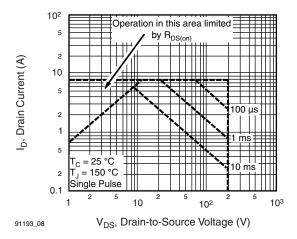


Fig. 8 - Maximum Safe Operating Area

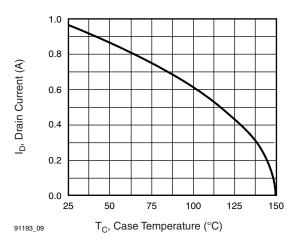


Fig. 9 - Maximum Drain Current vs. Case Temperature

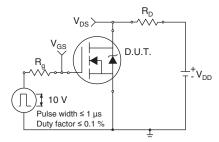


Fig. 10a - Switching Time Test Circuit

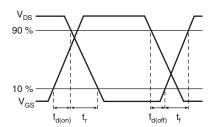


Fig. 10b - Switching Time Waveforms

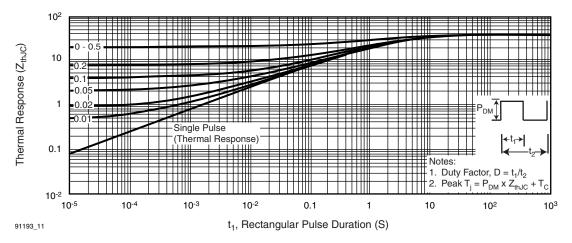


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case



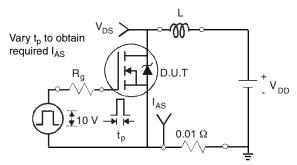


Fig. 12a - Unclamped Inductive Test Circuit

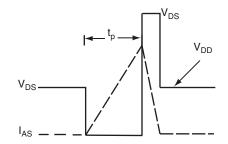


Fig. 12b - Unclamped Inductive Waveforms

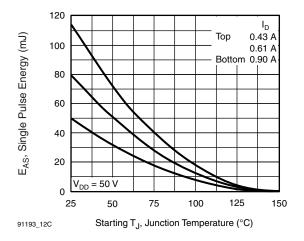


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

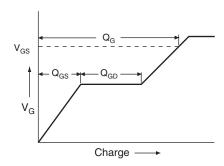


Fig. 13a - Basic Gate Charge Waveform

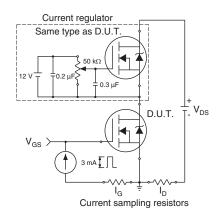
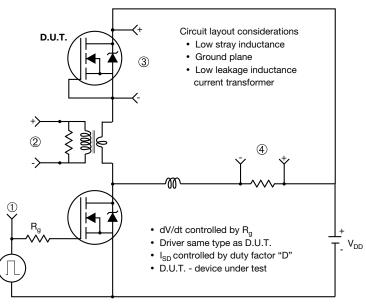


Fig. 13b - Gate Charge Test Circuit



Peak Diode Recovery dV/dt Test Circuit



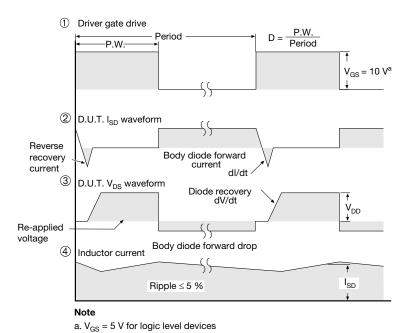
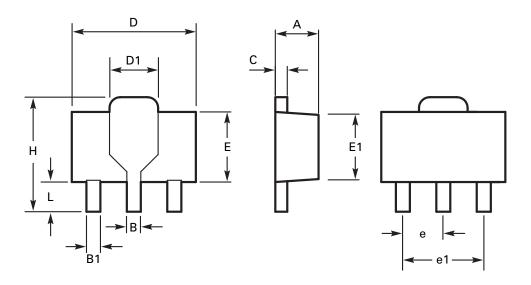


Fig. 14 - For N-Channel



Package outline - SOT89



DIM	Millin	neters	Inc	hes	DIM	Millimeters		Inches	
	Min	Max	Min	Max		Min	Max	Min	Max
Α	1.40	1.60	0.550	0.630	Е	2.29	2.60	0.090	0.102
В	0.44	0.56	0.017	0.022	E1	2.13	2.29	0.084	0.090
B1	0.36	0.48	0.014	0.019	е	1.50 BSC		0.059 BSC	
С	0.35	0.44	0.014	0.017	e1	3.00 BSC		0.118 BSC	
D	4.40	4.60	0.173	0.181	Н	3.94	4.25	0.155	0.167
D1	1.62	1.83	0.064	0.072	L	0.89	1.20	0.035	0.047

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches

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