

# K2151-VB Datasheet P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY								
V <sub>DS</sub> (V)	$R_{DS(on)}$ ( $\Omega$ )	I <sub>D</sub> (A) <sup>d</sup>	Q <sub>g</sub> (Typ.)					
- 30	0.050 at V <sub>GS</sub> = - 10 V	- 7.6	13 nC					
	0.056 at V <sub>GS</sub> = - 4.5 V	- 6.0	13110					

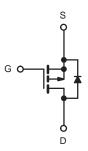
#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- Trench Power MOSFET
- 100 % R<sub>g</sub> Tested



#### **APPLICATIONS**

- Load Switch
- · Battery Switch



P-Channel MOSFET

	D	<u> </u>
G	D	S

ABSOLUTE MAXIMUM RATINGS T	A = 25 °C, unless other	erwise noted		
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	- 30	V	
Gate-Source Voltage		V <sub>GS</sub>	± 20	V
	T <sub>C</sub> = 25 °C		- 7.6	
Continuous Drain Current (T <sub>.1</sub> = 150 °C)	T <sub>C</sub> = 70 °C	1 , —	- 5.8	
Continuous Diam Current (1) = 150 °C)	T <sub>A</sub> = 25 °C	l <sub>D</sub>	- 6.0 <sup>a, b</sup>	
	T <sub>A</sub> = 70 °C		- 5.2 <sup>a, b</sup>	Α
Pulsed Drain Current	I <sub>DM</sub>	- 35		
Continuous Course Dusin Diada Courset	T <sub>C</sub> = 25 °C		- 3.5	
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	- I <sub>S</sub> -	- 2.1 <sup>a, b</sup>	
	T <sub>C</sub> = 25 °C		6.5	
Manianum Danian Disain ation	T <sub>C</sub> = 70 °C	D .	3.5	14/
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2.5 <sup>a, b</sup>	W
	T <sub>A</sub> = 70 °C	1	1.6 <sup>a, b</sup>	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stq</sub>	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS								
Parameter	Symbol	Typical	Maximum	Unit				
Maximum Junction-to-Ambient <sup>a, c</sup>	t ≤ 10 s	R <sub>thJA</sub>	40	50	°C/W			
Maximum Junction-to-Foot	Steady State	R <sub>thJF</sub>	24	30	C/VV			

#### Notes

- a. Surface mounted on 1" x 1" FR4 board.
- b. t = 10 s.
- c. Maximum under Steady State conditions is 95 °C/W.
- d. Package limited.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static			ı			
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V, } I_{D} = -250 \mu\text{A}$	- 30			V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	In = - 250 µA		- 31		mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = - 250 μA		4.5		
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.0		- 2.5	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zana Cata Valtana Brain Commant	1	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$			- 1	μΑ
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 5	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le -5 \text{ V}, V_{GS} = -10 \text{ V}$	- 20			Α
	D	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 7.0 A		0.050		Ω
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 5.6 A		0.056		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 7.0 A		18		S
Dynamic <sup>b</sup>			ı			
Input Capacitance	C <sub>iss</sub>			1355		
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		180		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			145		1
Total Cata Charra	0	$V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -7.0 \text{ A}$		25	38	nC
Total Gate Charge	Q <sub>g</sub> V <sub>DS</sub> = - 13 v, v <sub>GS</sub> = - 10 v, 1 <sub>E</sub>			13	20	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -7.0 \text{ A}$		3.5		
Gate-Drain Charge	Q <sub>gd</sub>			5.5		
Gate Resistance	R <sub>g</sub>	f = 1 MHz	0.4	2.0	4.0	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			10	20	
Rise Time	t <sub>r</sub>	$V_{DD} = -15 \text{ V}, R_{L} = 2.7 \Omega$		13	20	1
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D \cong$ - 5.6 A, $V_{GEN}$ = - 10 V, $R_g$ = 1 $\Omega$		23	35	1
Fall Time	t <sub>f</sub>			9	18	
Turn-On Delay Time	t <sub>d(on)</sub>			38	57	ns
Rise Time	t <sub>r</sub>	$V_{DD} = -15 \text{ V}, R_{L} = 2.7 \Omega$		89	134	1
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_{D} \cong -5.6 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_{g} = 1 \Omega$		22	33	
Fall Time	t <sub>f</sub>			11	17	1
<b>Drain-Source Body Diode Characteris</b>	tics					
Continous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 6.5	^
Pulse Diode Forward Current	I <sub>SM</sub>				- 30	A
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 5.6 A, V <sub>GS</sub> = 0 V		- 0.71	- 1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>			22	33	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	L ECA di/dt 400 A/:- T 0500		17	26	nC
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = -5.6 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		13		ns
Reverse Recovery Rise Time	t <sub>b</sub>			9		

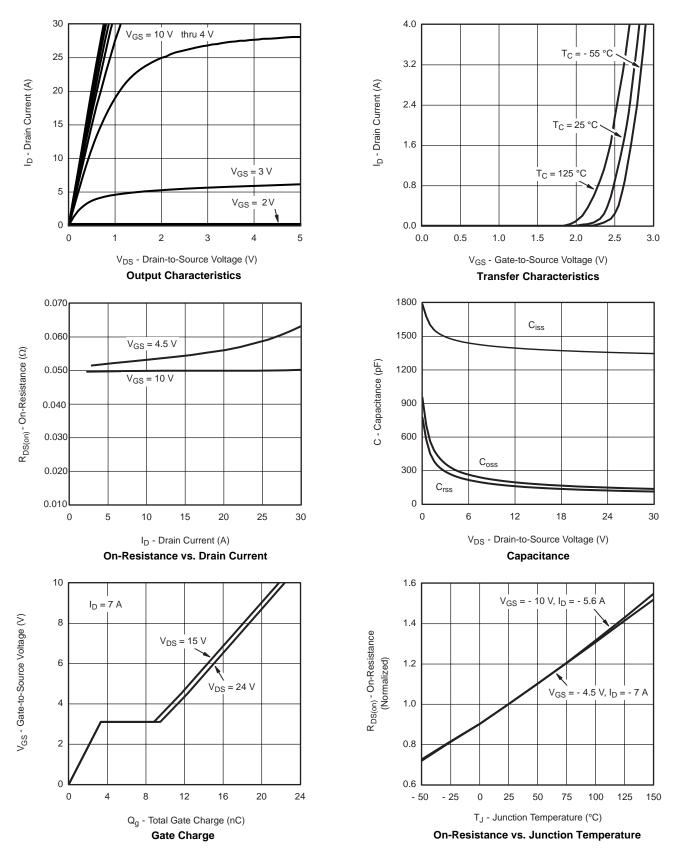
#### Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

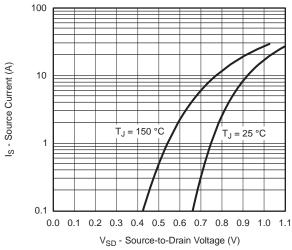
a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$ 

b. Guaranteed by design, not subject to production testing.

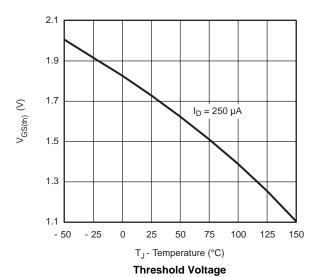








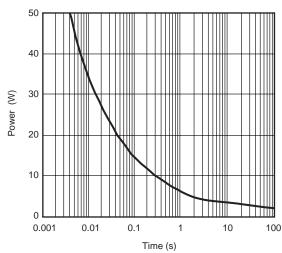
#### Source-Drain Diode Forward Voltage



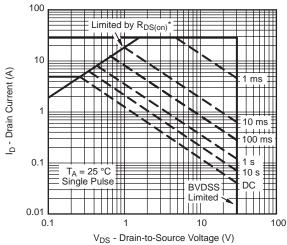
 $C_{J} = 7.000$   $C_{J} = 7.00$ 

V<sub>GS</sub> - Gate-to-Source Voltage (V)

On-Resistance vs. Gate-to-Source Voltage



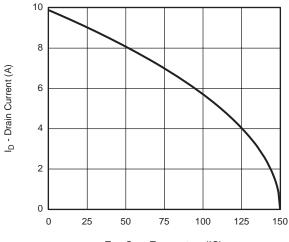
Single Pulse Power, Junction-to-Ambient



 $^{\star}$  VGS > minimum VGS at which RDS(on) is specified

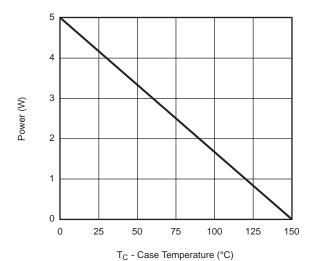
Safe Operating Area



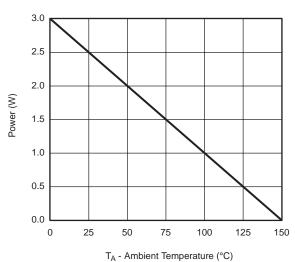


 $T_C$  - Case Temperature (°C)





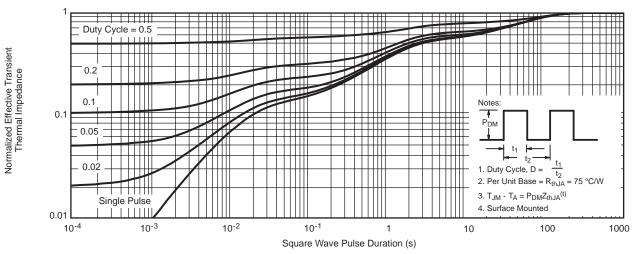
Power, Junction-to-Foot



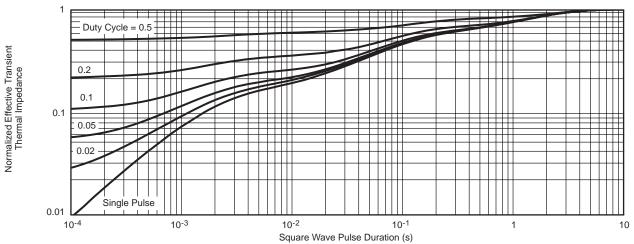
Power Derating, Junction-to-Ambient

<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





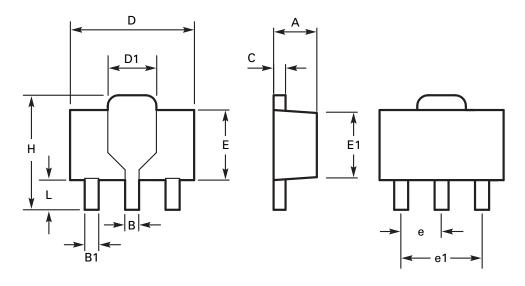
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot



## Package outline - SOT89



DIM	Millin	neters	Inc	hes	DIM	Millimeters		Inches	
	Min	Max	Min	Max		Min	Max	Min	Мах
Α	1.40	1.60	0.550	0.630	Е	2.29	2.60	0.090	0.102
В	0.44	0.56	0.017	0.022	E1	2.13	2.29	0.084	0.090
B1	0.36	0.48	0.014	0.019	е	1.50 BSC		0.059 BSC	
С	0.35	0.44	0.014	0.017	e1	3.00 BSC		0.118 BSC	
D	4.40	4.60	0.173	0.181	Н	3.94	4.25	0.155	0.167
D1	1.62	1.83	0.064	0.072	L	0.89	1.20	0.035	0.047

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches

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