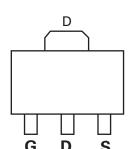
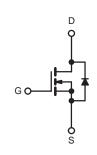


# **BSP108-VB Datasheet** N-Channel 100 V (D-S) MOSFET

MOSFET PRODUCT SUMMARY							
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$ Typ.	Q <sub>g</sub> (Typ.)					
	0.102 at V <sub>GS</sub> = 10 V	4.2					
100	0.120 at V <sub>GS</sub> = 6 V	3.8	2.9 nC				
	0.125 at V <sub>GS</sub> = 4.5 V	3.6					





N-Channel MOSFET

### **FEATURES**

- Trench Power MOSFET
  100 % R<sub>g</sub> and UIS Tested



#### **APPLICATIONS**

- DC/DC Converters / Boost Converters
- Load Switch
- LED Backlighting in LCD TVs
- · Power Management for Mobile Computing

ABSOLUTE MAXIMUM RATINGS (T	$_{A}$ = 25 °C, unless oth	nerwise noted)			
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V <sub>DS</sub>	100	V		
Gate-Source Voltage	$V_{GS}$	± 20	, v		
	T <sub>C</sub> = 25 °C		4.2		
Continuous Drain Current (T <sub>.I</sub> = 150 °C)	T <sub>C</sub> = 70 °C	I <sub>D</sub>	3.5		
Continuous Brain Guirent (1) = 100 °C)	T <sub>A</sub> = 25 °C	טי	3.2 <sup>b,c</sup>		
	T <sub>A</sub> = 70 °C		2.8 <sup>b,c</sup>	$\Box$ A	
Pulsed Drain Current (t = 300 μs)		I <sub>DM</sub>	15	7	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	l <sub>a</sub>	2.1		
Continuous Source-Diain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	1 <sup>b, c</sup>		
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	3		
Single Pulse Avalanche Energy	L = 0.1 IIII	E <sub>AS</sub>	0.45	mJ	
	T <sub>C</sub> = 25 °C		2.5		
Maximum Power Dissipation	T <sub>C</sub> = 70 °C	P <sub>D</sub>	1.6	w	
Maximum Fower Dissipation	T <sub>A</sub> = 25 °C		1.25 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		0.8 <sup>b, c</sup>	7	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS								
Parameter	Symbol	Typical	Maximum	Unit				
Maximum Junction-to-Ambient <sup>b, d</sup>	≤ 5 s	R <sub>thJA</sub>	75	100	°C/W			
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>th,IF</sub>	40	50	7 0/11			

#### Notes:

- a. Based on  $T_C$  = 25 °C.
- b. Surface mounted on 1" x 1" FR4 board.
- d. Maximum under steady state conditions is 166 °C/W.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static					L	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V, } I_D = 250  \mu\text{A}$	100			V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$			59		\//0/
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA		- 4.8		mV/°C
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.2		3	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zana Cata Valtana Brain Commant		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V			- 1	μΑ
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			- 10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	5			Α
		$V_{GS} = 10 \text{ V}, I_{D} = 2 \text{ A}$		0.102		Ω
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 6 \text{ V}, I_{D} = 1 \text{ A}$		0.120		
		$V_{GS} = 4.5 \text{ V}, I_D = 1 \text{ A}$		0.125		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 20 \text{ V}, I_{D} = 2 \text{ A}$		5		S
Dynamic <sup>b</sup>	<u> </u>					<u> </u>
Input Capacitance	C <sub>iss</sub>			196		
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		67		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			14		
· ·		V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 2.2 A		5.2	10.4	1
Total Gate Charge	Qg	20 00 2		2.9	5.8	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 50 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 2.2 \text{ A}$		1		
Gate-Drain Charge	Q <sub>gd</sub>			1.4		
Gate Resistance	R <sub>g</sub> f = 1 MHz		0.9	4.3	8.6	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			40	60	
Rise Time	t <sub>r</sub>	$V_{DD} = 50 \text{ V}, R_{L} = 27.7 \Omega$		68	102	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D = 1.8 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		14	21	
Fall Time	t <sub>f</sub>	•		20	30	
Turn-On Delay Time	t <sub>d(on)</sub>			8	16	ns
Rise Time	t <sub>r</sub>	$V_{DD} = 50 \text{ V}, R_{1} = 27.7 \Omega$		10	20	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D = 1.8 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		10	20	1
Fall Time				7	14	1
<b>Drain-Source Body Diode Characteristi</b>	cs				l	.I.
Continuous Source-Drain Diode Current	Is	T <sub>C</sub> = 25 °C			- 2.1	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				- 8	A
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 1.8 A		- 0.8	- 1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>			23	35	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	$I_F = 1.8 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s},$		21	32	nC
Reverse Recovery Fall Time	t <sub>a</sub>	T <sub>J</sub> = 25 °C		17		
Reverse Recovery Rise Time	t <sub>b</sub>			6		ns

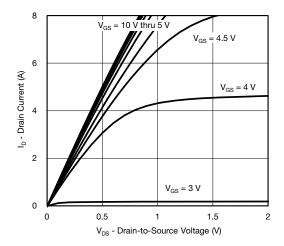
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

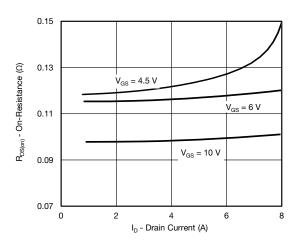
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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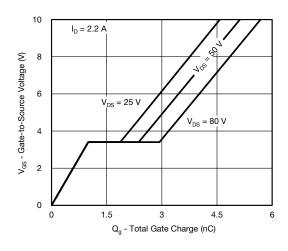




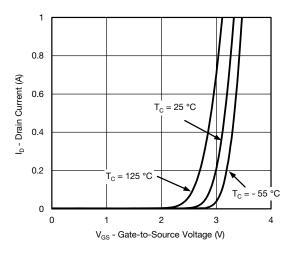
#### **Output Characteristics**



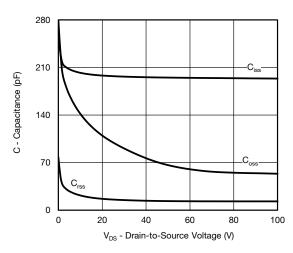
On-Resistance vs. Drain Current and Gate Voltage



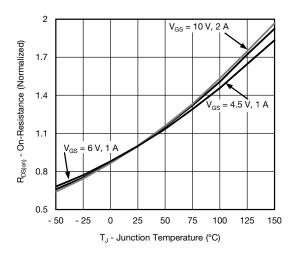
**Gate Charge** 



**Transfer Characteristics** 

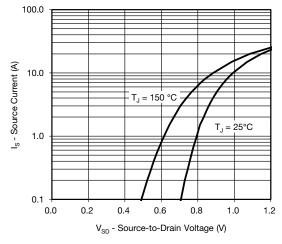


Capacitance

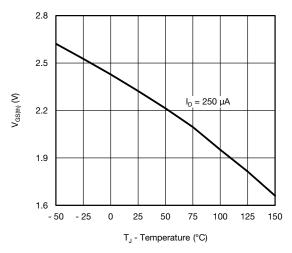


On-Resistance vs. Junction Temperature

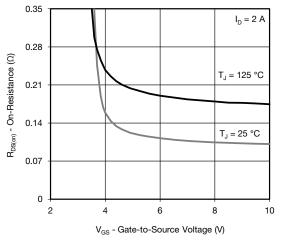




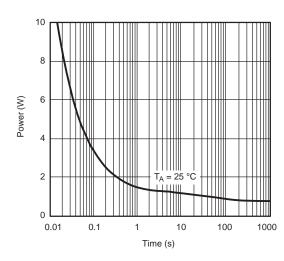
### Source-Drain Diode Forward Voltage



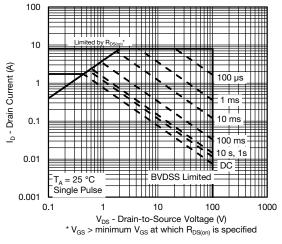
**Threshold Voltage** 



On-Resistance vs. Gate-to-Source Voltage

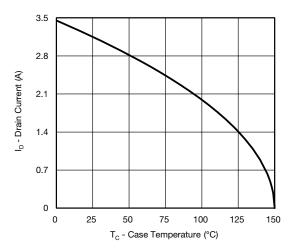


Single Pulse Power

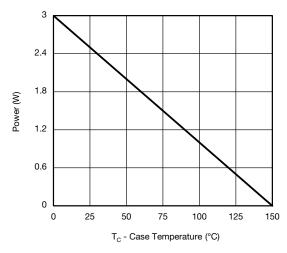


Safe Operating Area

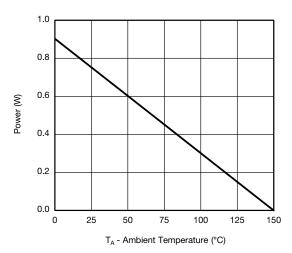




#### **Current Derating\***





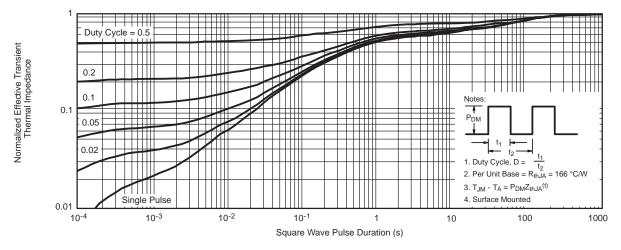


Power, Junction-to-Ambient

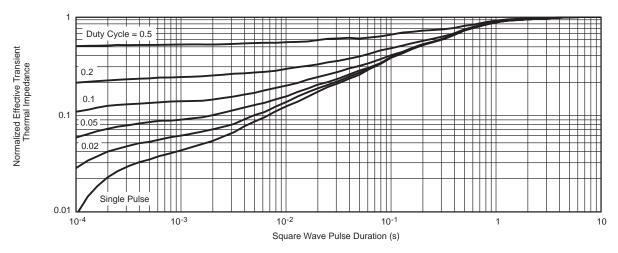
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<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max.)}$  = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





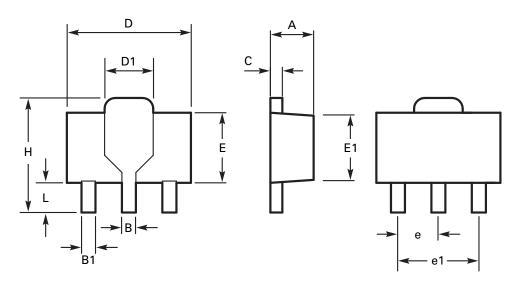
#### Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot



# Package outline - SOT89



DIM	Millin	neters	ters Inches DIM Millimeters		Inches DIM Millimeters		Inches		
	Min	Max	Min	Max		Min	Max	Min	Max
Α	1.40	1.60	0.550	0.630	Е	2.29	2.60	0.090	0.102
В	0.44	0.56	0.017	0.022	E1	2.13	2.29	0.084	0.090
B1	0.36	0.48	0.014	0.019	е	1.50 BSC		0.059 BSC	
С	0.35	0.44	0.014	0.017	e1	3.00 BSC		0.118 BSC	
D	4.40	4.60	0.173	0.181	Н	3.94	4.25	0.155	0.167
D1	1.62	1.83	0.064	0.072	L	0.89	1.20	0.035	0.047

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches

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