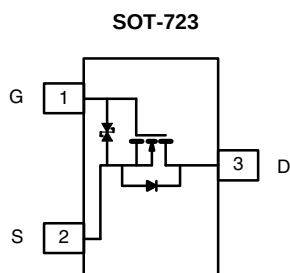


K3547-VB Datasheet

N-Channel60V(D-S)MOSFET

PRODUCT SUMMARY

$V_{DS(min.)}$ (V)	$R_{DS(on)}$ (Ω)	$V_{GS(th)}$ (V)	I_D (mA)
60	1.25 at $V_{GS} = 10$ V	1 to 2.5	330



FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- Trench Power MOSFETs
- Low On-Resistance: 1.25 Ω
- Low Threshold: 2.5 V
- Low Input Capacitance: 30 pF
- Fast Switching Speed: 25 ns
- Low Input and Output Leakage
- Miniature Package
- ESD Protected: 2000 V
- Compliant to RoHS Directive 2002/95/EC



RoHS
COMPLIANT
HALOGEN
FREE

APPLICATIONS

- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.
- Battery Operated Systems
- Solid State Relays

BENEFITS

- Low Offset Voltage
- Low-Voltage Operation
- High-Speed Circuits
- Low Error Voltage
- Small Board Area

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ^a	$T_A = 25^\circ\text{C}$	I_D 330	mA
	$T_A = 85^\circ\text{C}$	240	
Pulsed Drain Current ^a	I_{DM}	650	
Power Dissipation ^a	$T_A = 25^\circ\text{C}$	P_D 250	mW
	$T_A = 85^\circ\text{C}$	130	
Thermal Resistance, Maximum Junction-to-Ambient ^a	R_{thJA}	500	$^\circ\text{C/W}$
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	$^\circ\text{C}$

Notes:

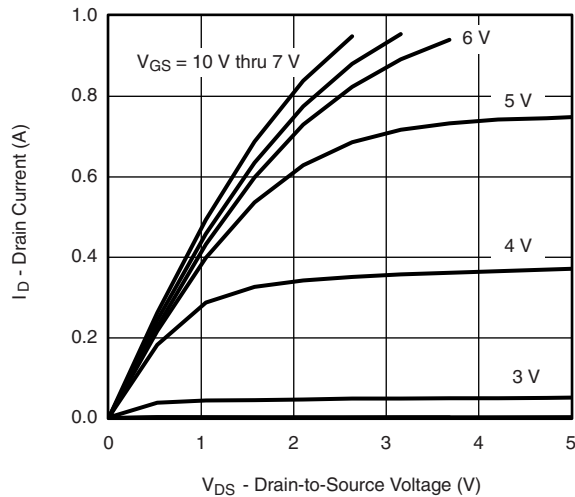
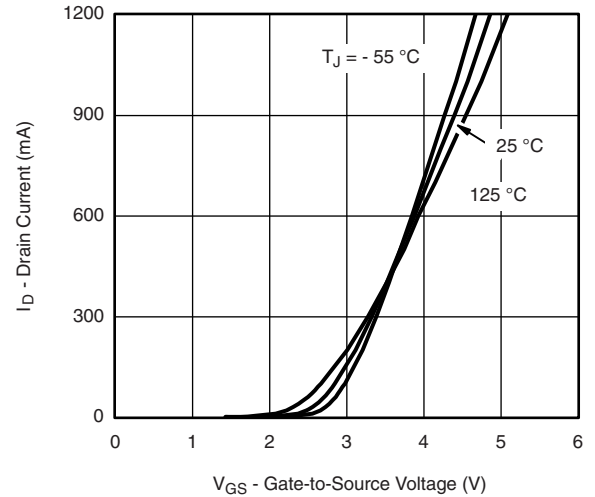
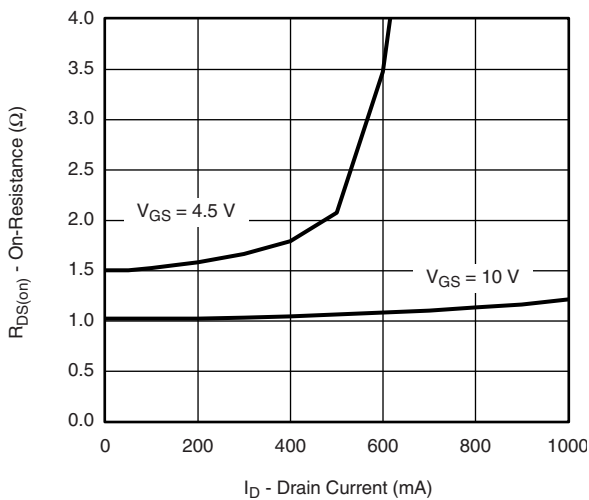
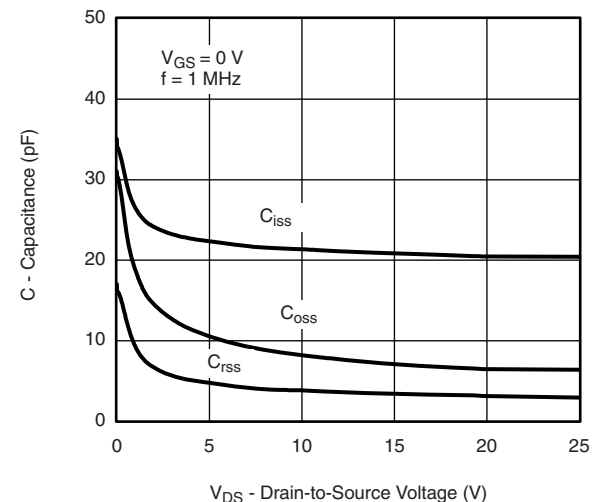
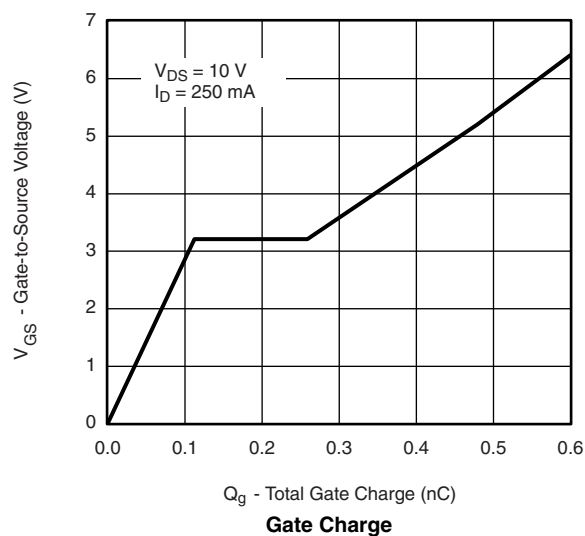
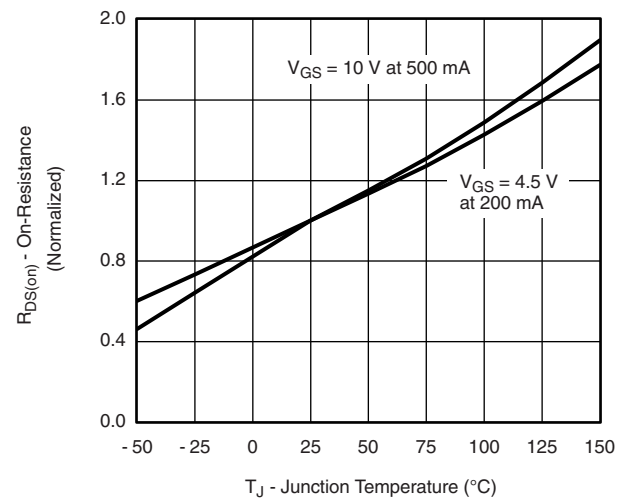
a. Surface mounted on FR4 board, power applied for $t \leq 10$ s.

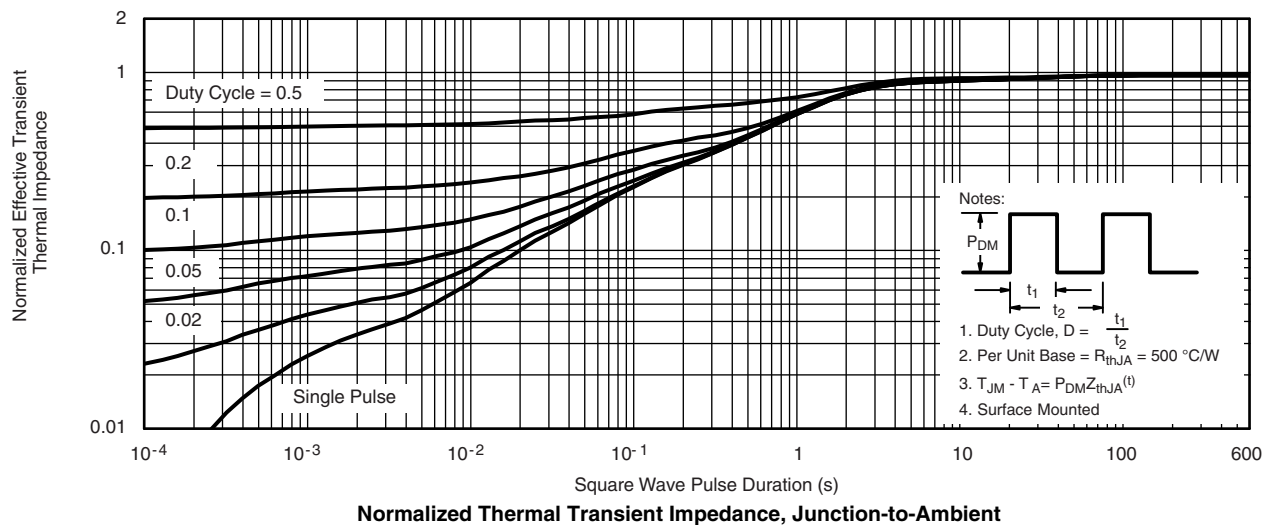
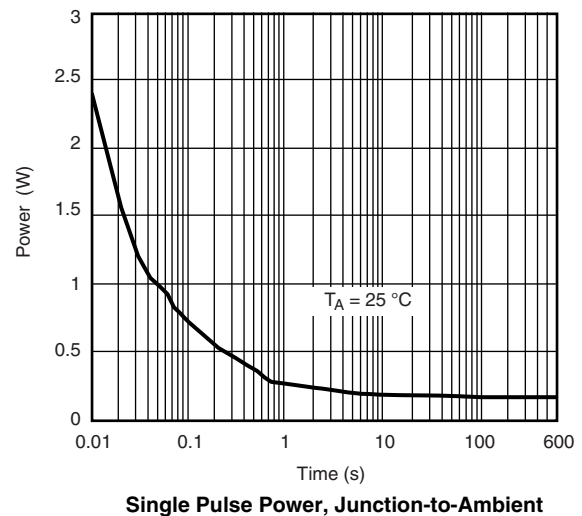
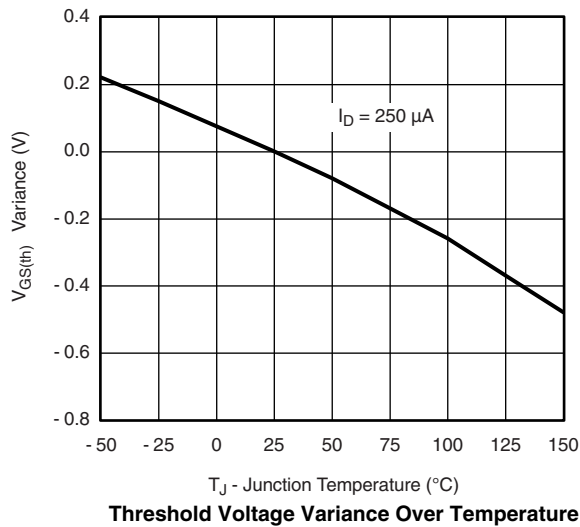
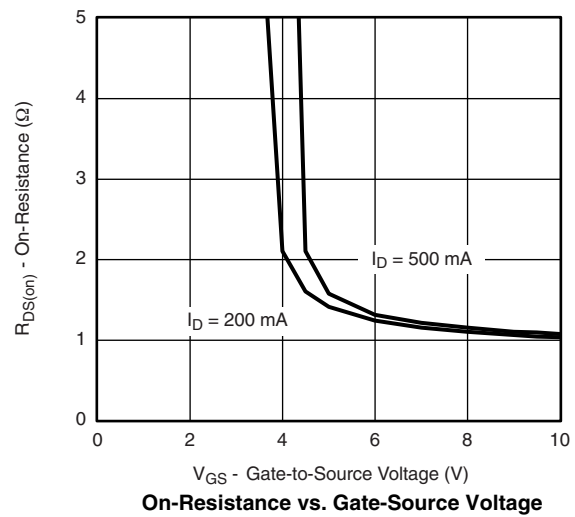
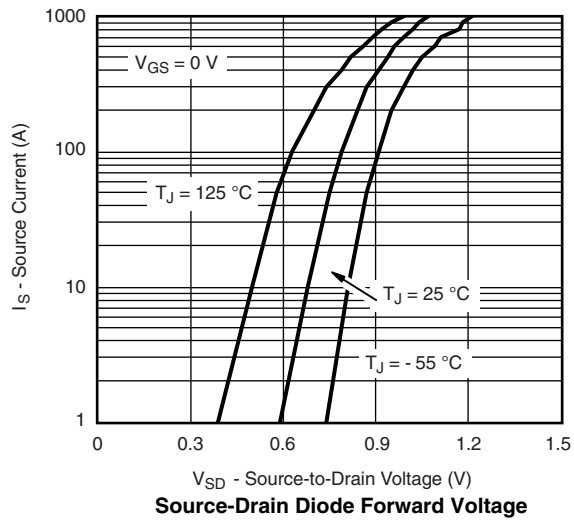
SPECIFICATIONS ($T_J = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}$, $I_D = 10\text{ }\mu\text{A}$	60			V
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 0.25\text{ mA}$	1		2.5	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{ V}$, $V_{GS} = \pm 10\text{ V}$			± 150	nA
		$T_J = 85\text{ }^{\circ}\text{C}$			± 500	
		$V_{DS} = 0\text{ V}$, $V_{GS} = \pm 5\text{ V}$			± 20	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 50\text{ V}$, $V_{GS} = 0\text{ V}$			10	
		$T_J = 85\text{ }^{\circ}\text{C}$			100	
		$V_{DS} = 60\text{ V}$, $V_{GS} = 0\text{ V}$			1	μA
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} = 10\text{ V}$, $V_{GS} = 4.5\text{ V}$	500			mA
		$V_{DS} = 7.5\text{ V}$, $V_{GS} = 10\text{ V}$	800			
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 4.5\text{ V}$, $I_D = 200\text{ mA}$		3.0		Ω
		$T_J = 125\text{ }^{\circ}\text{C}$		5.0		
		$V_{GS} = 10\text{ V}$, $I_D = 500\text{ mA}$		1.25		
		$T_J = 125\text{ }^{\circ}\text{C}$		2.25		
Forward Transconductance ^a	g_{fs}	$V_{DS} = 10\text{ V}$, $I_D = 200\text{ mA}$	100			mS
Diode Forward Voltage ^a	V_{SD}	$V_{GS} = 0\text{ V}$, $I_S = 200\text{ mA}$			1.3	V
Dynamic^b						
Input Capacitance	C_{iss}	$V_{DS} = 25\text{ V}$, $V_{GS} = 0\text{ V}$, $f = 1\text{ MHz}$		30		pF
Output Capacitance	C_{oss}			6		
Reverse Transfer Capacitance	C_{rss}			2.5		
Gate Charge	Q_g	$V_{DS} = 10\text{ V}$, $I_D = 250\text{ mA}$, $V_{GS} = 4.5\text{ V}$			0.6	nC
Switching^{b, c}						
Turn-On Time	$t_{(on)}$	$V_{DD} = 30\text{ V}$, $R_L = 150\text{ }\Omega$, $I_D = 200\text{ mA}$, $V_{GEN} = 10\text{ V}$, $R_g = 10\text{ }\Omega$			25	ns
Turn-Off Time	$t_{(off)}$				35	

Notes:

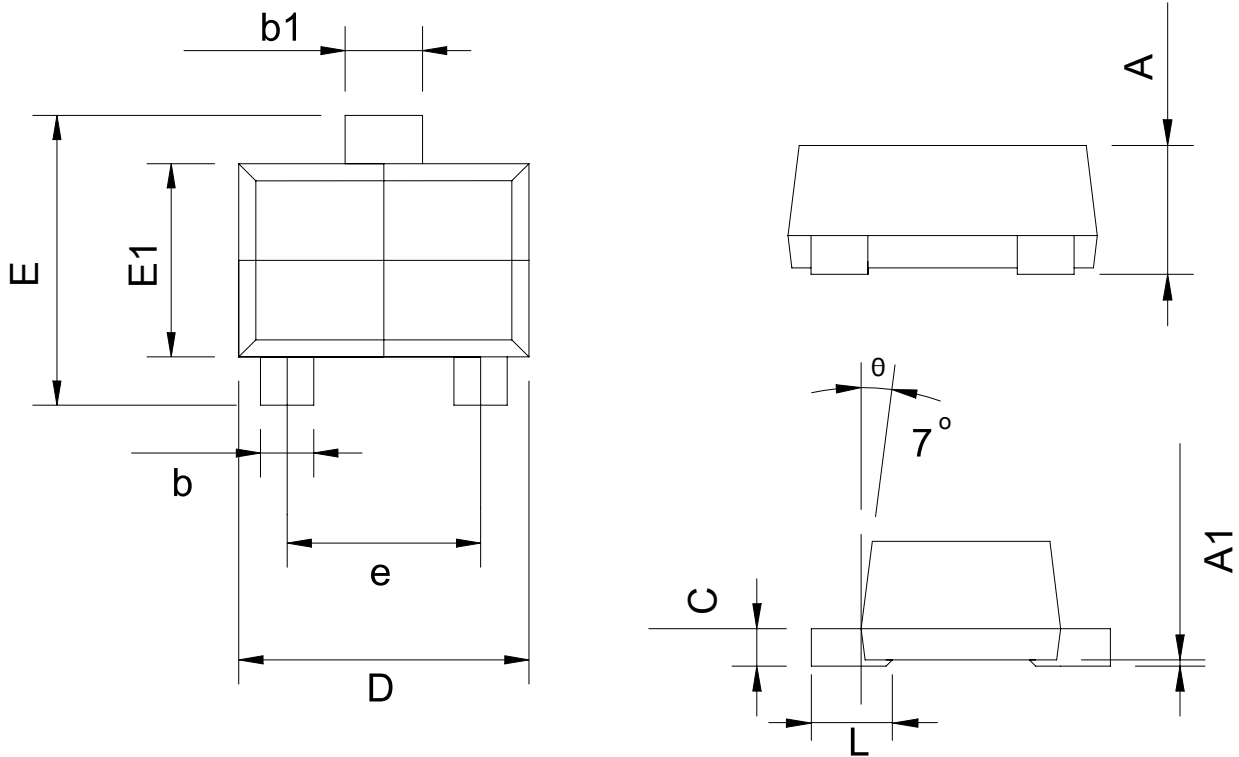
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
 b. For DESIGN AID ONLY, not subject to production testing.
 c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)

Output Characteristics

Transfer Characteristics

On-Resistance vs. Drain Current

Capacitance

Gate Charge

On-Resistance vs. Junction Temperature

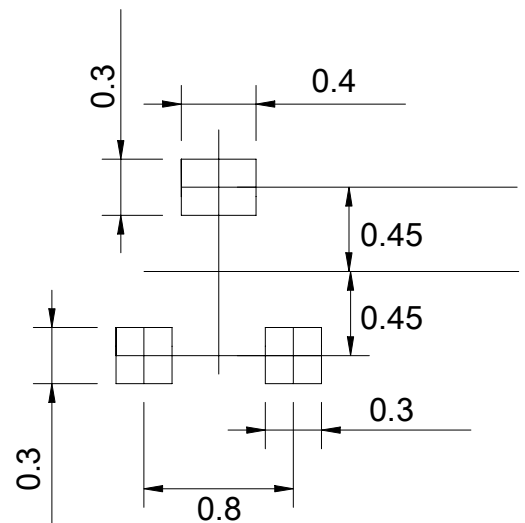
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)


SOT-723: 3 Leads



SYMBOL	SOT-723			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	-	0.500	-	0.020
A1	0.000	0.050	0.000	0.002
b	0.170	0.270	0.007	0.011
b1	0.270	0.370	0.011	0.015
c	-	0.150	-	0.006
D	1.150	1.250	0.045	0.049
E	1.150	1.250	0.045	0.049
E1	0.750	0.850	0.030	0.033
e	0.800 TYP.		0.031 TYP.	
L	0.32 BSC		0.013 BSC	
-	° REF.		° REF.	

RECOMMENDED LAND PATTERN



UNIT: mm

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