

AM3933P-T1-PF-VB Datasheet

Dual P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A) ^a	Q _g (Typ.)			
- 20	$0.075 \text{ at V}_{GS} = -4.5 \text{V}$	- 4.0	2.7 nC			
	0.100 at V _{GS} = - 2.5 V	- 3.2	2.7 110			

FEATURES

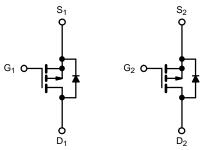
- Halogen-free According to IEC 61249-2-21 Definition
- Trench Power MOSFET
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC



ROHS COMPLIANT HALOGEN FREE

APPLICATIONS

- · Load Switch for Portable Applications
- · Battery Switch for Portable Devices
- Computers
 - Bus Switch
 - Load Switch



P-Channel MOSFET

P-Channel MOSFET

ABSOLUTE MAXIMUM RATIN	GS (T _A = 25 °C	, unless oth	erwise noted)		
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	- 20	V	
Gate-Source Voltage		V_{GS}	± 12		
	T _C = 25 °C		- 4.0		
Continuous Drain Current (T _{.1} = 150 °C)	T _C = 70 °C		- 3.3		
Continuous Diain Current (1) = 130 °C)	T _A = 25 °C	- I _D	- 3.6 ^{b, c}		
	T _A = 70 °C		-3.1 ^{b, c}	A	
Pulsed Drain Current		I _{DM}	- 12		
	T _C = 25 °C		- 1.17		
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	- 0.95 ^{b, c}		
	T _C = 25 °C		1.4		
Maximum Power Dissipation	T _C = 70 °C	P _D	0.9	W	
	T _A = 25 °C	- FD	1.14 ^{b, c}		
	T _A = 70 °C		0.73 ^{b, c}		
Operating Junction and Storage Temperature	Range	T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS								
Parameter	Symbol	Typical	Maximum	Unit				
Maximum Junction-to-Ambient ^{b, d}	t ≤ 5 s	R _{thJA}	93	110	°C/W			
Maximum Junction-to-Foot	Steady State	R _{thJF}	75	90	C/VV			

Notes:

- a. $T_C = 25 \,^{\circ}C$.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s
- d. Maximum under steady state conditions is 150 °C/W.



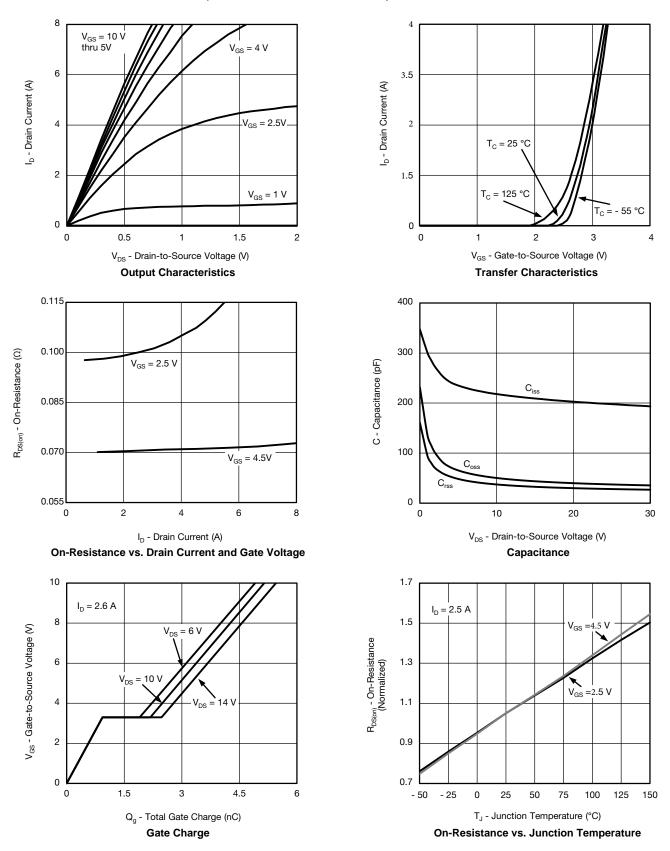
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static	•					
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 20			V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = - 250 μA		- 17		mV/°C
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_{J}$	I _D = - 250 μA		3.5		
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$	- 0.5		- 2.0	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 100	nA
7 0	I _{DSS}	V _{DS} = - 20 V, V _{GS} = 0 V			1	μA
Zero Gate Voltage Drain Current		V _{DS} = - 20 V, V _{GS} = 0 V, T _J = 55 °C			10	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{V}$	- 8			Α
	Б	V _{GS} = - 4.5V, I _D = - 2.5 A		0.075		Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 2.5 V, I _D = - 1 A		0.100		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 2.6 A		5		S
Dynamic ^b					L	
Input Capacitance	C _{iss}			210		
Output Capacitance	C _{oss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		45		pF
Reverse Transfer Capacitance	C _{rss}			33		
Total Gate Charge	Q _g	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -2.6 \text{ A}$		5.2	8	nC
				2.7	4	
Gate-Source Charge	Q _{gs}	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -2.6 \text{ A}$		0.94		
Gate-Drain Charge	Q_{gd}			1.3		
Gate Resistance	R _g	f = 1 MHz	2	7	14	Ω
Turn-On Delay Time	t _{d(on)}			39	59	
Rise Time	t _r	$V_{DD} = -10 \text{ V}, R_{L} = 7.1 \Omega$		25	38	- ns
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ - 2.1 A, V_{GEN} = - 4.5 V, R_g = 1 Ω		13	20	
Fall Time	t _f			9	18	
Turn-On Delay Time	t _{d(on)}			5	10	
Rise Time	t _r	$V_{DD} = -10 \text{ V}, R_{L} = 7.1 \Omega$		10	20	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ - 2.1 A, V_{GEN} = - 4.5 V, R_g = 1 Ω		14	21	
Fall Time	t _f			7	14	
Drain-Source Body Diode Characteristi	cs				L	
Continuous Source-Drain Diode Current	I _S	$T_C = 25 ^{\circ}C$			1.17	۸
Pulse Diode Forward Current	I _{SM}				8	A
Body Diode Voltage	V_{SD}	I _S = - 2.1 A, V _{GS} = 0 V		0.85	1.2	V
Body Diode Reverse Recovery Time	t _{rr}			13	20	ns
Body Diode Reverse Recovery Charge	Q _{rr}	L = 2.4 A dl/dt = 400 A/v = T = 25 °C		6	12	nC
Reverse Recovery Fall Time	t _a	$I_F = -2.1 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		9		
Reverse Recovery Rise Time	th	t _b		4		ns

Notes:

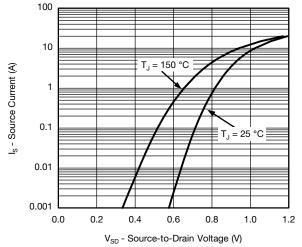
- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

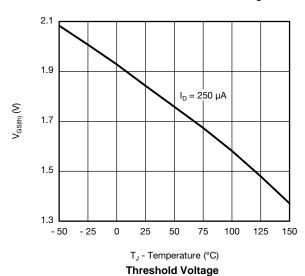


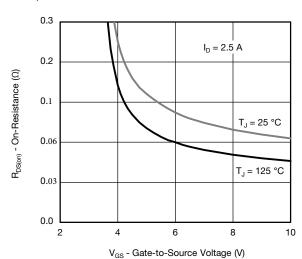




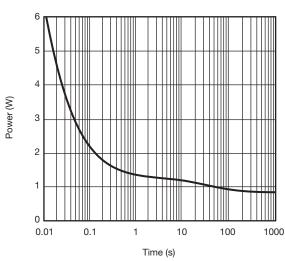


Source-Drain Diode Forward Voltage

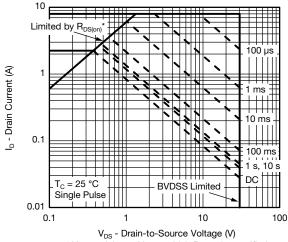




On-Resistance vs. Gate-to-Source Voltage



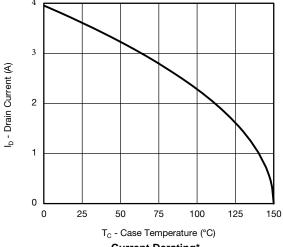
Single Pulse Power (Junction-to-Ambient)



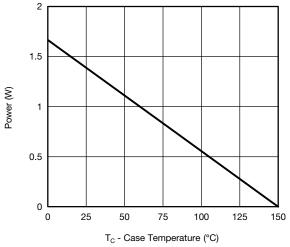
* V_{DS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Ambient

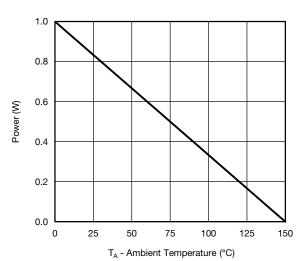




Current Derating*



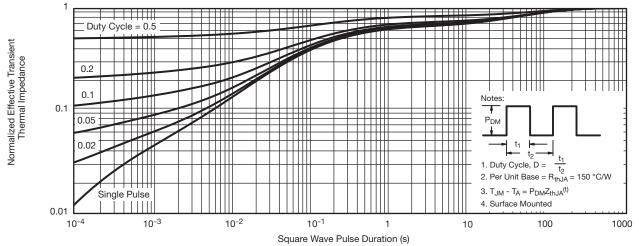




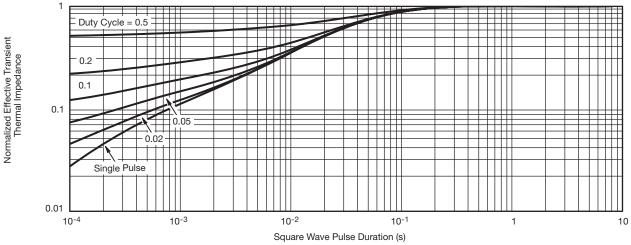
Power Derating, Junction-to-Ambient

^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





Normalized Thermal Transient Impedance, Junction-to-Ambient

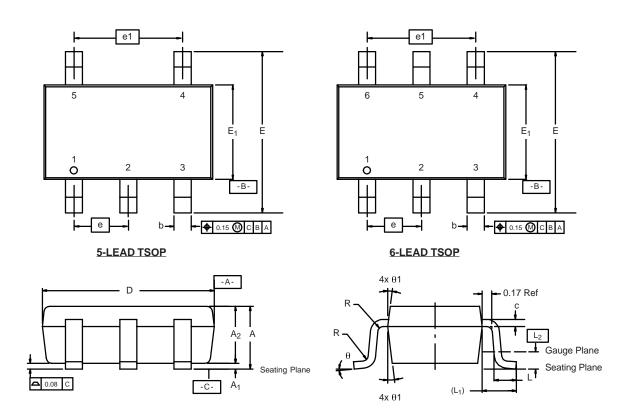


Normalized Thermal Transient Impedance, Junction-to-Foot



TSOP: 5/6-LEAD

JEDEC Part Number: MO-193C

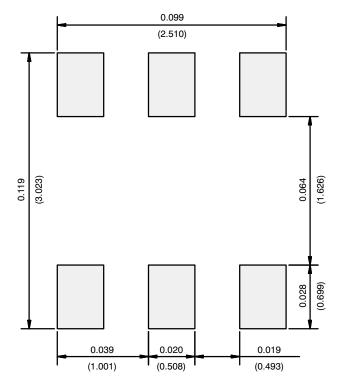


	MILLIMETERS			INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.91	-	1.10	0.036	-	0.043	
A ₁	0.01	-	0.10	0.0004	-	0.004	
A ₂	0.90	-	1.00	0.035	0.038	0.039	
b	0.30	0.32	0.45	0.012	0.013	0.018	
С	0.10	0.15	0.20	0.004	0.006	0.008	
D	2.95	3.05	3.10	0.116	0.120	0.122	
Е	2.70	2.85	2.98	0.106	0.112	0.117	
E ₁	1.55	1.65	1.70	0.061	0.065	0.067	
е	0.95 BSC			0.0374 BSC			
e ₁	1.80	1.90	2.00	0.071	0.075	0.079	
L	0.32	-	0.50	0.012	-	0.020	
L ₁		0.60 Ref		0.024 Ref			
L ₂	0.25 BSC			0.010 BSC			
R	0.10	-	-	0.004	-	-	
θ	0°	4°	8°	0°	4°	8°	
θ_1	7° Nom			7° Nom			
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540							

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RECOMMENDED MINIMUM PADS FOR TSOP-6



Recommended Minimum Pads Dimensions in Inches/(mm)



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