

WST2339-VB Datasheet P-Channel 12-V (D-S) MOSFET

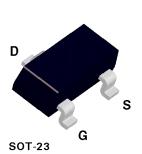
PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)	Q _g (Typ.)		
	$0.018 \text{ at V}_{GS} = -4.5 \text{ V}$	- 6 ^a			
- 12	0.021 at V _{GS} = - 2.5 V	- 6 ^a	20 nC		
	0.040 at V _{GS} = - 1.8 V	- 4			

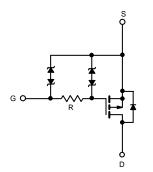
FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- Trench Power MOSFET
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC









P-Channel MOSFET

APPLICATIONS

- Portable Devices
 - Load Switch
 - Battery Switch
 - Charger Switch

Parameter	Symbol	Limit	Unit V	
Drain-Source Voltage	V_{DS}	- 12		
Gate-Source Voltage	V_{GS}	± 12		
Continuous Drain Current (T _J = 150 °C)	T _C = 25 °C		- 6 ^a	
	$T_C = 70 ^{\circ}\text{C}$ $T_A = 25 ^{\circ}\text{C}$	I _D	- 6 ^a - 5 ^{b, c}	_
	T _A = 70 °C		- 4.1 ^{b, c}	А
Pulsed Drain Current	I _{DM}	- 50		
Continuous Source-Drain Diode Current	$T_C = 25 ^{\circ}\text{C}$ $T_A = 25 ^{\circ}\text{C}$	I _S	- 6 ^a - 2.9 ^{b, c}	
	T _C = 25 °C		19	
Maximum Power Dissipation	T _C = 70 °C	P _D	12	_ w
	T _A = 25 °C		3.5 ^{b, c}	
	T _A = 70 °C		2.2 ^{b, c}	
Operating Junction and Storage Temperature R	T _J , T _{stg}	- 55 to 150	°C	
Soldering Recommendations (Peak Temperatur		260		

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{b, e}	t ≤ 5 s	R _{thJA}	28	36	°C/W		
Maximum Junction-to-Case (Drain)	Steady State	R_{thJC}	5.3	6.5	O/ VV		

Notes:

- a. Package limited.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 5 s.
- d. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.
- e. Maximum under Steady State conditions is 80 °C/W.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 12			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Γ _J I _D = - 250 μA		- 12		m\//ºC	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	1D = - 250 μΑ		3		mV/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$	- 0.5		- 1.2	V	
Gate-Source Leakage	,	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 20		
	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$			± 0.5	.5	
Zana Oata Valla va Basia Oama i	I _{DSS}	V _{DS} = - 20 V, V _{GS} = 0 V			- 1	μA	
Zero Gate Voltage Drain Current		V _{DS} = - 20 V, V _{GS} = 0 V, T _J = 55 °C			- 10	7	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 20			Α	
		V _{GS} = - 4.5 V, I _D = - 5.6 A		0.018			
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -2.5 \text{ V}, I_D = -5.3 \text{ A}$		0.021		Ω	
		V _{GS} = - 1.8 V, I _D = - 2.5 A		0.040			
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 5.6 A		35		S	
Dynamic ^b							
Total Gate Charge		V _{DS} = - 10 V, V _{GS} = - 8 V, I _D = - 5 A		50	75		
	Q _g			20	30	nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -5 \text{ A}$		3.3			
Gate-Drain Charge	Q_{gd}			8.4		1	
Gate Resistance	R_{g}	f = 1 MHz	0.2	1	2	kΩ	
Turn-On Delay Time	t _{d(on)}			0.71	1.1		
Rise Time	t _r	V_{DD} = - 10 V, R_L = 1 Ω		1.7	2.6		
Turn-Off Delay Time		$I_D \cong$ - 5 A, V_{GEN} = - 4.5 V, R_g = 1		6	9		
Fall Time	t _f	Ω		3.2	5	Ī	
Turn-On Delay Time	t _{d(on)}			0.3	0.45	us	
Rise Time	t_r $V_{DD} = -10 \text{ V}, R_L = 1 \Omega$			0.6	0.9]	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ - 5 A, $V_{GEN} =$ - 10 V, $R_g = 1$		10	15		
Fall Time	$t_{\rm f}$ Ω			3.5	5.5	1	
Drain-Source Body Diode Characterist	ics			•		•	
Continuous Source-Drain Diode Current	I _S	$T_C = 25 ^{\circ}C$			- 6	Α	
Pulse Diode Forward Current	I _{SM}				- 50	_ ^	
Body Diode Voltage	V_{SD}	I _S = - 5 A, V _{GS} = 0 V		- 0.85	- 1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			30	60	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	L = 6 A dl/dt = 100 A/vs T = 25 °C		20	40	nC	
Reverse Recovery Fall Time	t _a	$I_F = 6 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 \text{ °C}$		13		ns	
Reverse Recovery Rise Time	t _b			17			

Notes:

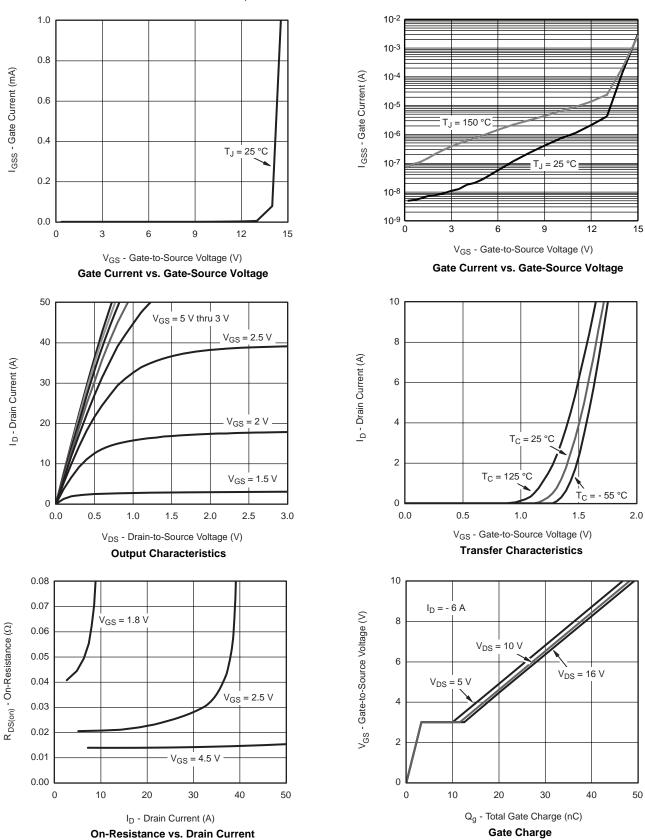
- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$ b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

服务热线:400-655-8788



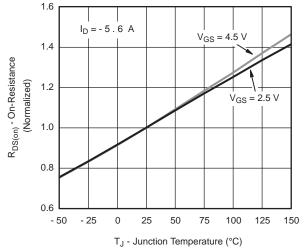
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



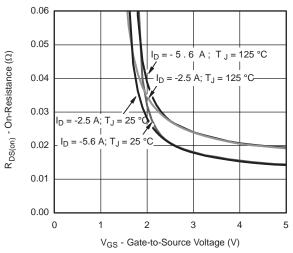
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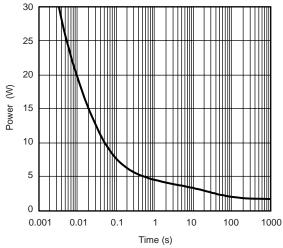
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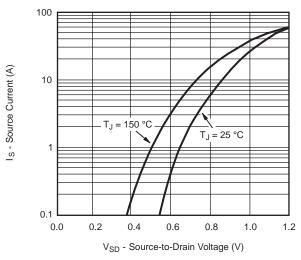
On-Resistance vs. Junction Temperature



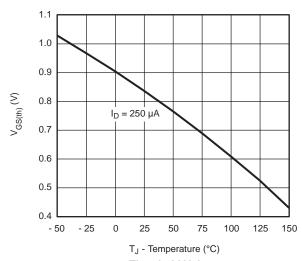
On-Resistance vs. Gate-to-Source Voltage



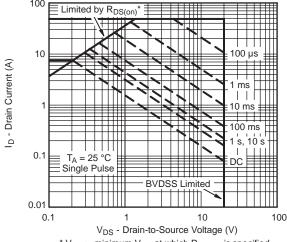
Single Pulse Power, Junction-to-Ambient



Soure-Drain Diode Forward Voltage



Threshold Voltage

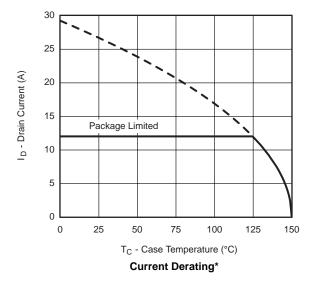


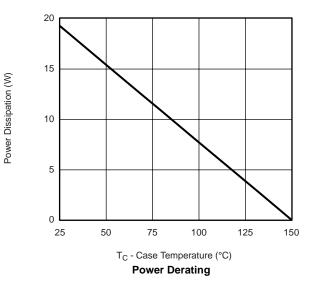
* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Ambient



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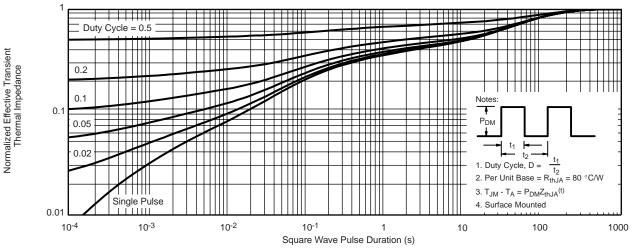
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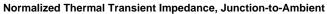
^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit

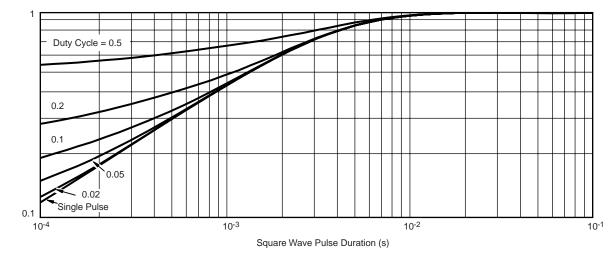
Normalized Effective Transient Thermal Impedance



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



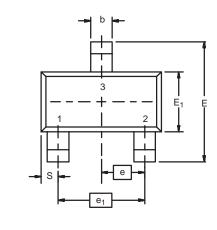


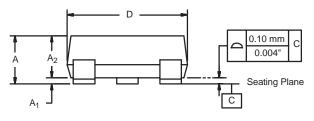


Normalized Thermal Transient Impedance, Junction-to-Case



SOT-23 (TO-236): 3-LEAD







Min 0.89 0.01 0.88	Max 1.12 0.10	Min 0.035 0.0004	Max 0.044
0.01 0.88	0.10		
0.88		0.0004	
	1.00		0.004
	1.02	0.0346	0.040
0.35	0.50	0.014	0.020
0.085	0.18	0.003	0.007
2.80	3.04	0.110	0.120
2.10	2.64	0.083	0.104
1.20	1.40	0.047	0.055
0.95 l	5 BSC 0.0374		Ref
1.90 [BSC	0.0748	Ref
0.40	0.60	0.016	0.024
0.64 Ref		0.025 Ref	
0.50 Ref		0.020 Ref	
3°	8°	3°	8°
	0.085 2.80 2.10 1.20 0.95 1.90 0.40 0.64	0.085 0.18 2.80 3.04 2.10 2.64 1.20 1.40 0.95 BSC 1.90 BSC 0.40 0.60 0.64 Ref 0.50 Ref	0.085 0.18 0.003 2.80 3.04 0.110 2.10 2.64 0.083 1.20 1.40 0.047 0.95 BSC 0.0374 1.90 BSC 0.0748 0.40 0.60 0.016 0.64 Ref 0.025 0.50 Ref 0.020

ECN: S-03946-Rev. K, 09-Jul-01

DWG: 5479

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RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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