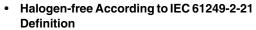


# WST03P06-VB Datasheet P-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	$R_{DS(on)}$ ( $\Omega$ ) Typ.	I <sub>D</sub> (A) <sup>d</sup>	Q <sub>g</sub> (TYP.)			
-60	0.070 at V <sub>GS</sub> = -10 V	-4.5	10.1 nC			
-00	0.085 at V <sub>GS</sub> = -4.5 V	-4.0	10.1110			

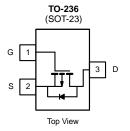
### **FEATURES**

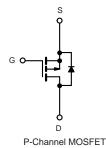




- Trench Power MOSFET
- Compliant to RoHS Directive 2002/95/EC







<b>ABSOLUTE MAXIMUM RATINGS</b> (TA	= 25 °C, unless other	wise noted)		
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage Gate-Source Voltage		V <sub>DS</sub>	-60	V
		V <sub>GS</sub>	± 20	
	T <sub>C</sub> = 25 °C		-4.5	
Continuous Dusin Comment /T 150 °C)	T <sub>C</sub> = 70 °C	1 , [	-4.0	
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	-3.5 <sup>a,b</sup>	
	T <sub>A</sub> = 70 °C		-3.0 <sup>a,b</sup>	
sed Drain Current (t = 100 μs)		I <sub>DM</sub>	-20	Α
Continuous Courses Drain Diada Current	T <sub>C</sub> = 25 °C		-3.9	
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	-2.1 <sup>a,b</sup>	
Avalanche Current	l 0.1 ml l	I <sub>AS</sub>	-15	
Single-Pulse Avalanche Energy	L = 0.1 mH	E <sub>AS</sub>	11.25	mJ
	T <sub>C</sub> = 25 °C		4.2	
Maximum Dawar Dissipation	T <sub>C</sub> = 70 °C	1 , [	2.7	w
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2 <sup>a,b</sup>	VV
	T <sub>A</sub> = 70 °C	1	1.3 <sup>a,b</sup>	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to 150	°C

THERMAL RESISTANCE RATIN	IGS				
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum junction-to-ambient <sup>a</sup>	t ≤ 5 s	R <sub>thJA</sub>	100	130	°C/W
Maximum junction-to-case (drain)	Steady state	R <sub>thJF</sub>	60	75	- C/W

#### Notes

- a. Surface mounted on 1" x 1" FR4 board.
- b. t = 10 s.
- c. Maximum under steady state conditions is 110 °C/W.
- d. Based on  $T_C = 25 \,^{\circ}C$ .

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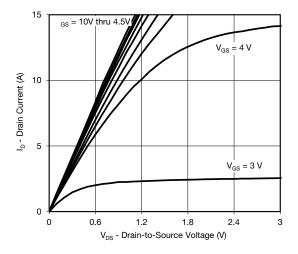
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static				I.	l		
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-60	-	-	V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	J 050 A	-	-6.7	-		
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	- I <sub>D</sub> = -250 μA	-	4.3	-	mv/°C	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-1	-	-3	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 100	nA	
Zara Cata Valta da Busin Comunet		V <sub>DS</sub> = -60 V, V <sub>GS</sub> = 0 V	-	-	-1	μΑ	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -60 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C	-	-	-5		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge -10 \text{ V}, V_{GS} = -10 \text{ V}$	-30	-	-	Α	
Dunin Course On Chata Basistana 3	D	V <sub>GS</sub> = -10 V, I <sub>D</sub> = -3.5 A	-	0.070	-	0	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -2.8 A	-	0.085	-	52	
Forward Transconductance a	9 <sub>fs</sub>	$V_{DS} = -30 \text{ V}, I_{D} = -3.5 \text{ A}$	-	11	-	S	
Dynamic <sup>b</sup>				I.	•		
Input Capacitance	C <sub>iss</sub>		-	832	-		
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	88	-	V mV/°C V nA μA A	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	63	-		
Total Cata Chausa	0	$V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -3.5 \text{ A}$	-	20	30		
Fotal Gate Charge	$Q_g$		-	10.1	15.2	0	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = -30 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -3.5 \text{ A}$	-	3.3	-	nC	
Gate-Drain Charge	Q <sub>gd</sub>		-	3.9	-	1	
Gate Resistance	$R_g$	f = 1 MHz	1.8	9	18	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>		-	8	16		
Rise Time	t <sub>r</sub>	$V_{DD} = -30 \text{ V}, R_L = 10.7 \Omega$	-	6	12	1	
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D \cong -2.8 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$	-	35	53		
Fall Time	t <sub>f</sub>		-	16	24		
Turn-On Delay Time	t <sub>d(on)</sub>		-	40	60	ns	
Rise Time	t <sub>r</sub>	$V_{DD} = -30 \text{ V}, R_L = 10.7 \Omega$	-	28	42	1	
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D \cong -2.8 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$	-	31	47	1	
Fall Time	t <sub>f</sub>		-	15	23	1	
Drain-Source Body Diode Characterist	ics		•		•		
Continous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C	-	-	-3.5		
Pulse Diode Forward Current (t = 100 µs)	I <sub>SM</sub>		-	-	-20	A	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = -2.8 A, V <sub>GS</sub> = 0 V	-	-0.85	-1.2	٧	
Body Diode Reverse Recovery Time	t <sub>rr</sub>		-	32	48	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = -2.8 A, dl/dt = 100 A/μs,	-	45	68	nC	
Reverse Recovery Fall Time	ta	T <sub>J</sub> = 25 °C	-	24	-		
Reverse Recovery Rise Time	t <sub>b</sub>			8	-	ns	

#### Notes

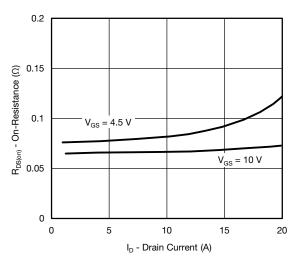
- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

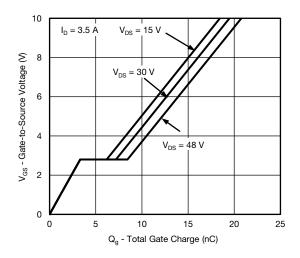




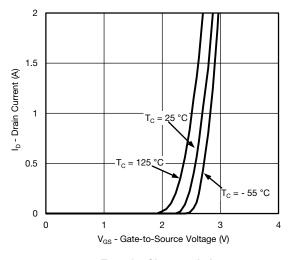
#### **Output Characteristics**



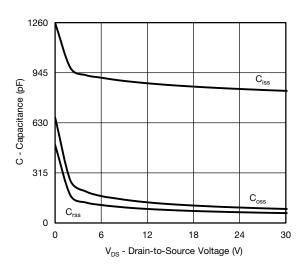
On-Resistance vs. Drain Current



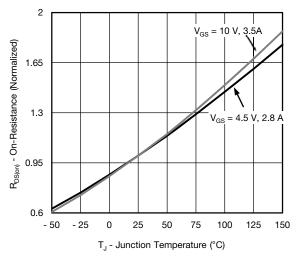
**Gate Charge** 



**Transfer Characteristics** 

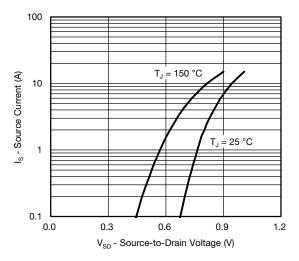


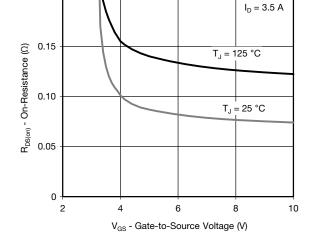
Capacitance



On-Resistance vs. Junction Temperature

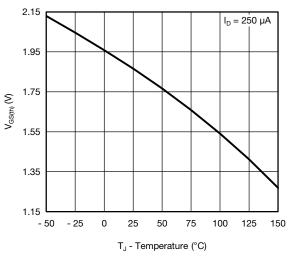


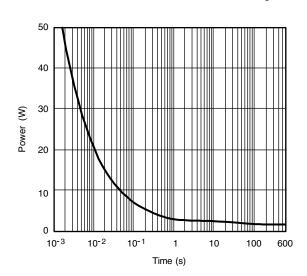




#### Source-Drain Diode Forward Voltage

On-Resistance vs. Gate-to-Source Voltage

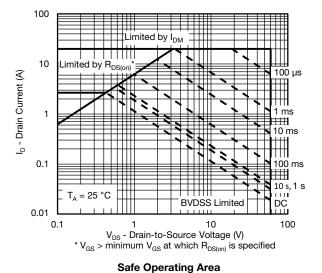




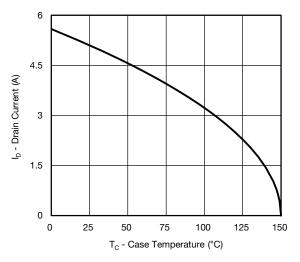
**Threshold Voltage** 

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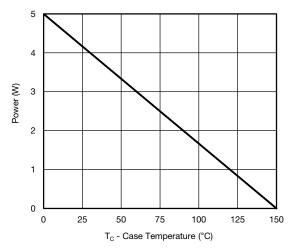
Single Pulse Power, Junction-to-Ambient



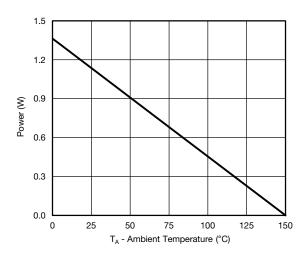




#### **Current Derating\***



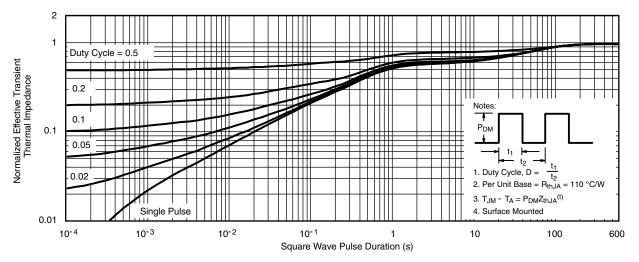




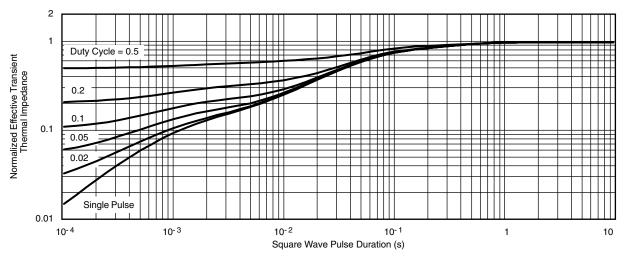
Power Derating, Junction-to-Ambient

<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J \text{ (max.)}} = 150 \,^{\circ}\text{C}$ , using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





Normalized Thermal Transient Impedance, Junction-to-Ambient



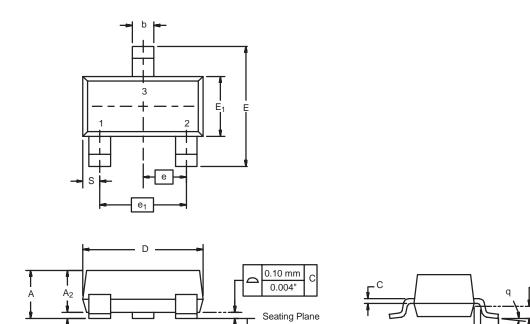
Normalized Thermal Transient Impedance, Junction-to-Foot



0.25 mm

Gauge Plane Seating Plane

## SOT-23 (TO-236): 3-LEAD

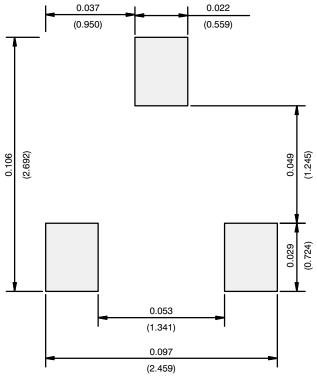


Dim -	MILLIM	IETERS	INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A <sub>1</sub>	0.01	0.10	0.0004	0.004	
A <sub>2</sub>	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E <sub>1</sub>	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e <sub>1</sub>	1.90	1.90 BSC		0.0748 Ref	
L	0.40	0.60	0.016	0.024	
L <sub>1</sub>	0.64 Ref		0.025	5 Ref	
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	

DWG: 5479



## **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)



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