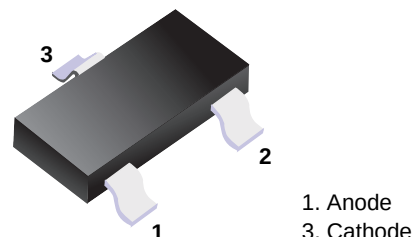
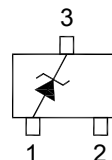


■ Silicon Planar Zener Diodes

This series of Zener diodes is offered in the convenient, surface mount plastic SOT-23 package. These devices are designed to provide voltage regulation with minimum space requirement. They are well suited for applications such as cellular phones, hand held portables, and high density PC boards.



■ Simplified outline(SOT-23)



Features

- Zener breakdown voltage range - 2.4 V
- Package designed for optimal automated board assembly
- Small package size for high density applications

Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Value	Unit
Power Dissipation	P_D	350	mW
Thermal Resistance, Junction to Ambient ¹⁾	$R_{\theta JA}$	357	$^\circ\text{C/W}$
Junction and Storage Temperature Range	T_j, T_{stg}	- 65 to + 150	$^\circ\text{C}$

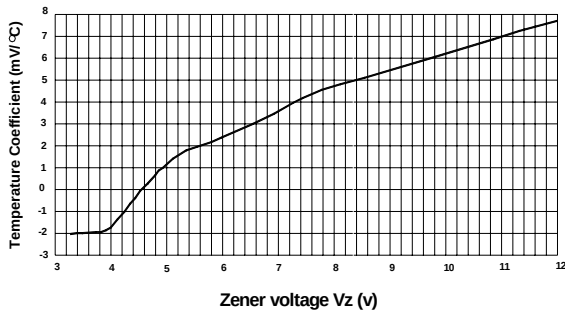
¹⁾ Alumina = 0.4 X 0.3 X 0.024 in, 99.5% alumina

Electrical Characteristics ($T_a = 25^\circ\text{C}$ unless otherwise noted, $V_F < 0.9\text{ V}$ at $I_F = 10\text{ mA}$)

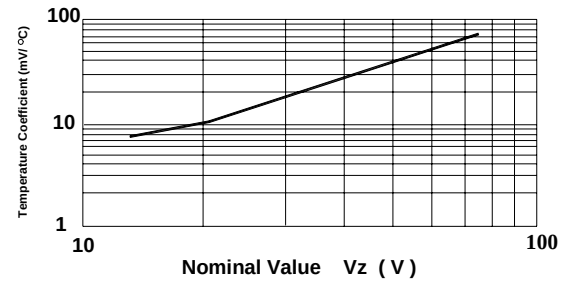
Type	Marking Code	Zener Voltage Range ¹⁾				Dynamic Impedance		Reverse Current	
		V_{ZT}			at I_{ZT}	Z_{ZT}	at I_{ZT}	I_R	at V_R
		Nom. (V)	Min. (V)	Max. (V)	(mA)	Max. (Ω)	(mA)	Max. (μA)	(V)
VB84C2V4		2.4	2.2	2.6	5	100	5	50	1

(1). Tested with pulses $t_p = 20\text{ ms}$.

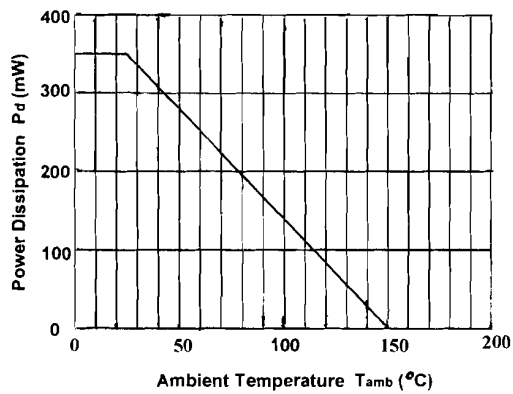
Temperature Coefficient



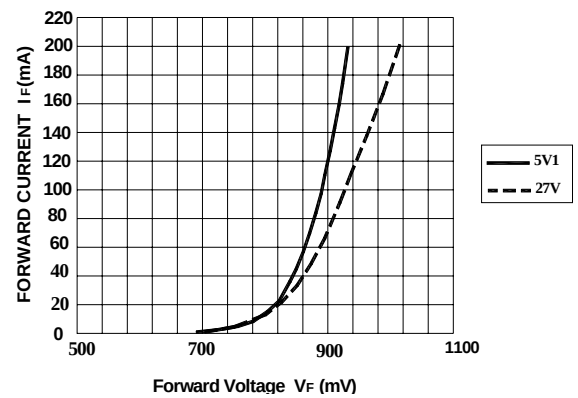
Temperature Coefficient



Power Derating Curve

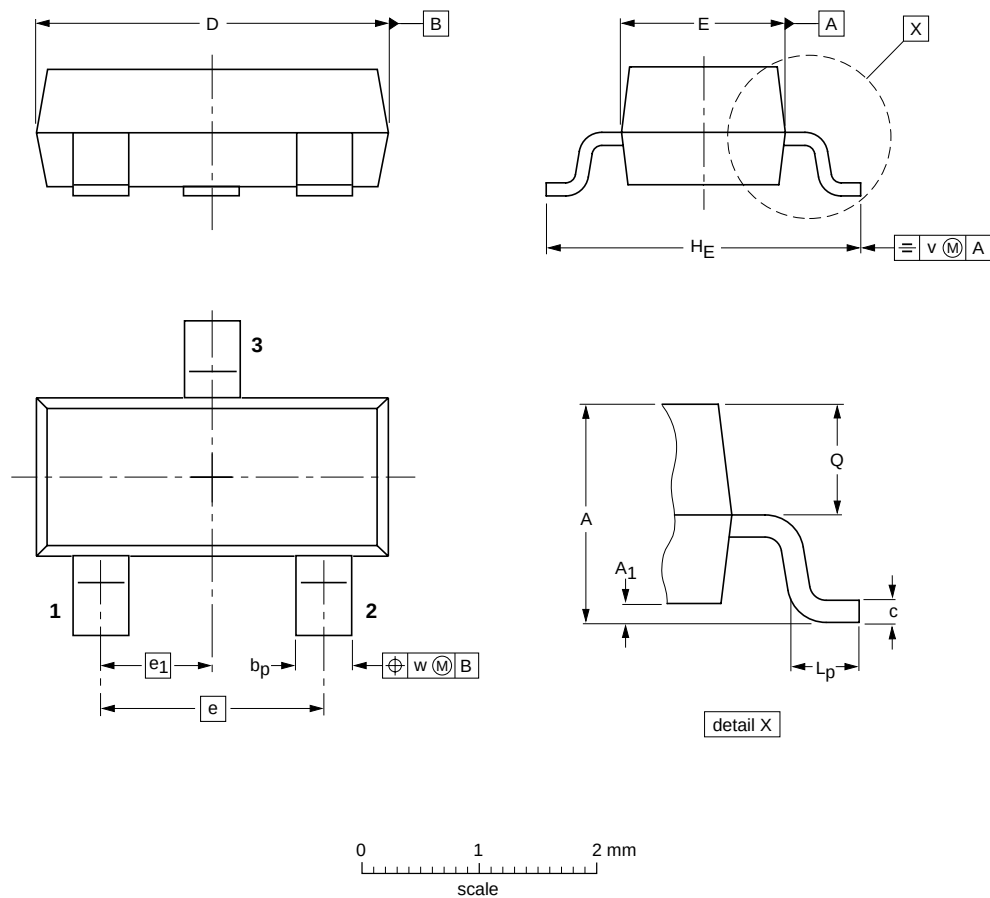


Typical Forward Voltage



Package Outline

SOT-23



DIMENSIONS (mm are the original dimensions)

UNIT	A	A ₁ max.	b _p	c	D	E	e	e ₁	H _E	L _p	Q	v	w
mm	1.1 0.9	0.1	0.48 0.38	0.15 0.09	3.0 2.8	1.4 1.2	1.9	0.95	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1