

FDN339AN-NL-VB Datasheet N-Channel 20 V (D-S) MOSFET

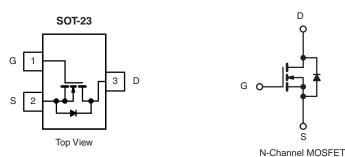
PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (Ω) I _D (A		Q _g (Typ.)			
	0.022 at V _{GS} = 4.5 V	6 ^a				
20	0.028 at V _{GS} = 2.5 V	at V _{GS} = 2.5 V 6 ^a 8.8 r				
	0.039 at V _{GS} = 1.8 V	5.6				

FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- Trench Power MOSFET
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- DC/DC Converters
- Load Switch for Portable Applications



ABSOLUTE MAXIMUM RATINGS $T_A = 25 \text{ °C}$, unless otherwise noted					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	20	V	
Gate-Source Voltage		V _{GS}	± 12	v	
	T _C = 25 °C		6 ^a		
Continuous Drain Current (T. 150 °C)	T _C = 70 °C	1 . F	5.1		
Continuous Drain Current ($T_J = 150 \ ^{\circ}C$)	T _A = 25 °C		5 ^{b, c}		
	T _A = 70 °C	1 F	4 ^{b, c}	A	
Pulsed Drain Current		I _{DM}	20		
Continuous Source-Drain Diode Current	T _C = 25 °C		1.75		
Continuous Source-Drain Diode Current	T _A = 25 °C	l ^I s	1.04 ^{b, c}		
	T _C = 25 °C		2.1		
Maximum Bower Dissipation	T _C = 70 °C		1.3	w	
Maximum Power Dissipation	T _A = 25 °C	P _D	1.25 ^{b, c}	VV	
	T _A = 70 °C	1 F	0.8 ^{b, c}		
Operating Junction and Storage Temperature	T _J , T _{stg}	- 55 to 150	°C		
Soldering Recommendations (Peak Tempera		260			

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	t ≤ 5 s	R _{thJA}	80	100	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	40	60	- 0/11	

Notes:

a. Package limited

b. Surface Mounted on 1" x 1" FR4 board.

c. t = 5 s.

d. Maximum under steady state conditions is 125 °C/W.

e. Based on T_C = 25 °C.



FREE

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	SPECIFICATIONS T _J = 25 °C, unless otherwise noted								
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Static								
$\begin{array}{ c c c c c c } \hline V_{QS(th)} & I_{D} = 250 \ \mu A & & & & & & & & & & & & & & & & & &$	Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = 250 \mu A$	20			V		
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I 250 uA		25		m\//°C		
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	η - 200 μπ		- 2.6		mv/ C		
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	0.45		1.0	V		
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA		
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $		1	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$			1			
$\begin{array}{ c c c c c c c } \hline Pair Source On-State Resistance^a \\ \hline P_{DS(on)} & V_{GS} = 4.5 \ V, \ I_D = 5.0 \ A & 0.022 & 0.028 & $	Zero Gate Voltage Drain Current	DSS	V_{DS} = 20 V, V_{GS} = 0 V, T_{J} = 70 °C			10	μΑ		
$\begin{array}{ c c c c c c } \hline Pain-Source On-State Resistance^a & P_{DS(on)} & V_{GS}=2.5 \ V, \ I_{D}=4.7 \ A & 0.028 & 0.039 $	On-State Drain Current ^a	I _{D(on)}	$V_{DS} \leq 5$ V, V_{GS} = 4.5 V	20			А		
$ \begin{array}{ c c c c c c } \hline V_{GS} = 1.8 \ V, \ I_D = 4.3 \ A & 0.039 & 0 \\ \hline V_{GS} = 1.8 \ V, \ I_D = 5.0 \ A & 24 & S \\ \hline \\$			$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 5.0 \text{ A}$		0.022				
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 2.5 \text{ V}, \text{ I}_{D} = 4.7 \text{ A}$		0.028		Ω		
$ \begin{array}{ c $			V _{GS} = 1.8 V, I _D = 4.3 A		0.039				
$ \begin{array}{c c c c c c c } \hline \mbox{Input Capacitance} & C_{iss} & V_{DS} = 10 \ V, \ V_{GS} = 0 \ V, \ f = 1 \ MHz & 105 & 0 & 0 & 0 \\ \hline \mbox{Input Capacitance} & C_{rss} & V_{DS} = 10 \ V, \ V_{GS} = 5 \ V, \ l_{D} = 5.0 \ A & 12 & 18 & 0 & 0 & 0 & 0 \\ \hline \mbox{Input Capacitance} & Q_{g} & V_{DS} = 10 \ V, \ V_{GS} = 5 \ V, \ l_{D} = 5.0 \ A & 12 & 18 & 0 & 0 & 0 & 0 & 0 & 0 \\ \hline \mbox{Input Capacitance} & Q_{gs} & V_{DS} = 10 \ V, \ V_{GS} = 4.5 \ V, \ l_{D} = 5.0 \ A & 1.1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & $	Forward Transconductance ^a	9 _{fs}	V _{DS} = 10 V, I _D = 5.0 A		24		S		
$ \begin{array}{ c c c c c } \hline Output Capacitance & C_{oss} \\ \hline Reverse Transfer Capacitance & C_{rss} \\ \hline Reverse Transfer Capacitance & C_{rss} \\ \hline Total Gate Charge & O_{g} \\ \hline O_{g} \\ \hline O_{g} \\ \hline O_{DS} = 10 \ V, \ V_{GS} = 5 \ V, \ I_{D} = 5.0 \ A \\ \hline V_{DS} = 10 \ V, \ V_{GS} = 5 \ V, \ I_{D} = 5.0 \ A \\ \hline V_{DS} = 10 \ V, \ V_{GS} = 4.5 \ V, \ I_{D} = 5.0 \ A \\ \hline O_{T} \hline O_{T} \\ \hline O_{T} \hline O_{T} \\ \hline O_{T} \hline $	Dynamic ^b				1	1	<u> </u>		
$ \begin{array}{c c c c c c c c } \hline Reverse Transfer Capacitance & C_{rss} & $V_{DS} = 10 \ V, \ V_{GS} = 5 \ V, \ I_D = 5.0 \ A & 12 & 18 \\ \hline Total \ Gate \ Charge & Q_g & $V_{DS} = 10 \ V, \ V_{GS} = 5 \ V, \ I_D = 5.0 \ A & 12 & 18 \\ \hline M_{DS} = 10 \ V, \ V_{GS} = 4.5 \ V, \ I_D = 5.0 \ A & 1.1 & 1.1 & 1.1 \\ \hline M_{DS} = 10 \ V, \ V_{GS} = 4.5 \ V, \ I_D = 5.0 \ A & 1.1 $	Input Capacitance	C _{iss}			865				
$ \begin{array}{c c c c c c c c } \hline Reverse Transfer Capacitance & C_{rss} & $V_{DS} = 10 \ V, \ V_{GS} = 5 \ V, \ I_D = 5.0 \ A & 12 & 18 \\ \hline Total \ Gate \ Charge & Q_g & $V_{DS} = 10 \ V, \ V_{GS} = 5 \ V, \ I_D = 5.0 \ A & 12 & 18 \\ \hline M_{DS} = 10 \ V, \ V_{GS} = 4.5 \ V, \ I_D = 5.0 \ A & 1.1 & 1.1 & 1.1 \\ \hline M_{DS} = 10 \ V, \ V_{GS} = 4.5 \ V, \ I_D = 5.0 \ A & 1.1 $	Output Capacitance	C _{oss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		105		pF		
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Reverse Transfer Capacitance				55				
$ \begin{array}{ c c c c c c } \hline \begin{tabular}{ c c c c c c c } \hline \begin{tabular}{ c c c c c c c c c c c c c c c c c c c$			V _{DS} = 10 V, V _{GS} = 5 V, I _D = 5.0 A		12	18			
$ \begin{array}{ c c c c c c } \hline Gate-Source Charge & Q_{gs} & V_{DS} = 10 \ V, \ V_{GS} = 4.5 \ V, \ I_D = 5.0 \ A & 1.1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 &$	Iotal Gate Charge	Qg			8.8	14	nC		
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Gate-Source Charge	Q _{gs}	V _{DS} = 10 V, V _{GS} = 4.5 V, I _D = 5.0 A		1.1				
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Gate-Drain Charge	Q _{gd}			0.7				
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Gate Resistance	Rg	f = 1 MHz	0.5	2.4	4.8	Ω		
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	Turn-On Delay Time	t _{d(on)}			8	16			
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Rise Time		BB E		17	26	1		
$ \begin{array}{ c c c c c c c } \hline Fall Time & t_{f} & & & & & & & & & & & & & & & & & & &$	Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong \text{4}$ A, V_GEN = 4.5 V, R_g = 1 Ω		31	47			
$ \begin{array}{c c} \mbox{Turn-On Delay Time} & t_{d(on)} \\ \mbox{Rise Time} & t_r & \\ \mbox{Turn-Off Delay Time} & t_{d(off)} \\ \mbox{Fall Time} & t_f & \\ \end{array} \\ \begin{array}{c c} \mbox{V}_{DD} = 10 \ V, \ R_L = 2.2 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 5 \ V, \ R_g = 1 \ \Omega \\ \mbox{I}_D \cong 4 \ A, \ V_{GEN} = 1 \ M \ A, $	Fall Time				8	16	ne		
$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$	Turn-On Delay Time	t _{d(on)}			5	10	. 113		
Fall Time t_f 612	Rise Time		55 -		13	20	1		
Fall Time tf 6 12	Turn-Off Delay Time	t _{d(off)}	$I_D \cong 4 \text{ A}, V_{GEN} = 5 \text{ V}, R_g = 1 \Omega$		21	32	1		
Drain-Source Body Diode Characteristics	Fall Time				6	12			
Continuous Source-Drain Diode Current I_S $T_C = 25 ^{\circ}C$ 1.75	Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			1.75	•		
Pulse Diode Forward Current I _{SM} A	Pulse Diode Forward Current	I _{SM}				20			
Body Diode Voltage V_{SD} $I_S = 4 \text{ A}, V_{GS} = 0 \text{ V}$ 0.75 1.2 V	Body Diode Voltage	V _{SD}	$I_{S} = 4 \text{ A}, V_{GS} = 0 \text{ V}$	T	0.75	1.2	V		
Body Diode Reverse Recovery Time trr 12 20 ns	Body Diode Reverse Recovery Time	t _{rr}			12	20	ns		
Body Diode Reverse Recovery Charge Q _{rr} L = 4.4 dl/dt = 100.4/µp T = 25 °C 5 10 nC	Body Diode Reverse Recovery Charge	Q _{rr}	L = 4.0 dt/dt = 100.0/m = 100.000		5	10	nC		
Body block nectors nectored for the second secon	Reverse Recovery Fall Time	$I_{\rm F} = 4$ A, dl/dt = 100 A/µs, $I_{\rm I} =$			7				
Reverse Recovery Rise Time t _b ns	Reverse Recovery Rise Time	t _b			5		ns		

Bsemi

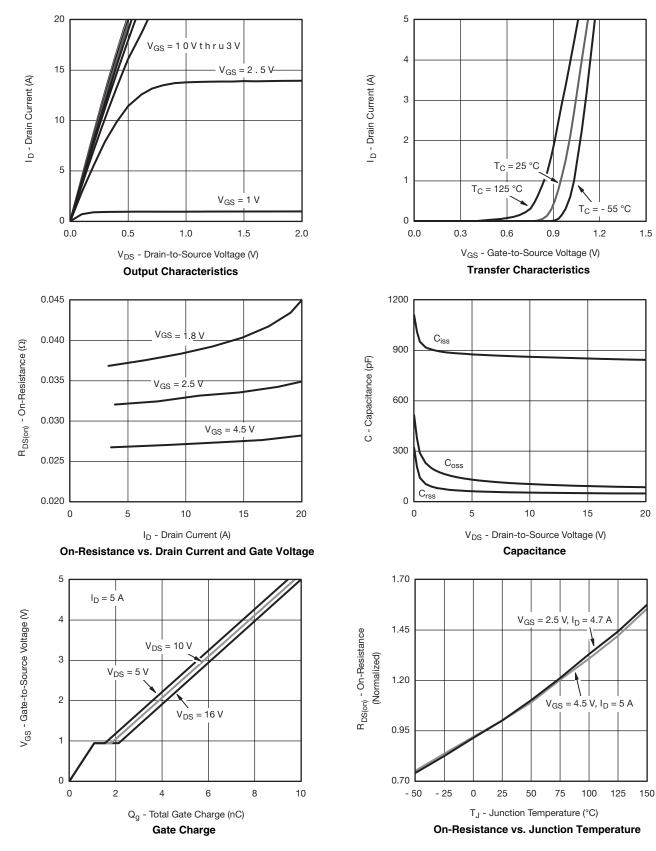
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Notes:

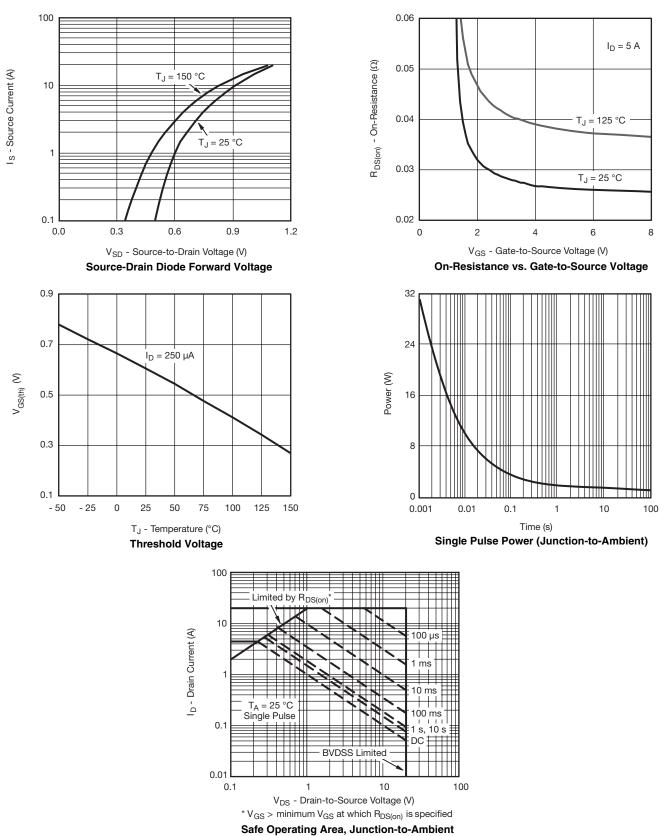
a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 % b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

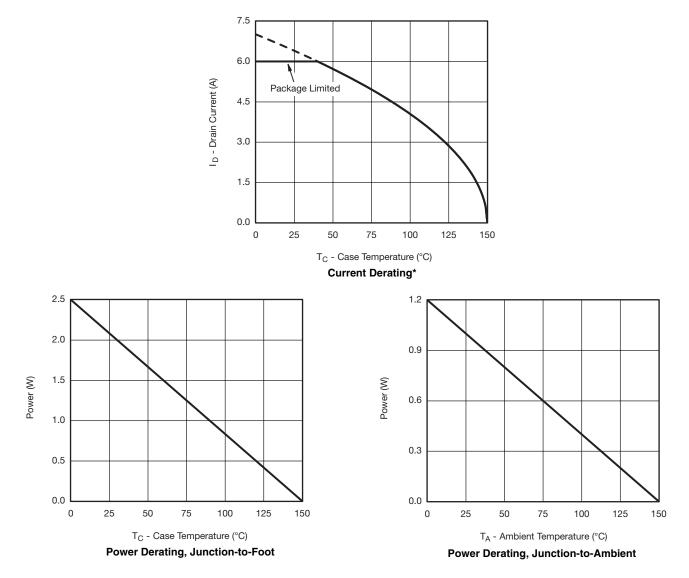






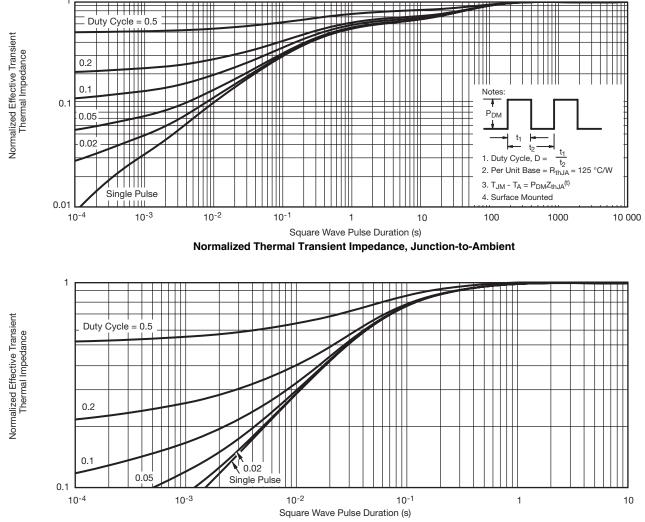






* The power dissipation P_D is based on $T_{J(max.)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



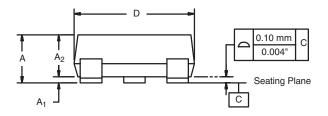


Normalized Thermal Transient Impedance, Junction-to-Foot



SOT-23 (TO-236): 3-LEAD







Dim	MILLIN	IETERS	INCHES			
	Min	Мах	Min	Max		
Α	0.89	1.12	0.035	0.044		
A ₁	0.01	0.10	0.0004	0.004		
A ₂	0.88	1.02	0.0346	0.040		
b	0.35	0.50	0.014	0.020		
С	0.085	0.18	0.003	0.007		
D	2.80	3.04	0.110	0.120		
E	2.10	2.64	0.083	0.104		
E ₁	1.20	1.40	0.047	0.055		
е	0.95 BSC		0.037	4 Ref		
e ₁	1.90 BSC		0.0748 Ref			
L	0.40	0.60	0.016	0.024		
L ₁	0.64 Ref		0.02	5 Ref		
S	0.50 Ref		0.02	0.020 Ref		
q	3°	8°	3°	8°		
ECN: S-03946-Rev. K, 09- DWG: 5479	Jul-01	•				



RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)



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