

# **BSS119N-VB** Datasheet N-Channel 100 V (D-S) MOSFET

MOSFET	PRODUCT SUMM	ARY	
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)
	0.240 at V <sub>GS</sub> = 10 V	2.0	
100	0.250 at V <sub>GS</sub> = 6 V	1.8	2.9 nC
	0.260 at V <sub>GS</sub> = 4.5 V	1.7	

#### **FEATURES**

- Trench Power MOSFET
- 100 % R<sub>g</sub> Tested
  100 % UIS Tested

**APPLICATIONS** 

• Material categorization:



#### DC/DC Converters Load Switch

• LED Backlighting in LCD TVs

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V <sub>DS</sub>	100	V
Gate-Source Voltage		V <sub>GS</sub>	± 20	v
Continuous Drain Current (T <sub>J</sub> = 150 °C)	$T_{C} = 25 \text{ °C}$ $T_{C} = 70 \text{ °C}$ $T_{A} = 25 \text{ °C}$ $T_{A} = 70 \text{ °C}$	I <sub>D</sub>	2 1.8 1.6 <sup>b, c</sup> 1.3 <sup>b, c</sup>	
Pulsed Drain Current (t = 300 µs)		I <sub>DM</sub>	7	A
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C T <sub>A</sub> = 25 °C	I <sub>S</sub>	2.1 1.0 <sup>b, c</sup>	
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	5	
Single Pulse Avalanche Energy		E <sub>AS</sub>	1.25	mJ
Maximum Power Dissipation	$T_{C} = 25 \text{ °C}$ $T_{C} = 70 \text{ °C}$ $T_{A} = 25 \text{ °C}$ $T_{A} = 70 \text{ °C}$	P <sub>D</sub>	2.5 1.6 1.25 <sup>b, c</sup> 0.8 <sup>b, c</sup>	W
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stq</sub>	- 55 to 150	°C

THERMAL RESISTANCE RA	TINGS				
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>b, d</sup>	≤ 5 s	R <sub>thJA</sub>	75	100	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	40	50	
Natao					

Notes: a. Based on T<sub>C</sub> = 25 °C.

b. Surface mounted on 1" x 1" FR4 board.

c. t = 5 s.

d. Maximum under steady state conditions is 166 °C/W.

G 1 3 s

D



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static					1	1
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{DS} = 0 V, I_{D} = 250 \mu A$	100			V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$			105		
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA		- 5.2		mV/°C
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1.2		2.8	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V			- 1	μA
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 ^{\circ}\text{C}$			- 10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5$ V, $V_{GS}$ = 4.5 V	5			Α
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 1.5 A		0.240		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 6 V, I <sub>D</sub> = 1 A		0.250		Ω
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 0.5 A		0.260		1
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 20 V, I <sub>D</sub> = 1.5 A		2.0		S
Dynamic <sup>b</sup>						<u> </u>
Input Capacitance	C <sub>iss</sub>			190		
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 0 V, f = 1 MHz		22		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			13		
		V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 1.6 A		5.2	10.4	
Total Gate Charge	Qg			2.9	5.8	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 50$ V, $V_{GS} = 4.5$ V, $I_{D} = 1.6$ A		0.75		
Gate-Drain Charge	Q <sub>gd</sub>			1.4		
Gate Resistance	R <sub>g</sub>	f = 1 MHz	0.3	1.4	2.8	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			30	45	
Rise Time	t <sub>r</sub>	$V_{DD} = 50 \text{ V}, \text{ R}_1 = 39 \Omega$		26	39	1
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_{D} = 1.3 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_{g} = 1 \Omega$		17	26	-
Fall Time	t <sub>f</sub>			12	20	
Turn-On Delay Time	t <sub>d(on)</sub>			6	12	ns
Rise Time	t <sub>r</sub>	$V_{DD}$ = 50 V, $R_{L}$ = 39 $\Omega$		10	20	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_{D} = 1.3 \text{ A}, V_{GEN} = 10 \text{ V}, \text{ R}_{g} = 1 \Omega$		10	20	
Fall Time	t <sub>f</sub>			6	12	-
Drain-Source Body Diode Characteristi	cs				I	1
Continuous Source-Drain Diode Current	۱ <sub>S</sub>	T <sub>C</sub> = 25 °C			- 2.1	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				- 20	A
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 1.3 A		- 0.8	- 1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>			22	33	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			21	32	nC
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = 1.3 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, \text{ T}_J = 25 ^\circ\text{C}$		16		1
Reverse Recovery Rise Time	t <sub>b</sub>			6		ns

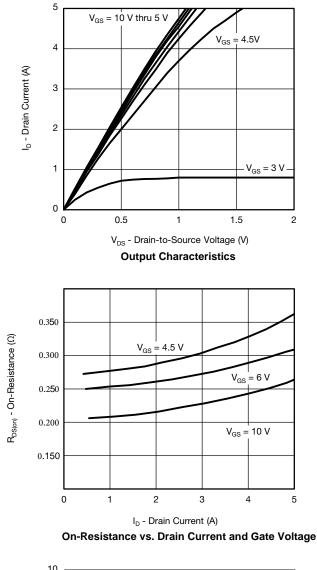
Notes:

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

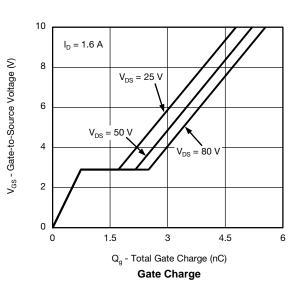
b. Guaranteed by design, not subject to production testing.

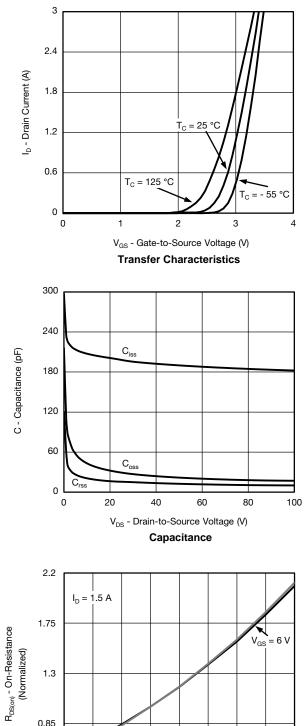
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

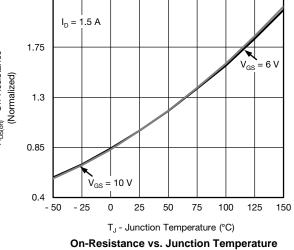




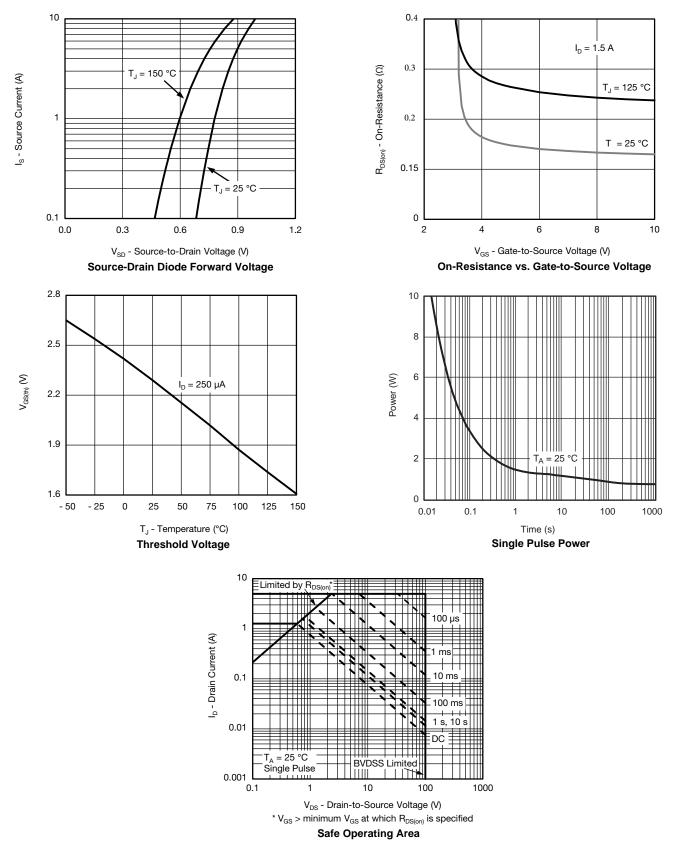
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)







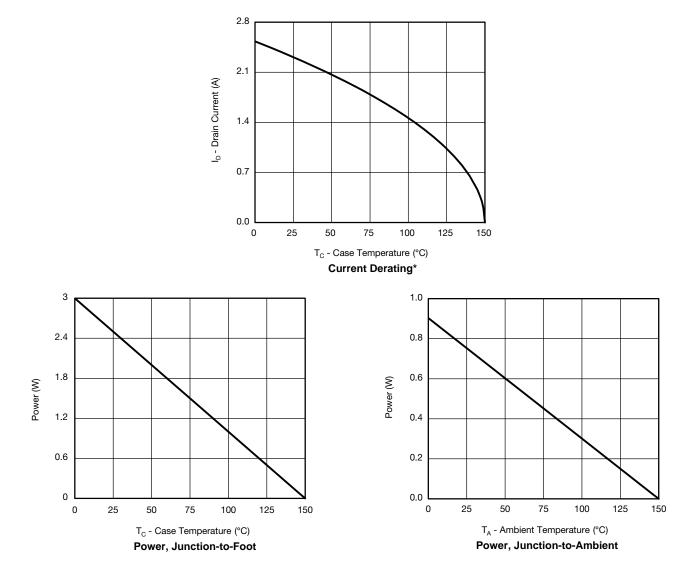




# TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

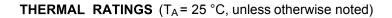


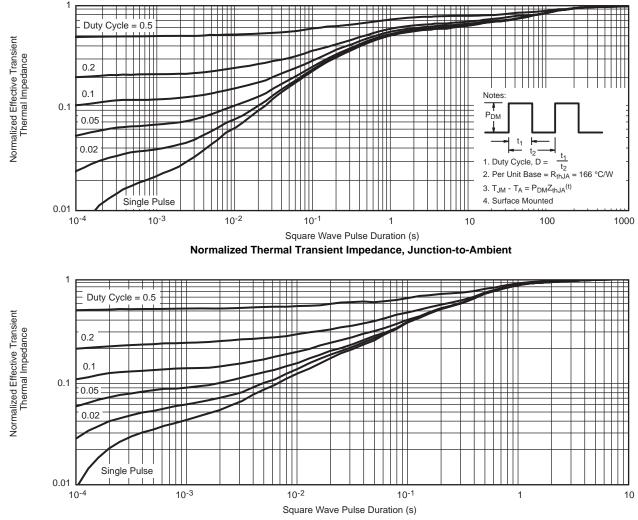
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



\* The power dissipation  $P_D$  is based on  $T_{J(max)}$  = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.







Normalized Thermal Transient Impedance, Junction-to-Foot

#### Note

The characteristics shown in the two graphs

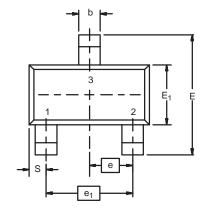
- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

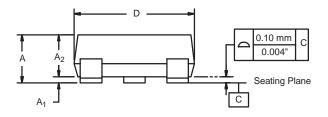
- Normalized Transient Thermal Impedance Junction-to-Foot (25 C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.



# SOT-23 (TO-236): 3-LEAD







Max           1.12           0.10           1.02           0.50           0.18           3.04           2.64           1.40	Min           0.035           0.0004           0.0346           0.014           0.003           0.110           0.083	Max           0.044           0.004           0.040           0.020           0.007           0.120           0.104	
0.10 1.02 0.50 0.18 3.04 2.64	0.0004 0.0346 0.014 0.003 0.110 0.083	0.004 0.040 0.020 0.007 0.120	
1.02         0.50         0.18         3.04         2.64	0.0346 0.014 0.003 0.110 0.083	0.040 0.020 0.007 0.120	
0.50 0.18 3.04 2.64	0.014 0.003 0.110 0.083	0.020 0.007 0.120	
0.18 3.04 2.64	0.003 0.110 0.083	0.007	
3.04 2.64	0.110 0.083	0.120	
2.64	0.083		
		0.104	
1 40			
1.40	0.047	0.055	
	0.0374	4 Ref	
	0.0748 Ref		
0.60	0.016	0.024	
	0.025	5 Ref	
	0.020	) Ref	
8°	3°	8°	
_	8°	0.020	



## **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)



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