

## WSG20N20-VB Datasheet N-Channel 200 V (D-S) MOSFET

PRODUCT	SUMMARY	
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)
200	0.283 at V <sub>GS</sub> = 10 V	3

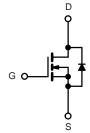
#### **FEATURES**

- Trench Power MOSFET
- 175 °C Junction Temperature
- PWM Optimized
- 100 % R<sub>g</sub> Tested
  Compliant to RoHS Directive 2002/95/EC









N-Channel MOSFET

#### **APPLICATIONS**

· Primary Side Switch

<b>ABSOLUTE MAXIMUM RATINGS</b> $(T_A = 25)$	5 °C, unless othe	rwise noted)			
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	200	V	
Gate-Source Voltage		$V_{GS}$	± 20	V	
Continuous Drain Current (T <sub>.I</sub> = 175 °C) <sup>b</sup>	T <sub>C</sub> = 25 °C	- I <sub>D</sub>	3		
Continuous Diam Current (1j = 175 C)	T <sub>C</sub> = 125 °C		2.7	1	
Pulsed Drain Current		I <sub>DM</sub>	10	Α	
Continuous Source Current (Diode Conduction)		I <sub>S</sub>	6		
Avalanche Current		I <sub>AS</sub>	6		
Single Pulse Avalanche Energy	L = 0.1 mH	E <sub>AS</sub>	18	mJ	
Maximum Power Dissipation	T <sub>C</sub> = 25 °C	P <sub>D</sub> 96 <sup>b</sup>		W	
Maximum Fower Dissipation	T <sub>A</sub> = 25 °C	' D	3 <sup>a</sup>	VV	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
lunation to Ambiant	t ≤ 10 s	R <sub>thJA</sub>	15	18			
Junction-to-Ambient <sup>a</sup>	Steady State	\ \thJA	40	50	°C/W		
Junction-to-Case (Drain)	•	R <sub>thJC</sub>	0.85	1.1			

#### Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. See SOA curve for voltage derating.



Parameter	Symbol	Test Conditions	Min.	Typ. <sup>a</sup>	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	200			.,
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2		4	V
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
		V <sub>DS</sub> = 200 V, V <sub>GS</sub> = 0 V			1	
Zero Gate Voltage Drain Current	$I_{DSS}$	V <sub>DS</sub> = 200 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50	μΑ
		V <sub>DS</sub> = 200 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			250	
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	40			Α
		$V_{GS} = 10 \text{ V}, I_D = 3 \text{ A}$		0.283		
Danie Course On Chata Besistance	R	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3 A, T <sub>J</sub> = 125 °C		0.320		Ω
Drain-Source On-State Resistance <sup>b</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3 A, T <sub>J</sub> = 175 °C		0.350		7.2
		$V_{GS} = 6 \text{ V}, I_D = 3 \text{ A}$		0.292		S
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 3 A		35		S
Dynamic <sup>a</sup>						
Input Capacitance	$C_{iss}$			1800		
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, F = 1 \text{ MHz}$		180		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			80		
Total Gate Charge <sup>c</sup>	$Q_g$			34	51	
Gate-Source Charge <sup>c</sup>	$Q_{gs}$	$V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 3 \text{ A}$		8		nC
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			12		
Gate Resistance	$R_g$		0.5		2.9	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			15	25	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 100 \text{ V}, R_{L} = 5.2 \Omega$		50	75	ns
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 3 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5 \Omega$		30	45	
Fall Time <sup>c</sup>	t <sub>f</sub>			60	90	
Source-Drain Diode Ratings and Char	acteristics (7	T <sub>C</sub> = 25 °C)				
Pulsed Current	I <sub>SM</sub>				5	Α
Diode Forward Voltage <sup>b</sup>	$V_{SD}$	I <sub>F</sub> = 3 A, V <sub>GS</sub> = 0 V		0.9	1.5	V
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 3 A, dI/dt = 100 A/μs		180	250	ns

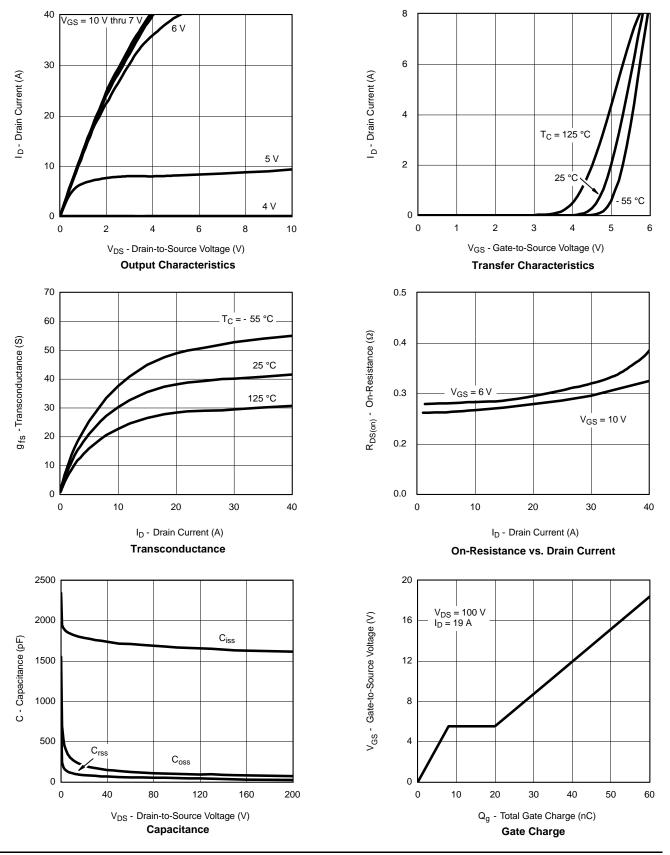
#### Notes:

- a. Guaranteed by design, not subject to production testing.
- b. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

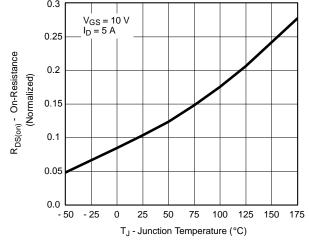


#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

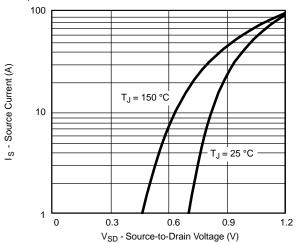




#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



On-Resistance vs. Junction Temperature

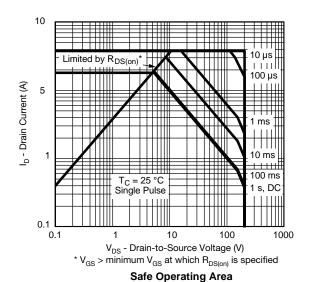


Source-Drain Diode Forward Voltage

#### THERMAL RATINGS



Maximum Avalanche Drain Current vs. Case Temperature



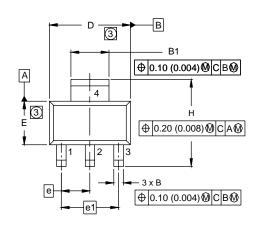
Normalized Thermal Transient Impedance, Junction-to-Case

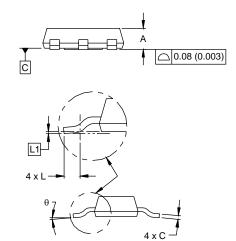
服务热线:400-655-8788

Normalized Effective Transient Thermal Impedance



### **SOT-223 (HIGH VOLTAGE)**





DIM.	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
А	1.55	1.80	0.061	0.071
В	0.65	0.85	0.026	0.033
B1	2.95	3.15	0.116	0.124
С	0.25	0.35	0.010	0.014
D	6.30	6.70	0.248	0.264
E	3.30	3.70	0.130	0.146
е	2.30	2.30 BSC		5 BSC
e1	4.60	BSC	0.181	BSC
Н	6.71	7.29	0.264	0.287
L	0.91	-	0.036	-
L1	0.061	0.061 BSC		4 BSC
θ	-	10'	-	10'

ECN: S-82109-Rev. A, 15-Sep-08

DWG: 5969

#### **Notes**

- 1. Dimensioning and tolerancing per ASME Y14.5M-1994.
- 2. Dimensions are shown in millimeters (inches).
- 3. Dimension do not include mold flash.
- 4. Outline conforms to JEDEC outline TO-261AA.



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