

## SFM9210TF-VB Datasheet

### P-Channel 200V (D-S)MOSFET

#### PRODUCT SUMMARY

$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)	$Q_g$ (Typ.)
- 200	0.8 at $V_{GS} = -10$ V	- 2.0	8.0
	0.9 at $V_{GS} = -6.0$ V	- 1.8	

#### FEATURES

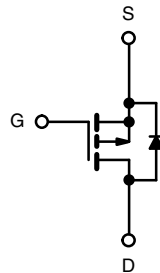
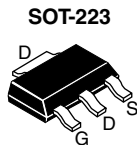
- Halogen-free According to IEC 61249-2-21 Available
- Trench Power MOSFET
- Ultra Low On-Resistance
- Small Size



**RoHS**  
COMPLIANT  
**HALOGEN**  
**FREE**  
Available

#### APPLICATIONS

- Active Clamp Circuits in DC/DC Power Supplies



P-Channel MOSFET

#### ABSOLUTE MAXIMUM RATINGS $T_A = 25^\circ\text{C}$ , unless otherwise noted

Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	- 200		V
Gate-Source Voltage		V <sub>GS</sub>	± 20		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a, b</sup>	T <sub>A</sub> = 25 °C	I <sub>D</sub>	- 2.0	- 1.68	A
	T <sub>A</sub> = 70 °C		- 1.8	- 1.56	
Pulsed Drain Current		I <sub>DM</sub>	- 5.8		
Continuous Source Current (Diode Conduction) <sup>a, b</sup>		I <sub>S</sub>	- 1.0	- 0.6	
Single Pulse Avalanche Current	L = 1.0 mH	I <sub>AS</sub>	4.0		
Single Pulse Avalanche Energy		E <sub>AS</sub>	1.2		
Maximum Power Dissipation <sup>a, b</sup>	T <sub>A</sub> = 25 °C	P <sub>D</sub>	1.45	0.95	W
	T <sub>A</sub> = 70 °C		0.8	0.48	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

#### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	$R_{thJA}$	$t \leq 5$ s	75	$^\circ\text{C/W}$
		Steady State	120	
Maximum Junction-to-Foot (Drain)	$R_{thJF}$	40	50	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

b. Pulse width limited by maximum junction temperature.

SPECIFICATIONS T <sub>J</sub> = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Limits			Unit
			Min.	Typ.	Max.	
Static						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = - 250 μA	- 200			V
Gate-Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = - 250 μA	- 2.5		- 4.5	
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 200 V, V <sub>GS</sub> = 0 V			- 1	μA
		V <sub>DS</sub> = - 200 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≤ - 15 V, V <sub>GS</sub> = 10 V	- 1.0			A
Drain-Source On-Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 0.5 A		0.80		Ω
		V <sub>GS</sub> = - 6.0 V, I <sub>D</sub> = - 0.5 A		0.90		
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 0.5 A		1.8		S
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 1.0 A, V <sub>GS</sub> = 0 V		- 0.85	- 1.2	V
Dynamic <sup>b</sup>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = - 100 V, V <sub>GS</sub> = 10 V I <sub>D</sub> ≅ - 0.5 A		8.0	12	nC
Gate-Source Charge	Q <sub>gs</sub>			1.3		
Gate-Drain Charge	Q <sub>gd</sub>			2.5		
Gate Resistance	R <sub>g</sub>	f = 1.0 MHz		8.0		Ω
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = - 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz		370	510	pF
Output Capacitance	C <sub>oss</sub>			28		
Reverse Transfer Capacitance	C <sub>rss</sub>			16		
Switching <sup>c</sup>						
Turn-On Time	t <sub>d(on)</sub>	V <sub>DD</sub> = - 100 V, R <sub>L</sub> = 100 Ω I <sub>D</sub> ≅ - 1.0 A, V <sub>GEN</sub> = - 10 V R <sub>g</sub> = 6 Ω		8	12	ns
	t <sub>r</sub>			11	17	
Turn-Off Time	t <sub>d(off)</sub>			16	25	
	t <sub>f</sub>			11	17	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 0.5 A, dI/dt = 100 A/μs		140	200	nC

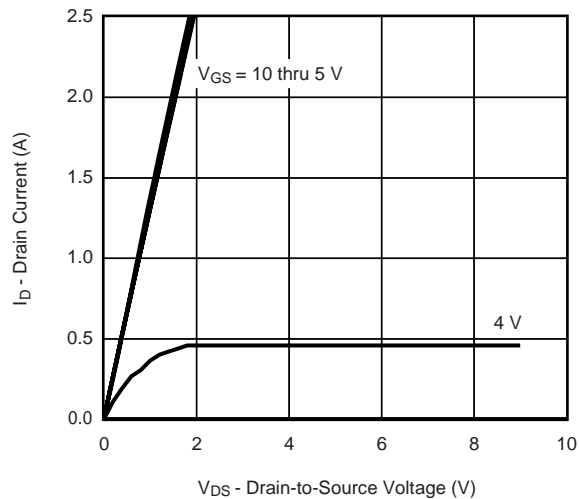
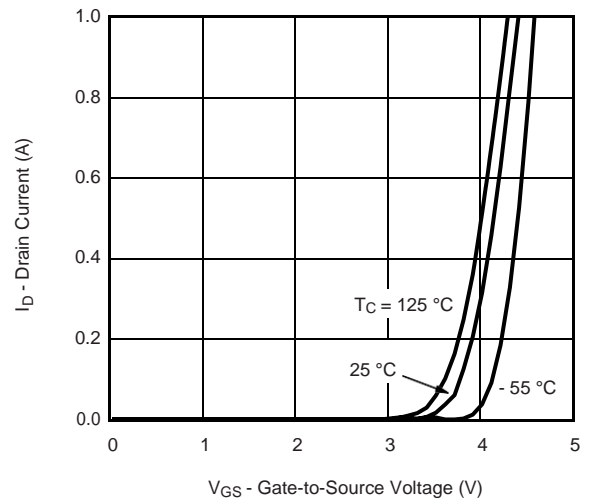
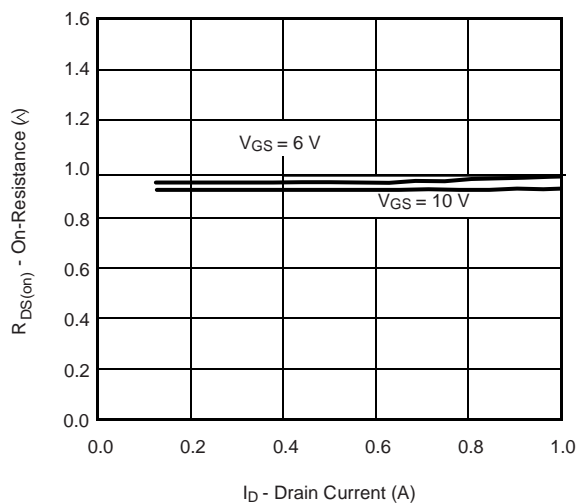
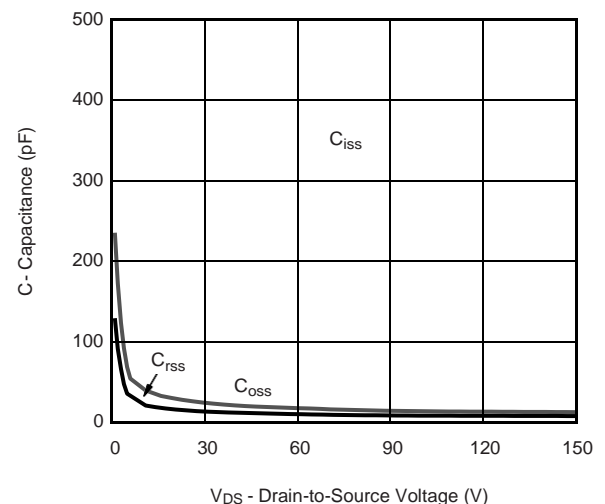
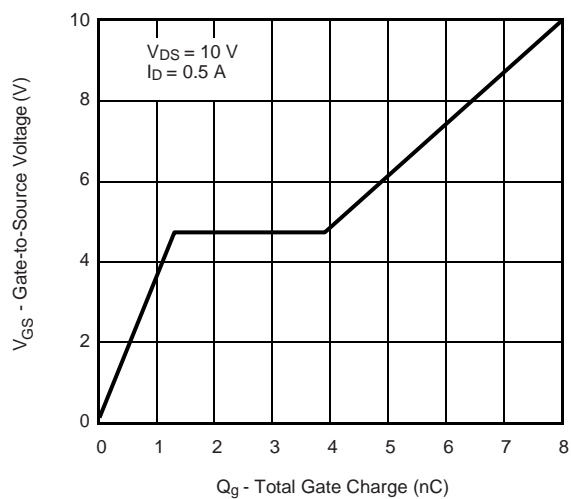
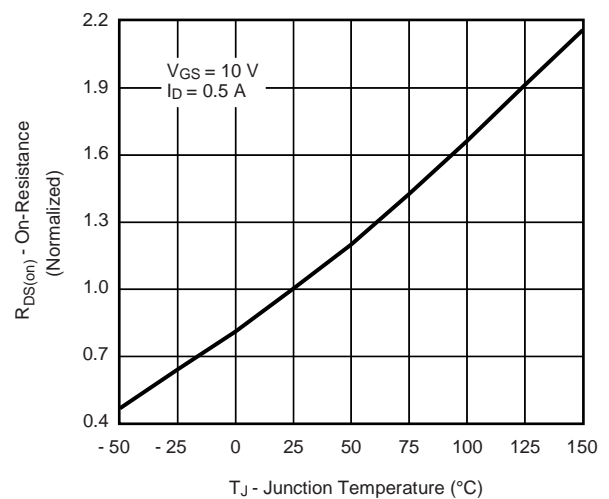
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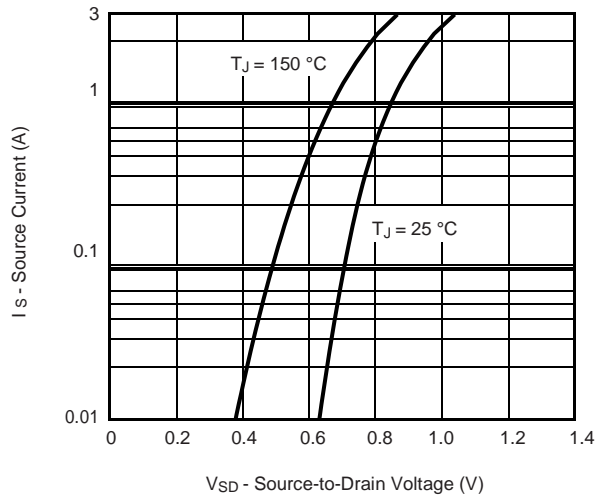
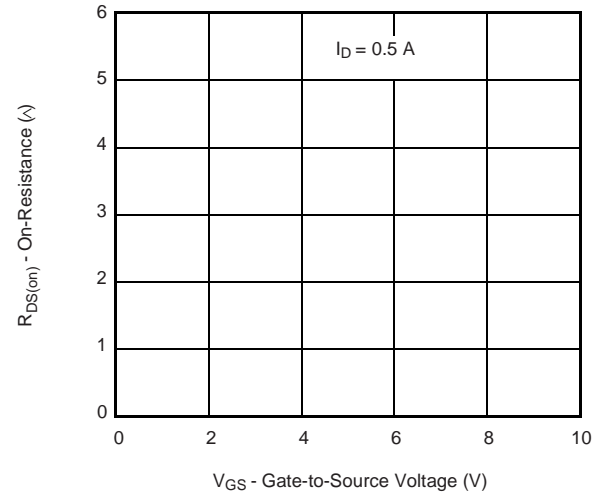
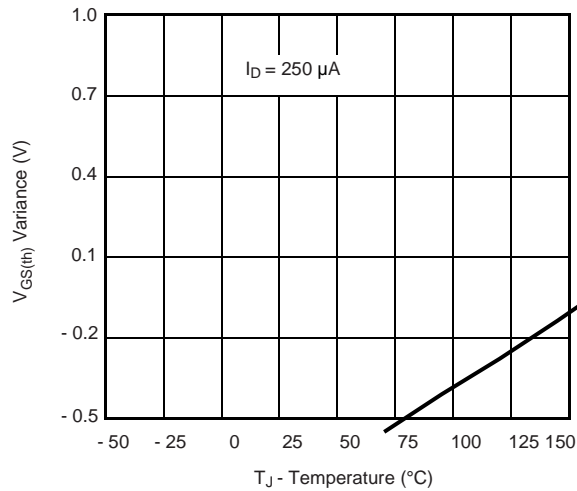
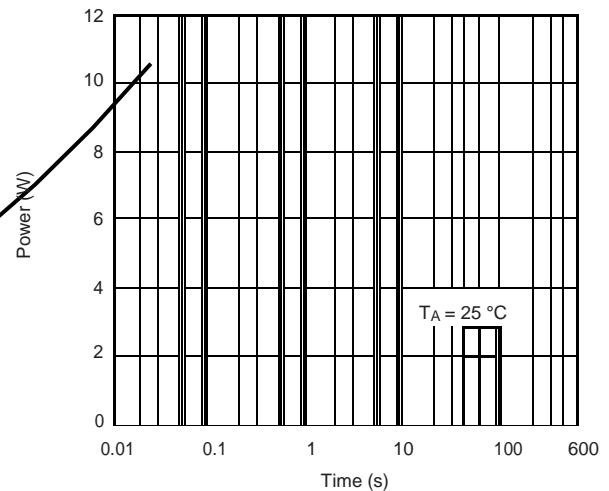
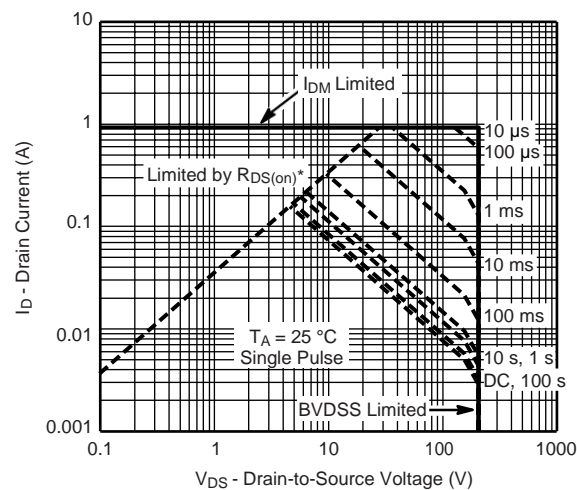
a. Pulse test:  $PW \leq 300\text{ }\mu\text{s}$  duty cycle  $\leq 2\%$ .

b. For DESIGN AID ONLY, not subject to production testing.

c. Switching time is essentially independent of operating temperature.

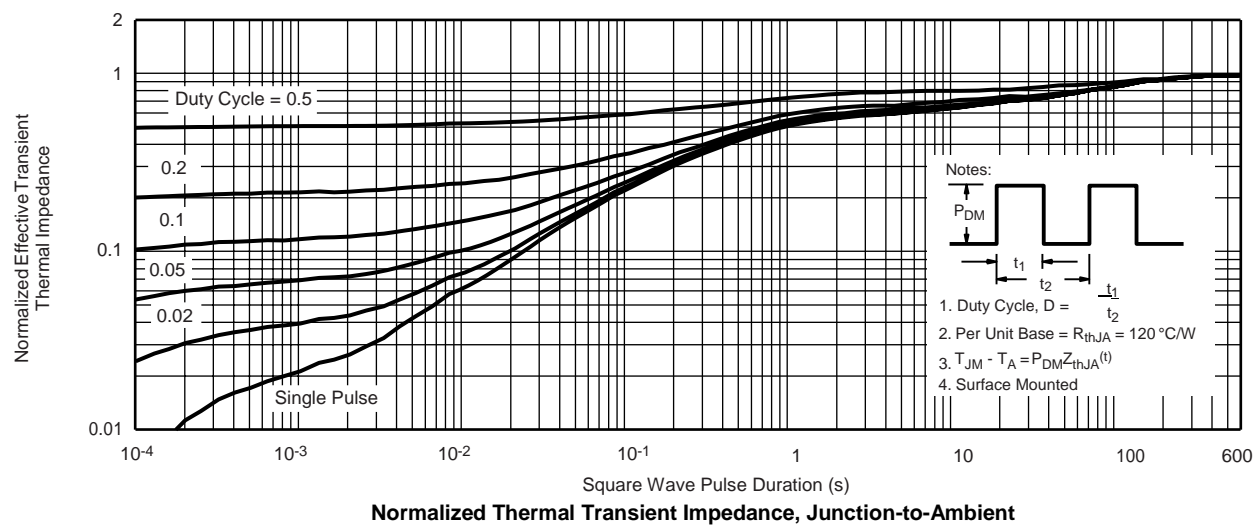
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted

**Output Characteristics**

**Transfer Characteristics**

**On-Resistance vs. Drain Current**

**Capacitance**

**Gate Charge**

**On-Resistance vs. Junction Temperature**

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted

**Source-Drain Diode Forward Voltage**

**On-Resistance vs. Gate-to-Source Voltage**

**Threshold Voltage**

**Single Pulse Power**


\*  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

**Safe Operating Area**

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted


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