

IRFM220A-VB Datasheet N-Channel 200 V (D-S) MOSFET

PRODUCT	SUMMARY	
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)
200	0.283 at V _{GS} = 10 V	3

FEATURES

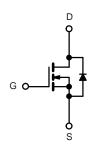
- Trench Power MOSFET
- 175 °C Junction Temperature
- PWM Optimized
- 100 % R_g Tested
 Compliant to RoHS Directive 2002/95/EC



APPLICATIONS

· Primary Side Switch





N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $(T_A = 2$	25 °C, unless othe	rwise noted)		
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V _{DS}	200	V
Gate-Source Voltage		V _{GS}	± 20	7 v
Continuous Drain Current (T _J = 175 °C) ^b	T _C = 25 °C	l-	3	
	T _C = 125 °C	- I _D	2.7	7
Pulsed Drain Current		I _{DM}	10	Α
Continuous Source Current (Diode Conduction)		I _S	6	
Continuous Source Current (Diode Conduction) Avalanche Current		I _{AS}	6	7
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	18	mJ
Maximum Dayor Dissination	T _C = 25 °C	P _D	96 ^b	W
Maximum Power Dissipation	T _A = 25 °C	L L D	3 ^a	- vv
Operating Junction and Storage Temperature Range	•	T _J , T _{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
hunding to Ambigut	t ≤ 10 s	D	15	18		
Junction-to-Ambient ^a	Steady State	R _{thJA}	40	50	°C/W	
Junction-to-Case (Drain)		R _{thJC}	0.85	1.1		

Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. See SOA curve for voltage derating.



Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	200			V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2		4	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V _{DS} = 200 V, V _{GS} = 0 V			1		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 200 V, V _{GS} = 0 V, T _J = 125 °C			50	μΑ	
		V _{DS} = 200 V, V _{GS} = 0 V, T _J = 175 °C			250		
On-State Drain Current ^b	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 10 V	40			Α	
Drain-Source On-State Resistance ^b		V _{GS} = 10 V, I _D = 3 A		0.283			
	D	V _{GS} = 10 V, I _D = 3 A, T _J = 125 °C		0.320		1	
	R _{DS(on)}	$V_{GS} = 10 \text{ V}, I_D = 3 \text{ A}, T_J = 175 ^{\circ}\text{C}$		0.350		Ω	
		V _{GS} = 6 V, I _D = 3 A		0.292			
Forward Transconductance ^b	9 _{fs}	V _{DS} = 15 V, I _D = 3 A		35		S	
Dynamic ^a							
Input Capacitance	C _{iss}			1800			
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, F = 1 \text{ MHz}$		180		pF	
Reverse Transfer Capacitance	C _{rss}			80			
Total Gate Charge ^c	Q_g			34	51		
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3 \text{ A}$		8		nC	
Gate-Drain Charge ^c	Q_{gd}			12			
Gate Resistance	R _g		0.5		2.9	Ω	
Turn-On Delay Time ^c	t _{d(on)}			15	25		
Rise Time ^c	t _r	$V_{DD} = 100 \text{ V}, R_L = 5.2 \Omega$		50	75	n -	
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong 3 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5 \Omega$		30	45	ns	
Fall Time ^c	t _f			60	90		
Source-Drain Diode Ratings and Char	acteristics (7	_C = 25 °C)					
Pulsed Current	I _{SM}				5	Α	
Diode Forward Voltage ^b	V_{SD}	I _F = 3 A, V _{GS} = 0 V		0.9	1.5	V	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 3 A, dI/dt = 100 A/µs		180	250	ns	

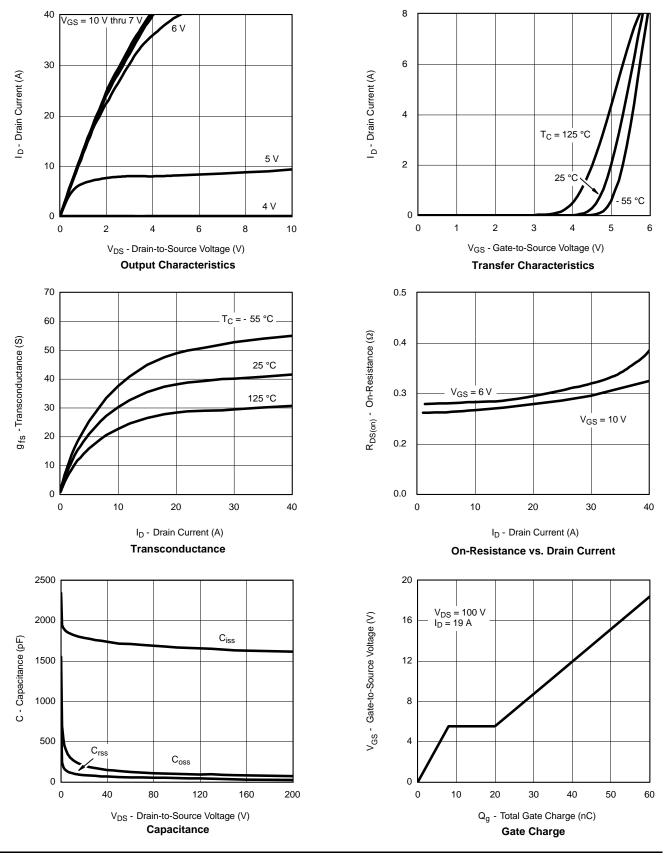
Notes:

- a. Guaranteed by design, not subject to production testing.
- b. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

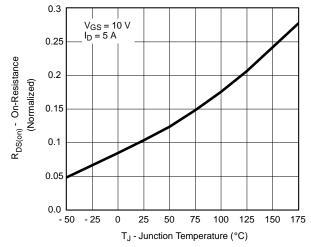


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

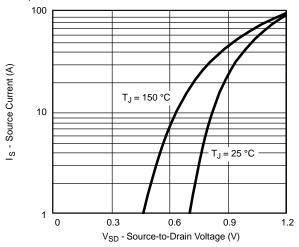




TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

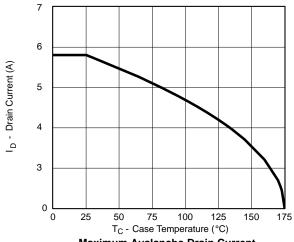


On-Resistance vs. Junction Temperature

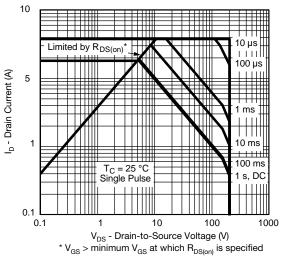


Source-Drain Diode Forward Voltage

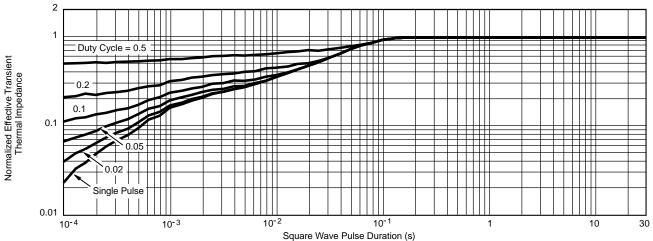
THERMAL RATINGS



Maximum Avalanche Drain Current vs. Case Temperature



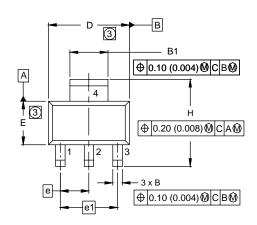
> minimum V_{GS} at which R_{DS(on)} is specified **Safe Operating Area**

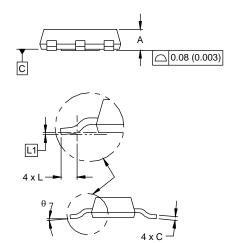


Normalized Thermal Transient Impedance, Junction-to-Case



SOT-223 (HIGH VOLTAGE)





DIM.	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
Α	1.55	1.80	0.061	0.071
В	0.65	0.85	0.026	0.033
B1	2.95	3.15	0.116	0.124
С	0.25	0.35	0.010	0.014
D	6.30	6.70	0.248	0.264
Е	3.30	3.70	0.130	0.146
е	2.30 BSC		0.0905 BSC	
e1	4.60	4.60 BSC		BSC
Н	6.71	7.29	0.264	0.287
L	0.91	-	0.036	-
L1	0.061 BSC		0.0024	4 BSC
θ	-	10'	-	10'

ECN: S-82109-Rev. A, 15-Sep-08

DWG: 5969

Notes

- 1. Dimensioning and tolerancing per ASME Y14.5M-1994.
- 2. Dimensions are shown in millimeters (inches).
- 3. Dimension do not include mold flash.
- 4. Outline conforms to JEDEC outline TO-261AA.



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