

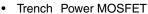
9435GK-VB Datasheet

P-Channel 35 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A) ^d	Q _g (Typ.)			
- 35	0.040 at V _{GS} = - 10 V	- 6.2	9.8 nC			
- 33	0.048 at V _{GS} = - 4.5 V	- 5.1	9.6110			

FEATURES





- 100 % R_g Tested
- 100 % UIS Tested
- Compliant to RoHS Directive 2002/95/EC



ROHS COMPLIANT HALOGEN FREE



P-Channel MOSFET

APPLICATIONS

- · Load Switches, Adaptor Switch
 - Notebook PCs

ABSOLUTE MAXIMUM RATINGS ($T_A = 25 ^{\circ}C$, unless oth	erwise noted)		
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	- 35	V	
Gate-Source Voltage		V _{GS}	± 20	v
	T _C = 25 °C		- 6.2	
Continuous Drain Current /T = 150 °C\	T _C = 70 °C	1 , [- 4.8	
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C	l _D	- 4.5 ^{a, b}	
	T _A = 70 °C	1	- 3.4 ^{a, b}	
Pulsed Drain Current	I _{DM}	- 20	Α	
0 11 0 0 10 11 0 1	T _C = 25 °C		- 3.5	
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	- 2.1 ^{a, b}	
Avalanche Current		I _{AS}	- 10	
Single-Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	5	mJ
	T _C = 25 °C		4.2	
Marian and David Disable ation	T _C = 70 °C	1 , [2.7	14/
Maximum Power Dissipation	T _A = 25 °C	P _D	2.5 ^{a, b}	W
	T _A = 70 °C		1.6 ^{a, b}	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{a, c}	t ≤ 10 s	R _{thJA}	40	50	°C/W	
Maximum Junction-to-Foot	Steady State	R _{thJF}	24	30	C/VV	

Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. t = 10 s.
- c. Maximum under steady state conditions is 85 °C/W.
- d. Based on T_C = 25 °C.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static				•			
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 35			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	L 050 vA		- 42		m\//°C	
V _{GS(th)} Temperature Coefficient	I _D = - 250 µA			4.6		mV/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 0.6		- 1.8	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zana Oaka Walka wa Busin Oawand		$V_{DS} = -35 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = -35 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			- 1	μΑ	
Zero Gate Voltage Drain Current	I _{DSS}				- 5		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge -10 \text{ V}, V_{GS} = -10 \text{ V}$	- 10			Α	
D : 0		V _{GS} = - 10 V, I _D = - 5 A		0.040			
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 4 A		0.048		Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 5 A		14		S	
Dynamic ^b				•	'		
Input Capacitance	C _{iss}			970			
Output Capacitance	C _{oss}	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		120		pF	
Reverse Transfer Capacitance	C _{rss}			95			
T		V _{DS} = - 20 V, V _{GS} = - 10 V, I _D = - 5 A		23	35		
Total Gate Charge	Q_g			9.8	16	1	
Gate-Source Charge	Q _{gs}	$V_{DS} = -20 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -5 \text{ A}$		3		nC	
Gate-Drain Charge	Q_{gd}			5.2			
Gate Resistance	R_g	f = 1 MHz	1.0	5.5	11	Ω	
Turn-On Delay Time	t _{d(on)}			7	14		
Rise Time	t _r	V_{DD} = - 20 V, R_L = 4 Ω		12	24		
Turn-Off DelayTime	t _{d(off)}	$I_D \cong -5 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$		30	60		
Fall Time	t _f			9	18		
Turn-On Delay Time	t _{d(on)}			44	80	ns	
Rise Time	t _r	V_{DD} = - 20 V, R_L = 4 Ω		33	60	1	
Turn-Off DelayTime	t _{d(off)}	$I_D \cong -5 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$		28	55		
Fall Time	t _f			13	25	1	
Drain-Source Body Diode Characterist	ics			•			
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 3.5		
Pulse Diode Forward Current	I _{SM}				- 20	A	
Body Diode Voltage	V _{SD}	I _S = - 2 A, V _{GS} = 0 V		- 0.76	- 1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			27	50	ns	
Body Diode Reverse Recovery Charge	Diode Reverse Recovery Charge Q			19	35	nC	
Reverse Recovery Fall Time	ta	I _F = - 2 A, dl/dt = 100 A/μs, T _J = 25 °C		14			
Reverse Recovery Rise Time	t _b			13		ns	

Notes:

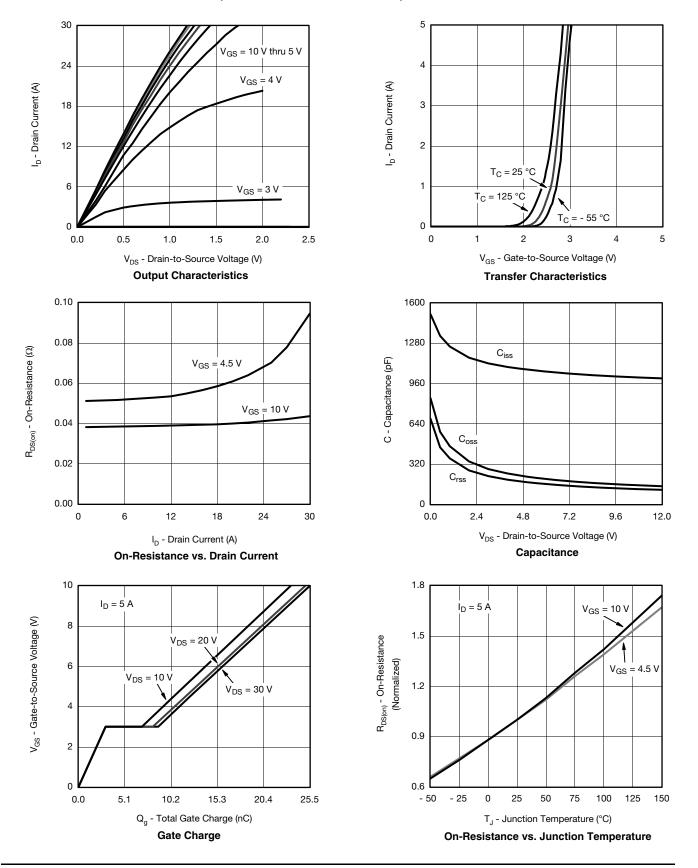
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- a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

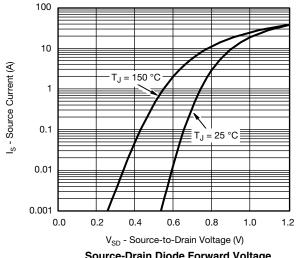


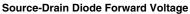
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

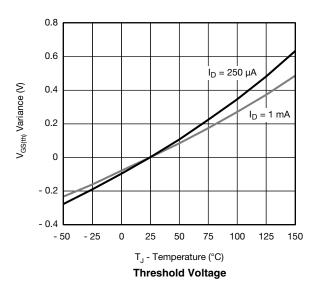


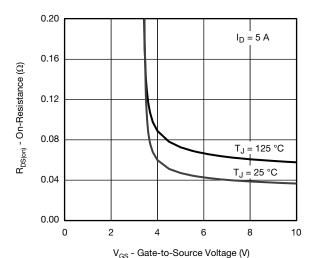


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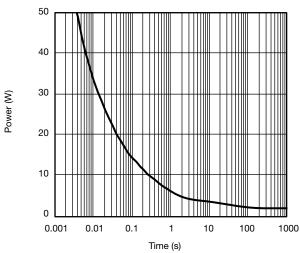




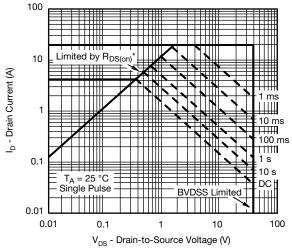




On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient

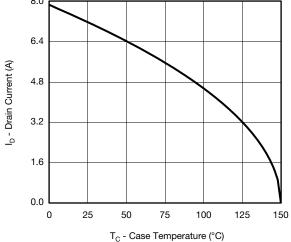


* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

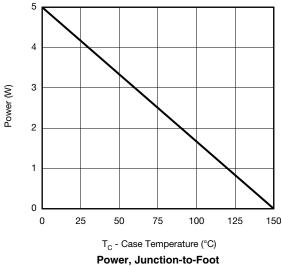
Safe Operating Area

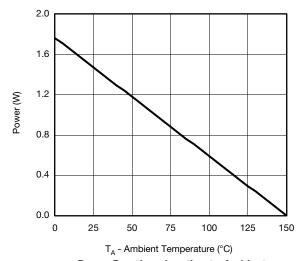


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating*





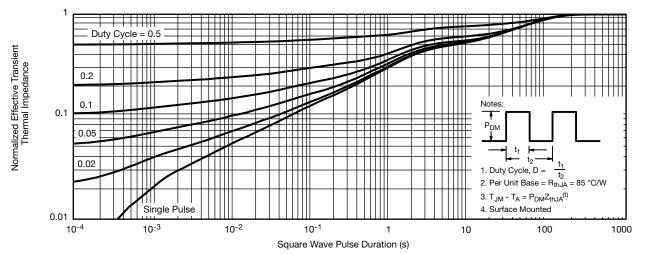
Power Derating, Junction-to-Ambient

^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

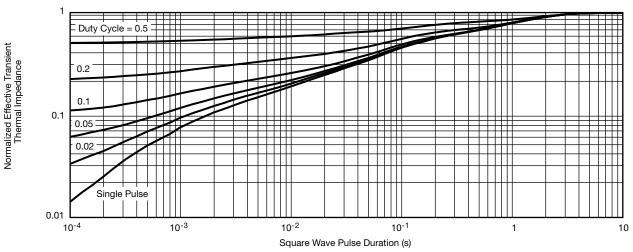
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



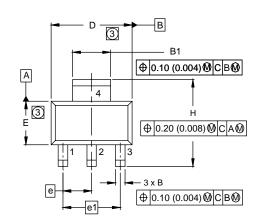
Normalized Thermal Transient Impedance, Junction-to-Ambient

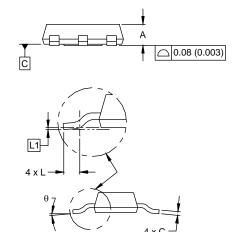


Normalized Thermal Transient Impedance, Junction-to-Foot



SOT-223 (HIGH VOLTAGE)





	MILLI	METERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
Α	1.55	1.80	0.061	0.071	
В	0.65	0.85	0.026	0.033	
B1	2.95	3.15	0.116	0.124	
С	0.25	0.35	0.010	0.014	
D	6.30	6.70	0.248	0.264	
E	3.30	3.70	0.130	0.146	
е	2.30 BSC		0.0905 BSC		
e1	4.60 BSC		0.181	BSC	
Н	6.71	7.29	0.264	0.287	
L	0.91	-	0.036	-	
L1	0.061 BSC		0.0024 BSC		
θ	-	10'	-	10'	
FCN: S-82109-Rev A 15-	San-08	•			

ECN: S-82109-Rev. A, 15-Sep-08

DWG: 5969

Notes

- 1. Dimensioning and tolerancing per ASME Y14.5M-1994.
- 2. Dimensions are shown in millimeters (inches).
- 3. Dimension do not include mold flash.
- 4. Outline conforms to JEDEC outline TO-261AA.



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