

## NTMS4P01R2G-VB Datasheet P-Channel 12-V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$I_{DS}(V)$ $R_{DS(on)}(\Omega)$ $I_{D}$				
	0.0050 at $V_{GS} = -4.5 \text{ V}$	- 16			
- 12	0.0065 at V <sub>GS</sub> = - 2.5 V	- 15			
	0.0100 at V <sub>GS</sub> = - 1.8 V	- 13			

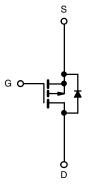
#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- Trench Power MOSFET
- Compliant to RoHS Directive 2002/95/EC

# Pb-free RoHS COMPLIANT HALOGEN FREE

#### **APPLICATIONS**

- · Load Switch
- · Battery Switch



P-Channel MOSFET

	SO-8	
S 1 S 2 S 3 G 4		8 D 7 D 6 D 5 D
	Top View	_

Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	- 12		V
Gate-Source Voltage		V <sub>GS</sub>	± 8		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	- 16	- 10	^
	T <sub>A</sub> = 70 °C		- 11.5	- 8	
Pulsed Drain Current		I <sub>DM</sub>	- 50		Α
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	- 2.7	- 1.36	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	В	3.0	1.5	W
	T <sub>A</sub> = 70 °C	$P_{D}$	1.9	0.95	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 10 s	- R <sub>thJA</sub>	33	42	°C/W	
	Steady State		70	84		
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	16	21	]	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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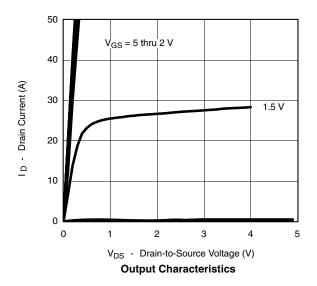
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			<u>'</u>				
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -600 \mu A$	- 0.5	-	1.0	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$	± 100		± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 12 V, V <sub>GS</sub> = 0 V			- 1		
		V <sub>DS</sub> = - 12 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 70 °C	= 70 °C		- 10	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 30			Α	
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 14 A		0.0050		Ω	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 13 A		0.0065			
		V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 12 A		0.0100			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 6 V, I <sub>D</sub> = - 14 A		80		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = - 2.7 A, V <sub>GS</sub> = 0 V - 0.		- 0.6	- 1.1	V	
Dynamic <sup>b</sup>							
Total Gate Charge	$Q_g$			110	165	nC	
Gate-Source Charge	$Q_{gs}$	$V_{DS} = -6 \text{ V}, V_{GS} = -5 \text{ V}, I_{D} = -14 \text{ A}$		15			
Gate-Drain Charge	$Q_{gd}$			27.5			
Turn-On Delay Time	t <sub>d(on)</sub>			110	170	- ns	
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 6 V, $R_L$ = 6 $\Omega$		235	350		
Turn-Off Delay Time	t <sub>d(off)</sub>	$\text{I}_\text{D}\cong$ - 1 A, $\text{V}_\text{GEN}$ = - 4.5 V, $\text{R}_\text{g}$ = 6 $\Omega$		410	620		
Fall Time	t <sub>f</sub>			285	430		
Gate Resistance	$R_{g}$			3.6		Ω	
Source-Drain Reverse Recovery Time t <sub>rr</sub>		I <sub>F</sub> = - 2.1 A, dI/dt = 100 A/μs		180	270	ns	

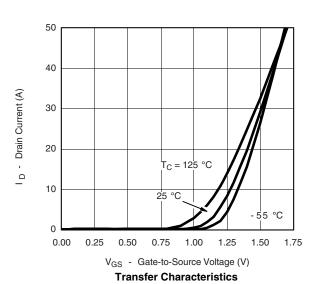
#### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C unless otherwise noted

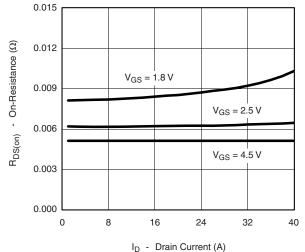




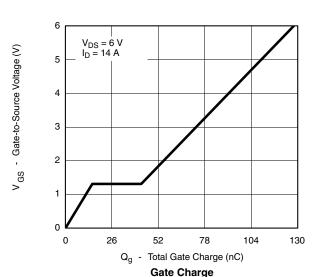
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#### TYPICAL CHARACTERISTICS 25 °C unless otherwise noted



On-Resistance vs. Drain Current



T<sub>J</sub> = 150 °C

T<sub>J</sub> = 25 °C

T<sub>J</sub> = 25 °C

O.1

O.0

O.2

O.4

O.6

O.8

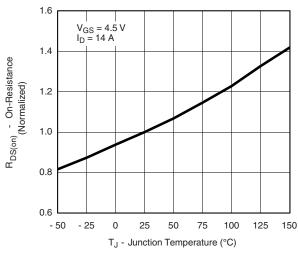
I.2

V<sub>SD</sub> - Source-to-Drain Voltage (V)

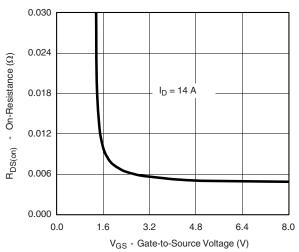
Source-Drain Diode Forward Voltage

12000 10000 C - Capacitance (pF) 8000 6000 Coss 4000  $\mathsf{C}_{\mathsf{rss}}$ 2000 0 0 2 4 6 8 10 12 V<sub>DS</sub> - Drain-to-Source Voltage (V)

Capacitance



On-Resistance vs. Junction Temperature

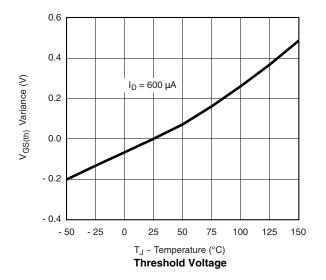


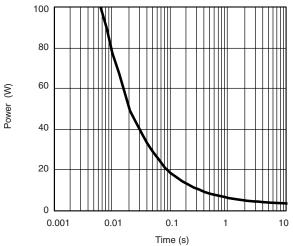
On-Resistance vs. Gate-to-Source Voltage

30

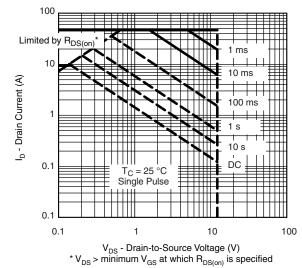


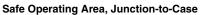
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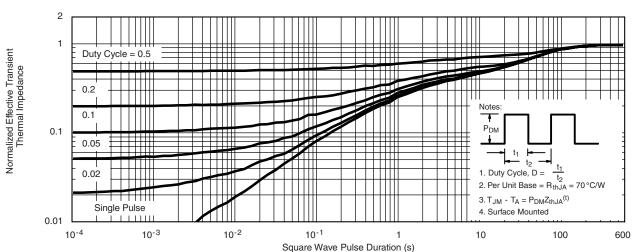




Single Pulse Power, Junction-to-Ambient





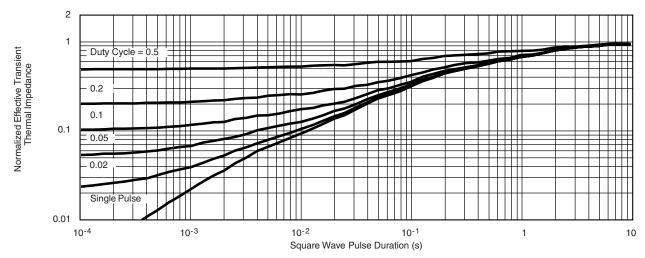


Normalized Thermal Transient Impedance, Junction-to-Ambient

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#### TYPICAL CHARACTERISTICS 25 °C unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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