

### IRF7492TRPBF-VB Datasheet N-Channel 200-V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)		
200	0.065 at V <sub>GS</sub> = 10 V	5.2		
	0.072 at V <sub>GS</sub> = 6.0 V	4.1		

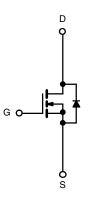
#### FEATURES

- Halogen-free According to IEC 61249-2-21
  Definition
- Trench Power MOSFETs
- Compliant to RoHS Directive 2002/95/EC



HALOGEN FREE Available

SO-8 S D 8 1 S D 2 7 S D 6 3 G D 4 5 Top View



N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> $T_A = 25 \degree C$ , unless otherwise noted					
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	200		V
Gate-Source Voltage		V <sub>GS</sub>	± 20		
Continuous Drain Current /T 150 °C)a	T <sub>A</sub> = 25 °C	L	5.2	3.35	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C	- I <sub>D</sub>	4.6	2.7	
Pulsed Drain Current		I <sub>DM</sub>	40		А
Avalanch Current	L = 0.1 mH	I <sub>AS</sub>	15		
Continuous Source Current (Diode Conduction) <sup>a</sup>	I <sub>S</sub>	2.6	1.3		
	T <sub>A</sub> = 25 °C	P <sub>D</sub>	3.1 1.56		W
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C		2.0	1.0	vv
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55	to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Manimum has bien to Anabien 18	t ≤ 10 s	R <sub>thJA</sub>	33	40	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State	<sup>1</sup> 'thJA	65	80	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	17	21	

Notes:

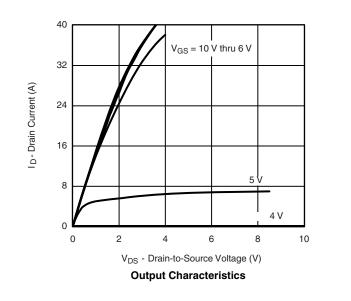
a. Surface Mounted on 1" x 1" FR4 board.

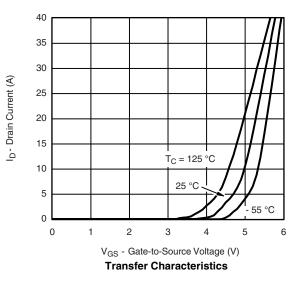
<b>SPECIFICATIONS</b> $T_J = 25 \text{ °C}$ , unless otherwise noted								
Parameter	Symbol	ymbol Test Conditions		Тур.	Max.	Unit		
Static								
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$ 2				V		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V$ , $V_{GS} = \pm 20 V$			± 100	nA		
Zara Cata Valtaga Drain Current	1	$V_{DS} = 160 \text{ V}, V_{GS} = 0 \text{ V}$			1			
Zero Gate Voltage Drain Current	IDSS	$V_{DS}$ = 160 V, $V_{GS}$ = 0 V, $T_{J}$ = 55 °C			5	μΑ		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5$ V, $V_{GS}$ = 10 V	40			А		
_	Б	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 4.0 \text{ A}$		0.065		0		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 6.0 \text{ V}, \text{ I}_{D} = 4.0 \text{ A}$		0.072		Ω		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 5 \text{ A}$		19		S		
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_{\rm S} = 2.8$ A, $V_{\rm GS} = 0$ V		0.75	1.2	V		
Dynamic <sup>b</sup>								
Total Gate Charge	Qg			34	42			
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 4.0 \text{ A}$		7.5		nC		
Gate-Drain Charge	Q <sub>gd</sub>			12.0		1		
Gate Resistance	Rg		0.2	0.85	1.3	Ω		
Turn-On Delay Time	t <sub>d(on)</sub>			14	20			
Rise Time	t <sub>r</sub>	$V_{DD}$ = 100 V, $R_L$ = 25 $\Omega$		20	30			
Turn-Off Delay Time	t <sub>d(off)</sub>	$\text{I}_\text{D}\cong$ 4.0 A, $\text{V}_\text{GEN}$ = 10 V, $\text{R}_\text{g}$ = 6 $\Omega$		32	50	ns		
Fall Time	t <sub>f</sub>			25	35			
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	$I_F = 2.8 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}$		70	100			

Notes: a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %. b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

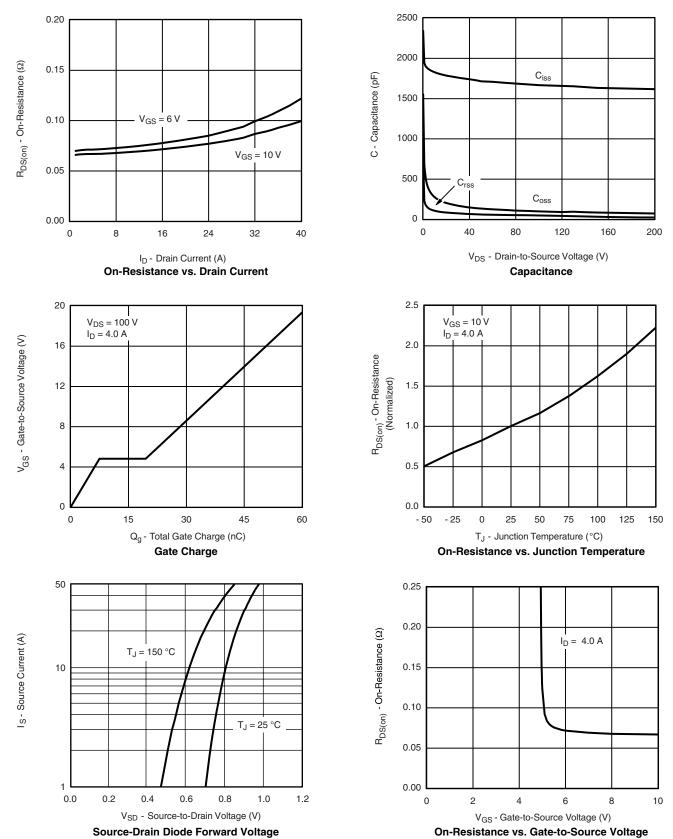




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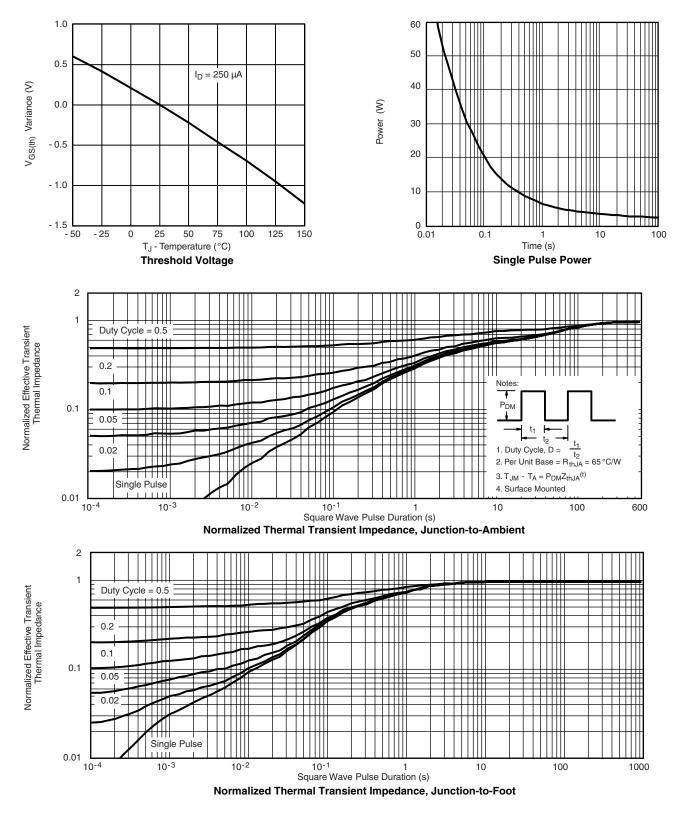


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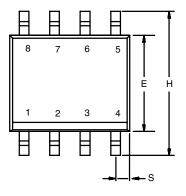


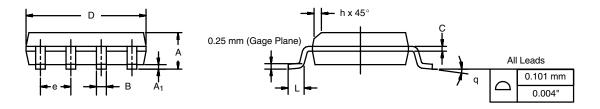
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





# SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012



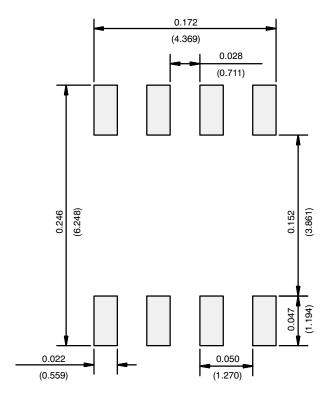


	MILLIM	IETERS	INCHES			
DIM	Min	Мах	Min	Max		
A	1.35	1.75	0.053	0.069		
A <sub>1</sub>	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
E	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050 BSC			
н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498						

### IRF7492TRPBF-VB



**RECOMMENDED MINIMUM PADS FOR SO-8** 



Recommended Minimum Pads Dimensions in Inches/(mm)



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