

AP9997GM-VB Datasheet

Dual N-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (Ω) Max.	I _D (A) ^a	Q _g (Typ.)			
	0.012 at V _{GS} = 10 V	12				
100	0.013 at V _{GS} = 7.5 V	11	20.7 nC			
	0.014 at V _{GS} = 4.5 V	10				

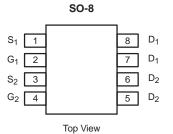
FEATURES

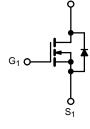
- Trench Power MOSFET
- 100 % R^g and UIS Tested

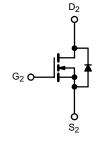
APPLICATIONS

- DC/DC Primary Side Switch
- Telecom/Server
- Industrial









N-Channel MOSFET

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N-Channel MOSFET

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V _{DS}	100	V	
Gate-Source Voltage		V _{GS}	± 20		
	T _C = 25 °C		12		
	T _C = 70 °C		9.6		
Continuous Drain Current ($T_J = 150 \ ^{\circ}C$)	T _A = 25 °C	I _D	10 ^{b, c}		
	T _A = 70 °C		8.3 ^{b, c}		
Pulsed Drain Current (t = 300 μs)		I _{DM}	45	A	
Continuous Source-Drain Diode Current	T _C = 25 °C		5.4		
	T _A = 25 °C	I _S	2.7 ^{b, c}		
Single Pulse Avalanche Current		I _{AS}	30		
Avalanche Energy	L = 0.1 mH	E _{AS}	45	mJ	
	T _C = 25 °C		6		
Maximum Davian Disaination	T _C = 70 °C		3.8		
Maximum Power Dissipation	T _A = 25 °C	P _D	3 ^{b, c}	W	
	T _A = 70 °C	1 –	1.9 ^{b, c}		
Operating Junction and Storage Temperature	T _J , T _{stg}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 s	R _{thJA}	33	42	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	16	21	C/ W		

Notes:

a. Based on $T_C = 25 \ ^{\circ}C$.

b. Surface mounted on 1" x 1" FR4 board.

c. t = 10 s.

d. Maximum under steady state conditions is 85 $^{\circ}\text{C/W}.$

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	•,			.,,			
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	100			V	
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J			64		mV/°C	
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J	I _D = 250 μA		- 5.8			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_{D} = 250 \ \mu A$	1.0		2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$	-		± 100	nA	
	I _{DSS}	$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current		$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 \text{ °C}$			10	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 V, V_{GS} = 10 V$	30			Α	
	2(01)	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 10 \text{ A}$		0.012			
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 7.5 V, I _D = 10 A		0.013		Ω	
	20(01)	$V_{GS} = 4.5 \text{ V}, I_D = 8\text{A}$		0.014		1	
Forward Transconductance ^a	g _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 10 \text{ A}$		54		S	
Dynamic ^b	0.0				I		
Input Capacitance	C _{iss}			1970		1	
Output Capacitance	C _{oss}	V _{DS} = 50 V, V _{GS} = 0 V, f = 1 MHz		695		pF	
Reverse Transfer Capacitance	C _{rss}			62			
		$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 8 \text{ A}$		44.4	67		
Total Gate Charge	Q _g		20.7	31	1		
Gate-Source Charge	Q _{gs}	$V_{DS} = 50 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 8 \text{ A}$		6.1		nC	
Gate-Drain Charge	Q _{gd}			9.1			
Output Charge	Q _{oss}	$V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}$		56	85		
Gate Resistance	Rg	f = 1 MHz	0.4	1.1	2.2	Ω	
Turn-On Delay Time	t _{d(on)}			15	30		
Rise Time	t _r	$V_{DD} = 50 \text{ V}, \text{ R}_{L} = 5 \Omega$		11	22	- ns -	
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong$ 10 A, V_GEN = 7.5 V, R_g = 1 Ω		31	60		
Fall Time	t _f			10	20		
Turn-On Delay Time	t _{d(on)}			12	24		
Rise Time	t _r	V_{DD} = 50 V, R_L = 5 Ω		10	20		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D} \cong$ 10 A, V_GEN = 10 V, R_g = 1 Ω		34	65		
Fall Time	t _f			10	20		
Drain-Source Body Diode Characteristi	cs	·					
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			5.4	_	
Pulse Diode Forward Current ^a	I _{SM}				70	A	
Body Diode Voltage	V _{SD}	I _S = 5 A		0.76	1.1	V	
Body Diode Reverse Recovery Time	t _{rr}			42	80	ns	
Body Diode Reverse Recovery Charge	Q _{rr}			40	80	nC	
Reverse Recovery Fall Time	t _a	$I_F = 10 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^\circ\text{C}$		19	1		
Reverse Recovery Rise Time	t _b			23	1	ns	

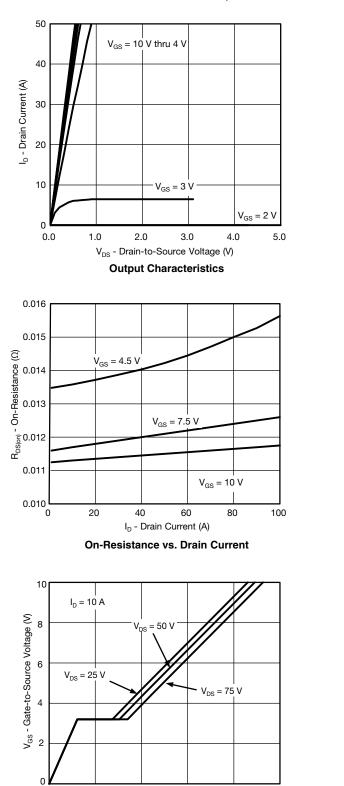
Notes:

a. Pulse test; pulse width \leq 300 μs , duty cycle \leq 2 % b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

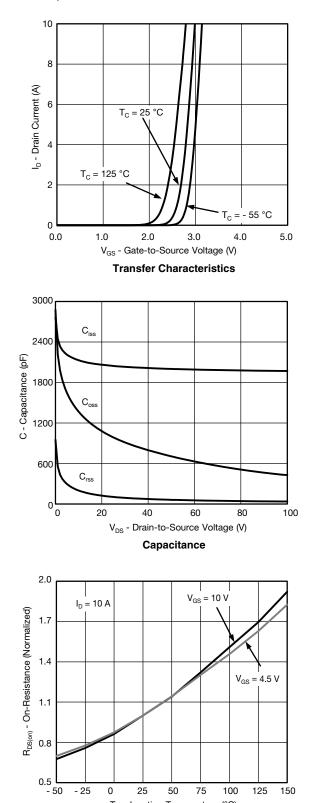


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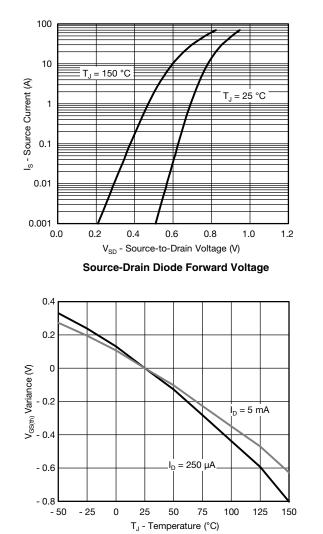


T_J - Junction Temperature (°C) On-Resistance vs. Junction Temperature

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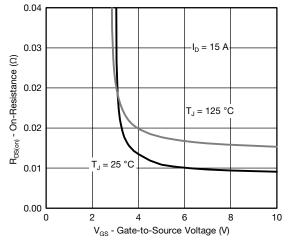
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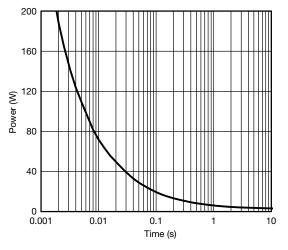


Threshold Voltage

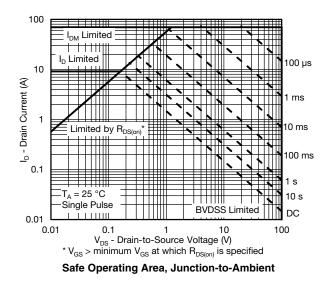
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



On-Resistance vs. Gate-to-Source Voltage

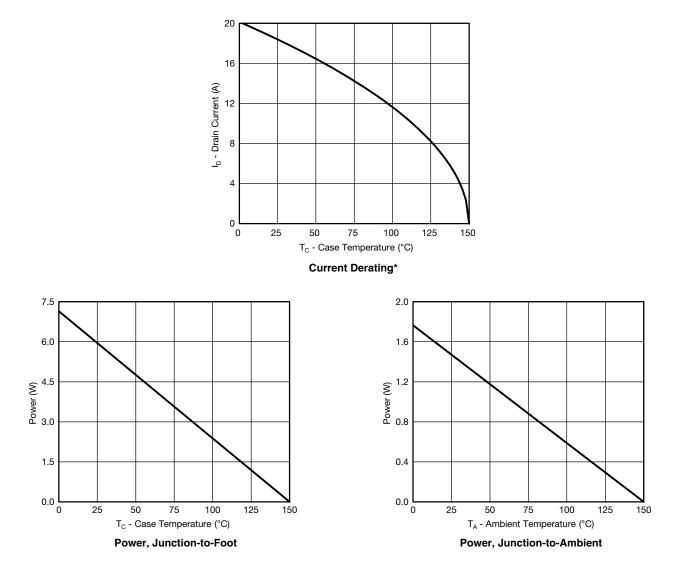


Single Pulse Power, Junction-to-Ambient





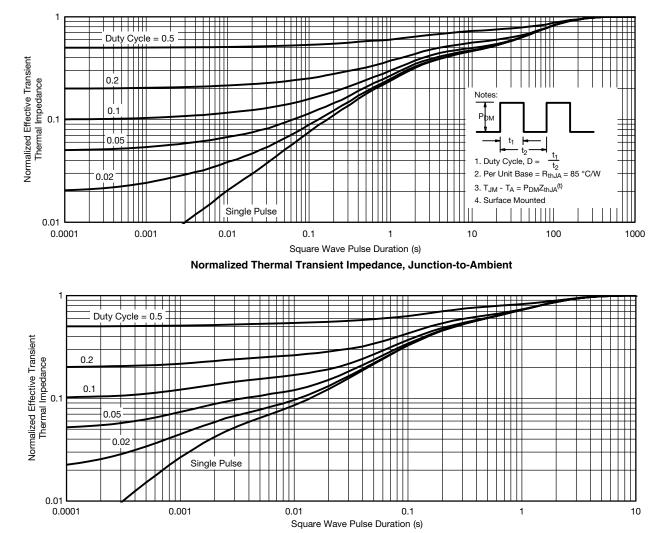
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



* The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





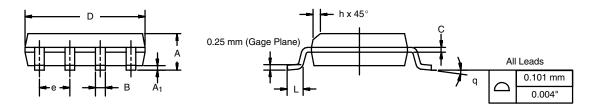


Normalized Thermal Transient Impedance, Junction-to-Foot



SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012

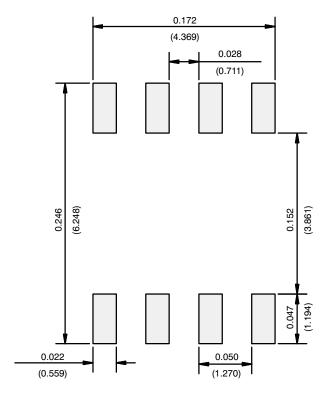




	MILLIM	IETERS	INCHES		
DIM	Min	Мах	Min	Max	
A	1.35	1.75	0.053	0.069	
A ₁	0.10	0.20	0.004	0.008	
В	0.35	0.51	0.014	0.020	
С	0.19	0.25	0.0075	0.010	
D	4.80	5.00	0.189	0.196	
E	3.80	4.00	0.150	0.157	
е	1.27 BSC		0.050 BSC		
н	5.80	6.20	0.228	0.244	
h	0.25	0.50	0.010	0.020	
L	0.50	0.93	0.020	0.037	
q	0°	8°	0°	8°	
S	0.44	0.64	0.018	0.026	
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498					



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)



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