SO-8

Top View

 $D_2$ 1

D<sub>2</sub> 2

G<sub>1</sub>

S<sub>1</sub>/A

3

4

8 G<sub>2</sub>

D1/S2/C

 $D_1/S_2/C$ 

 $D_1/S_2/C$ 

7

6

5



## AP6901GSM-HF-VB Datasheet Dual N-Channel 30-V (D-S) MOSFET

Pin name

DRAIN2

DRAIN2

GATE1

SOURCE1/ANODE

DRAIN1/SOURCE2/CATHODE

DRAIN1/SOURCE2/CATHODE

DRAIN1/SOURCE2/CATHODE

GATE2

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)			
30	0.008 at V <sub>GS</sub> = 10 V	8	15 nC			
	0.012 at V <sub>GS</sub> = 4.5 V	6.8	15 110			

Pin No.

1

2

3

4

5

6

7

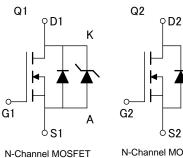
8

#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- Trench Power MOSFET
- 100 % UIS Tested ٠
- 100 % R<sub>g</sub> Tested
- Compliant to RoHS Directive 2002/95/EC

#### **APPLICATIONS**

- Set Top Box
- Low Current DC/DC ٠



N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> $T_A = 25 \text{ °C}$ , unless otherwise noted					
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V <sub>DS</sub>	30	V	
Gate-Source Voltage		V <sub>GS</sub>	± 20	V	
	$T_{\rm C} = 25 ^{\circ}{\rm C}$	-	8a		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 70 °C	I <sub>D</sub>	6.2		
	T <sub>A</sub> = 25 °C	5	7.2 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		6.2 <sup>b, c</sup>	A	
Pulsed Drain Current		I <sub>DM</sub>	40	~	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	_ I <sub>S</sub>	2.25		
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C		1.48 <sup>b, c</sup>		
Single Pulse Avalanche Current	Avalanche Current L = 0.1 mH		5		
Single Pulse Avalanche Energy	L = 0.1 mm	E <sub>AS</sub>	1.25	mJ	
	T <sub>C</sub> = 25 °C	P <sub>D</sub>	2.7		
Maximum Power Dissipation	T <sub>C</sub> = 70 °C		1.77	W	
Maximum Fower Dissipation	T <sub>A</sub> = 25 °C		1.78 <sup>b, c</sup>	VV	
	T <sub>A</sub> = 70 °C	]	1.14 <sup>b, c</sup>		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a, c, d</sup>	t ≤ 10 s	R <sub>thJA</sub>	58	70	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	38	45	0/10	

Notes:

a. Package limited,  $T_C = 25 \ ^{\circ}C$ .

b. Surface Mounted on 1" x 1" FR4 board.

c. t = 10 s.

d. Maximum under Steady State conditions is 110 °C/W.

COMPLIANT

HALOGEN

FREE



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			•		1	1	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	30			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	1 250 4		32		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA		- 5.0			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	1.0		2.5	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
		$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	1	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 30 V, V_{GS} = 0 V, T_{J} = 55 °C$			10	μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 V, V_{GS} = 10 V$	10			А	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5 A	0.008			+	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 4 \text{ A}$		0.012		Ω	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	$V_{DS} = 10 \text{ V}, I_D = 5 \text{ A}$		16		S	
Dynamic <sup>b</sup>	1 -10 1						
Input Capacitance	C <sub>iss</sub>			586		pF	
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz		117			
Reverse Transfer Capacitance	C <sub>rss</sub>			55			
Total Gate Charge	Q <sub>g</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 5 \text{ A}$		15	-		
				3.7	5.6	nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 5 \text{ A}$		1.4			
Gate-Drain Charge	Q <sub>gd</sub>			1.05			
Gate Resistance	R <sub>g</sub>	f = 1 MHz	0.8	4.3	8.6	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			12	24		
Rise Time	t <sub>r</sub>	$V_{DD} = 15 \text{ V}, \text{ R}_{\text{I}} = 3 \Omega$		55	100	-	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 5 \text{ A}, \text{ V}_{\text{GEN}} = 4.5 \text{ V}, \text{ R}_{\text{g}} = 1 \Omega$		11	22		
Fall Time	t <sub>f</sub>			8	16		
Turn-On Delay Time	t <sub>d(on)</sub>			4	8	ns	
Rise Time	t <sub>r</sub>	$V_{DD} = 15 \text{ V}, \text{ R}_{\text{I}} = 3 \Omega$		9	18	-	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 5 \text{ A}, V_{GEN} = 10 \text{ V}, \text{ R}_g = 1 \Omega$		10	20		
Fall Time	t <sub>f</sub>	-		6	12		
Drain-Source Body Diode Characteristi	cs				L		
Continuous Source-Drain Diode Current	۱ <sub>S</sub>	T <sub>C</sub> = 25 °C			2.25		
Pulse Diode Forward Current	I <sub>SM</sub>				24	A	
Body Diode Voltage	V <sub>SD</sub>	$I_{S} = 2 \text{ A}, V_{GS} = 0 \text{ V}$		0.8	1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>		1	11	20	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			4	8	nC	
Reverse Recovery Fall Time	ta	$I_F = 5 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, \text{ T}_J = 25 \text{ °C}$		7			
Reverse Recovery Rise Time				4		ns	

Notes:

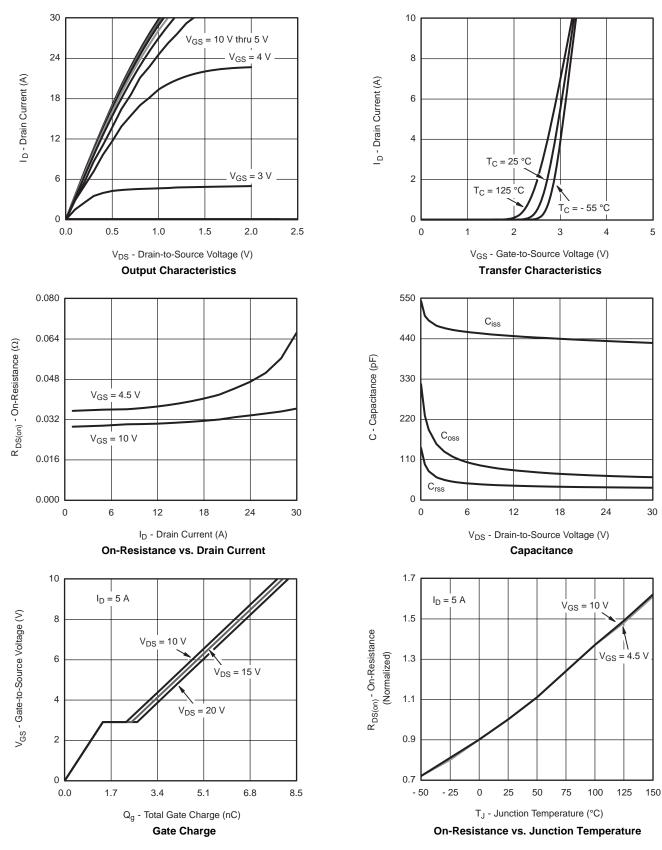
a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

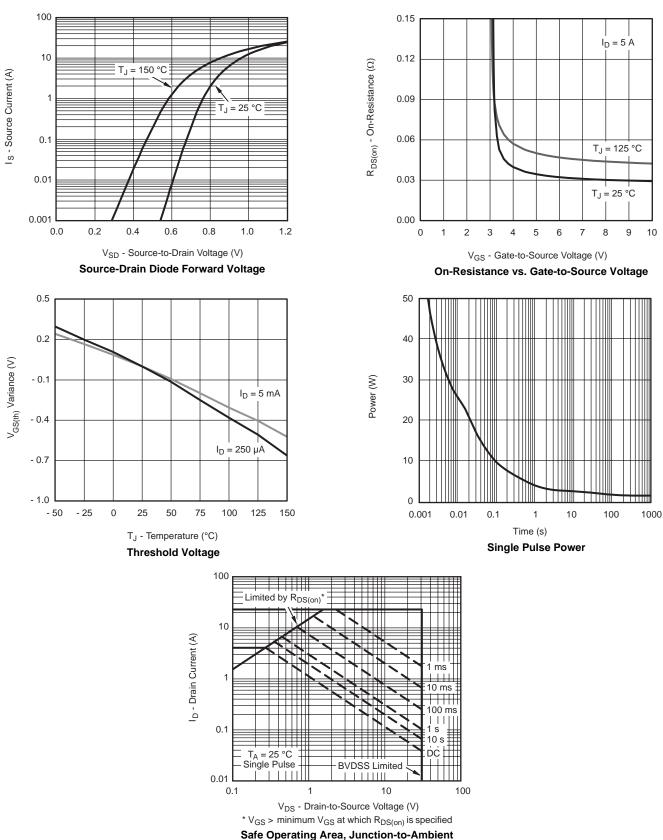






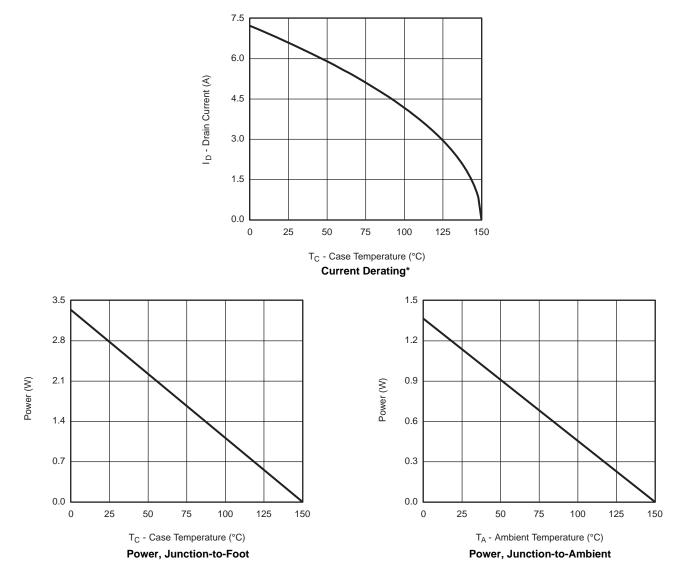








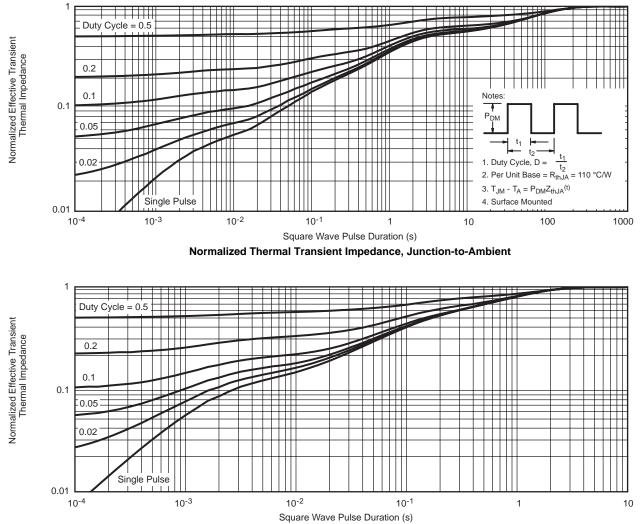
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



\* The power dissipation  $P_D$  is based on  $T_{J(max)}$  = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

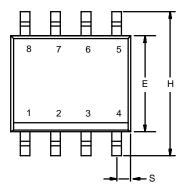


Normalized Thermal Transient Impedance, Junction-to-Foot



## SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012

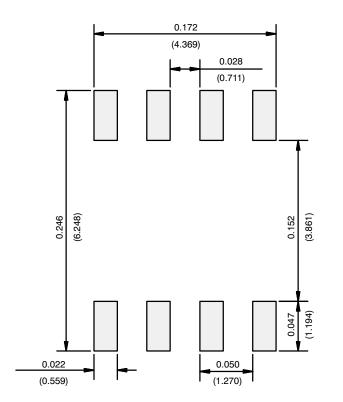




	MILLIMETERS		INCHES		
DIM	Min	Мах	Min	Max	
A	1.35	1.75	0.053	0.069	
A <sub>1</sub>	0.10	0.20	0.004	0.008	
В	0.35	0.51	0.014	0.020	
С	0.19	0.25	0.0075	0.010	
D	4.80	5.00	0.189	0.196	
E	3.80	4.00	0.150	0.157	
е	1.27 BSC		0.050 BSC		
н	5.80	6.20	0.228	0.244	
h	0.25	0.50	0.010	0.020	
L	0.50	0.93	0.020	0.037	
q	0°	8°	0°	8°	
S	0.44	0.64	0.018	0.026	
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498					



**RECOMMENDED MINIMUM PADS FOR SO-8** 



Recommended Minimum Pads Dimensions in Inches/(mm)

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