

SSM6P15FE-VB Datasheet Dual P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)	Q _g (TYP.) (nC)	
	0.450 at V _{GS} = -4.5 V	-0.55		
-20	0.500 at V _{GS} = -2.5 V	-0.50	1	
	0.600 at V _{GS} = -1.8 V	-0.38		

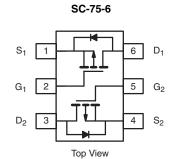
FEATURES

- Trench power MOSFET
- 100 % R tested
- Fast switching speed



APPLICATIONS

- Load / power switch for portable devices
- Drivers: relays, solenoids, displays
- Battery operated systems



ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)						
PARAMETER		SYMBOL	LIMIT	UNIT		
Drain-Source Voltage		V_{DS}	-20	V		
Gate-Source Voltage		V_{GS}	± 8	7 v		
Continuous Drain Current /T 150 °C)	T _A = 25 °C		-0.55 b, c			
Continuous Drain Current (T _J = 150 °C)	T _A = 70 °C	I _D	-0.45 ^{b, c}	A		
Pulsed Drain Current (t = 300 μs)		I _{DM}	-1.8			
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	-0.16 ^{b, c}			
Maximum Dawar Dissination	T _A = 25 °C	В	0.19 ^{b, c}	w		
Maximum Power Dissipation	T _A = 70 °C	P _D	0.12 ^{b, c}	VV		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +150	°C		

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum Junction-to-Ambient ^{a, b}	t ≤ 5 s	R _{thJA}	440	530	°C/W	
iviaximum sunction-to-ambient 4, 2	Steady State		540	650		

Notes

- a. Maximum under steady state conditions is 650 °C/W.
- b. Surface mounted on 1" x 1" FR4 board.
- $c. \ t=5 \ s.$

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static	STWIBOL	TEST CONDITIONS	IVIIIV.	117.	WAX.	ONIT	
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0, I _D = -250 μA	-20	I _	_	l v	
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	VGS = 0, ID = 200 μΛ	-	-12	-	•	
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J	$I_D = -250 \mu A$		1.8	_	mV/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$	-0.4	-	-1	V	
date double Threshold Voltage	▼GS(tn)	$V_{DS} = V_{GS}, I_D = 230 \mu\text{A}$ $V_{DS} = 0 \text{V}, V_{GS} = \pm 8 \text{V}$	-	_	± 30	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$	-	_	± 1		
	I _{DSS} -	$V_{DS} = -20 \text{ V}, V_{GS} = -2.3 \text{ V}$	_	_	 -1	μΑ	
Zero Gate Voltage Drain Current		$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 85 \text{ °C}$	_	_	-10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, 13 = 60 \text{ C}$ $V_{DS} = 25 \text{ V}, V_{GS} = -4.5 \text{ V}$	-1.5	_	-	Α	
On State Brain Garrent	iD(on)	$V_{GS} = -4.5 \text{ V}, V_{GS} = -4.5 \text{ V}$	-	0.450	_		
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -2.5 \text{ V}, I_D = -0.2 \text{ A}$	_	0.500	_	Ω	
Brain Godres on Grace Hodiotanies	1 103(011)	$V_{GS} = -1.8 \text{ V}, I_D = -0.1 \text{ A}$	_	0.600	_	1	
Forward Transconductance	9 _{fs}	V _{DS} = -10 V, I _D = 0.4 A	_	1	_	S	
Dynamic b	915	V _{DS} = 10 V, I _D = 5.174					
Input Capacitance	C _{iss}		_	45	_		
Output Capacitance	C _{oss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	15	-	pF	
Reverse Transfer Capacitance	C _{rss}	20 1 , de 1 ,	_	10	-		
·	Q _g -	V _{DS} = -10 V, V _{GS} = -4.5 V, I _D = -0.4 A	-	1.65	2.50	+	
Total Gate Charge		, do , b	-	1	2	nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = -0 \text{ V}, V_{GS} = -2.5 \text{ V}, I_D = -0.4$	-	0.2	-		
Gate-Drain Charge	Q _{gd}		_	0.26	-		
Gate Resistance	R_g	f = 1 MHz	2.4	12	24	Ω	
Turn-On Delay Time	t _{d(on)}		-	9	18		
Rise Time	t _r	$V_{DD} = -10 \text{ V}, R_{L} = 33.3 \Omega$	-	10	20	-	
Turn-Off DelayTime	t _{d(off)}	$I_D \cong -0.3 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$	-	10	20		
Fall Time	t _f		-	8	16		
Turn-On Delay Time	t _{d(on)}		-	1	2	ns	
Rise Time	t _r	$V_{DD} = -10 \text{ V}, R_{L} = 33.3 \Omega$	-	8	16	1 - 1	
Turn-Off DelayTime	t _{d(off)}	$I_D \cong$ -0.3 A, V_{GEN} = -8 V, R_g = 1 Ω	-	9	18		
Fall Time	t _f		-	5	10		
Drain-Source Body Diode Characteris	tics						
Pulse Diode Forward Current ^a	I _{SM}		-	-	-1.5	Α	
Body Diode Voltage	V_{SD}	I _S = -0.3 A	-	-0.8	-1.2	V	
Body Diode Reverse Recovery Time	t _{rr}		-	16	24	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = -0.3 A, dl/dt = 100 A/μs		8	16	nC	
Reverse Recovery Fall Time	ta			11	-		
Reverse Recovery Rise Time	t _b		-	5	-	ns	

Notes

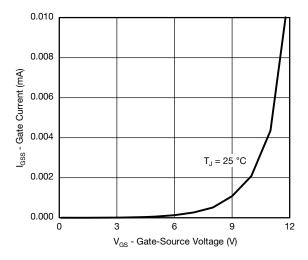
- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

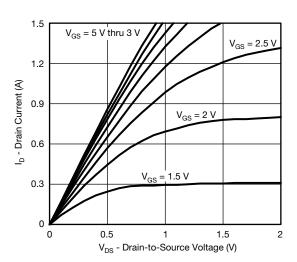
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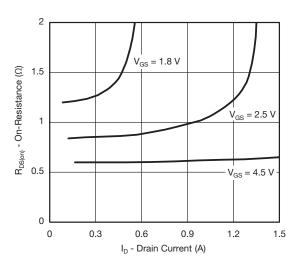
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



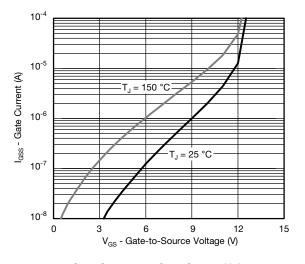
Gate Current vs. Gate-Source Voltage



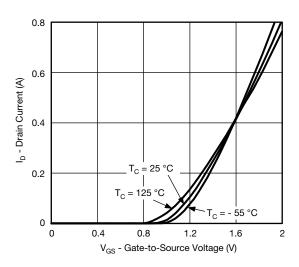
Output Characteristics



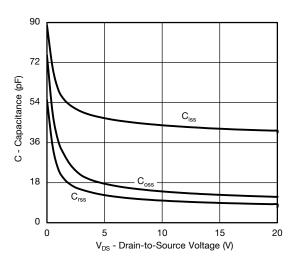
On-Resistance vs. Drain Current



Gate Current vs. Gate-Source Voltage



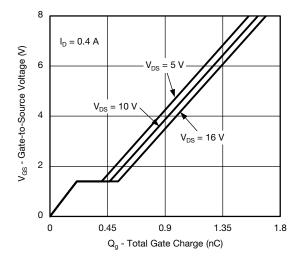
Transfer Characteristics



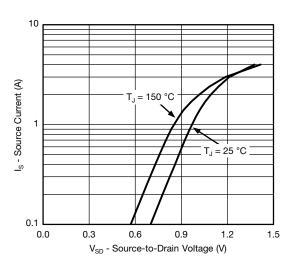
Capacitance



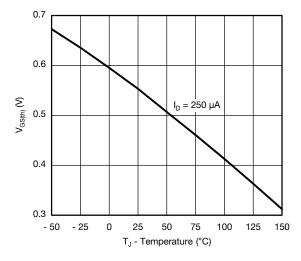
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



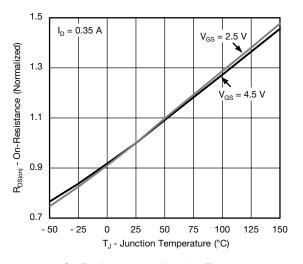
Gate Charge



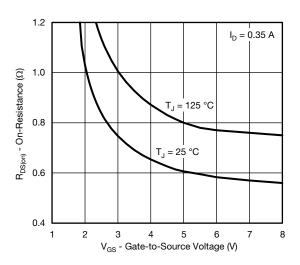
Source-Drain Diode Forward Voltage



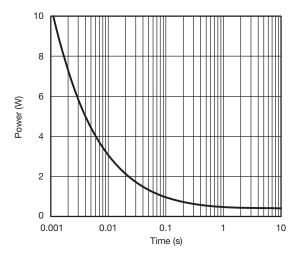
Threshold Voltage



On-Resistance vs. Junction Temperature



On-Resistance vs. Gate-to-Source Voltage

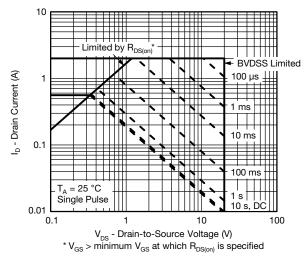


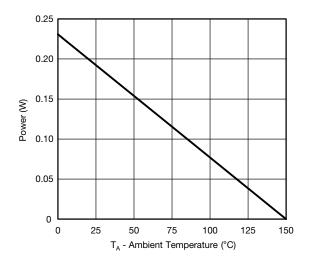
Single Pulse Power, Junction-to-Ambient

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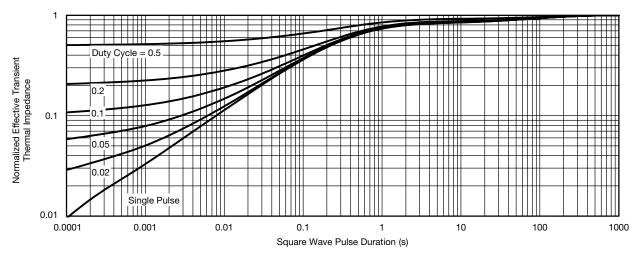
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





Safe Operating Area, Junction-to-Ambient

Power Derating, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient

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