

## AO5803E-VB Datasheet Dual P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)	Q <sub>g</sub> (TYP.) (nC)	
	0.450 at V <sub>GS</sub> = -4.5 V	-0.55		
-20	0.500 at V <sub>GS</sub> = -2.5 V	-0.50	1	
	0.600 at V <sub>GS</sub> = -1.8 V	-0.38		

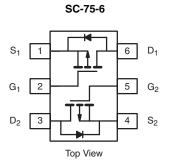
## FEATURES

- Trench power MOSFET
- 100 % R tested
- Fast switching speed



#### **APPLICATIONS**

- Load / power switch for portable devices
- Drivers: relays, solenoids, displays
- Battery operated systems



ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V <sub>DS</sub>	-20	v	
Gate-Source Voltage		V <sub>GS</sub>	± 8	v	
Continuous Durain Current (T. 150 °C)	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	-0.55 <sup>b, c</sup>		
Continuous Drain Current ( $T_J = 150 \ ^{\circ}C$ )	T <sub>A</sub> = 70 °C		-0.45 <sup>b, c</sup>	A	
Pulsed Drain Current (t = 300 µs)		I <sub>DM</sub>	-1.8		
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	-0.16 <sup>b, c</sup>		
Maximum Dawar Dissinction	T <sub>A</sub> = 25 °C	Р	0.19 <sup>b, c</sup>	w	
Maximum Power Dissipation	T <sub>A</sub> = 70 °C	P <sub>D</sub>	0.12 <sup>b, c</sup>	vv	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C	

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum Junction-to-Ambient <sup>a, b</sup>	t≤5 s	Р	440	530	°C/W	
Waximum Sunction-to-Amblent 4, 5	Steady State	R <sub>thJA</sub>	540	650		

#### Notes

a. Maximum under steady state conditions is 650 °C/W.

b. Surface mounted on 1" x 1" FR4 board.

c. t = 5 s.

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT		
Static								
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0, I <sub>D</sub> = -250 μA	-20	-	-	V		
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_J$	L 050 A	-	-12	-	mV/°C		
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = -250 μA	-	1.8	-			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = -250 \ \mu A$	-0.4	-	-1	V		
Gate-Source Leakage	l <sub>GSS</sub> -	$V_{DS} = 0 V$ , $V_{GS} = \pm 8 V$	-	-	± 30			
		$V_{DS} = 0 V, V_{GS} = \pm 4.5 V$ -			± 1	1.		
Zero Gate Voltage Drain Current		$V_{DS} = -20 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	-	-1	μA 1		
	IDSS	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$	-	-	-10	1		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = \ge 5 \text{ V}, V_{GS} = -4.5 \text{ V}$	-1.5	-	-	А		
		$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -0.4 \text{ A}$	-	0.450	-	1		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS}$ = -2.5 V, $I_D$ = -0.2 A	-	0.500	-	Ω		
		V <sub>GS</sub> = -1.8 V, I <sub>D</sub> = -0.1 A	-	0.600	-	1		
Forward Transconductance	<b>g</b> fs	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = 0.4 \text{ A}$	-	1	-	S		
Dynamic <sup>b</sup>								
Input Capacitance	C <sub>iss</sub>		-	45	-	pF		
Output Capacitance	Coss	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	15	-			
Reverse Transfer Capacitance	C <sub>rss</sub>		-	10	-			
	Qg -	$V_{DS}$ = -10 V, $V_{GS}$ = -4.5 V, $I_D$ = -0.4 A	-	1.65	2.50	)		
Total Gate Charge			-	1	2	nC		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = -0 V, $V_{GS}$ = -2.5 V, $I_{D}$ = -0.4	-	0.2	-			
Gate-Drain Charge	Q <sub>gd</sub>		-	0.26	-			
Gate Resistance	Rg	f = 1 MHz	2.4	12	24	Ω		
Turn-On Delay Time	t <sub>d(on)</sub>		-	9	18			
Rise Time	t <sub>r</sub>	$V_{DD} = -10 \text{ V}, \text{ R}_{\text{L}} = 33.3 \Omega$	-	10	20			
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D\cong$ -0.3 A, $V_{GEN}$ = -4.5 V, $R_g$ = 1 $\Omega$	-	10	20			
Fall Time	t <sub>f</sub>		-	8	16			
Turn-On Delay Time	t <sub>d(on)</sub>		-	1	2	ns		
Rise Time	t <sub>r</sub>	$V_{DD} = -10 \text{ V}, \text{ R}_{\text{L}} = 33.3 \Omega$	-	8	16			
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D\cong$ -0.3 A, $V_{GEN}$ = -8 V, $R_g$ = 1 $\Omega$	-	9	18			
Fall Time	t <sub>f</sub>		-	5	10	]		
Drain-Source Body Diode Characteris	tics							
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>		-	-	-1.5	A		
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = -0.3 A	-	-0.8	-1.2	V		
Body Diode Reverse Recovery Time	t <sub>rr</sub>		-	16	24	ns		
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	8	16	nC		
Reverse Recovery Fall Time	ta	I <sub>F</sub> = -0.3 A, dl/dt = 100 A/µs	-	11	-			
Reverse Recovery Rise Time	t <sub>b</sub>	 D		5	-	ns		

#### Notes

a. Pulse test; pulse width  $\leq 300~\mu s,~duty~cycle \leq 2~\%.$ 

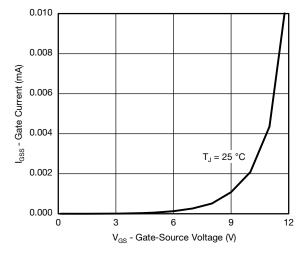
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

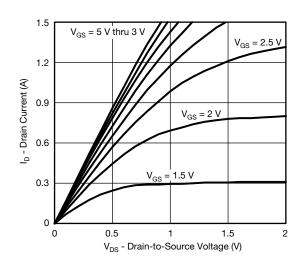
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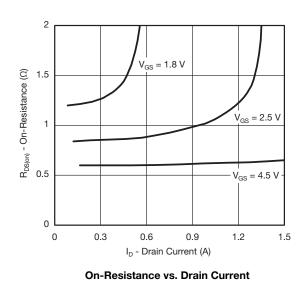
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

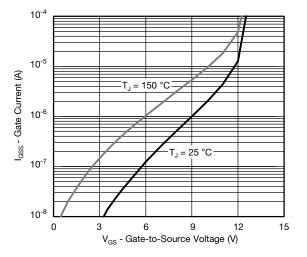


Gate Current vs. Gate-Source Voltage

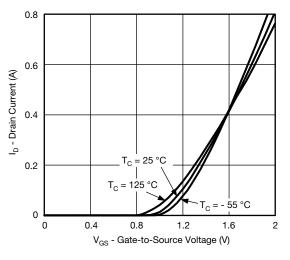


**Output Characteristics** 

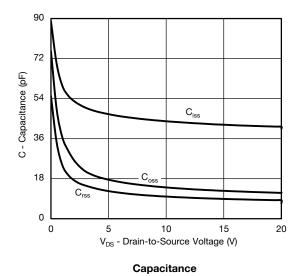




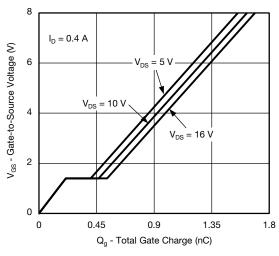
Gate Current vs. Gate-Source Voltage



Transfer Characteristics

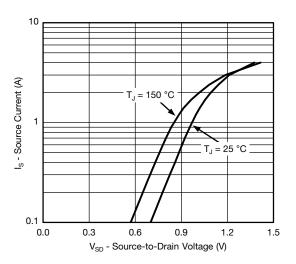




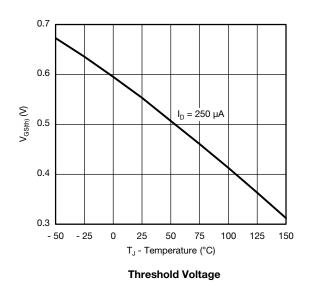


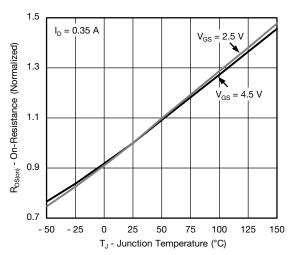
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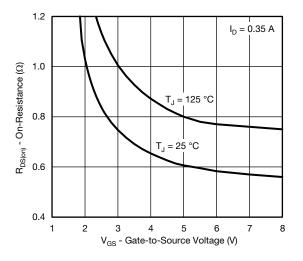


Source-Drain Diode Forward Voltage

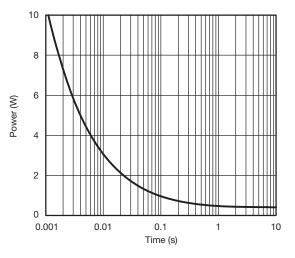




**On-Resistance vs. Junction Temperature** 

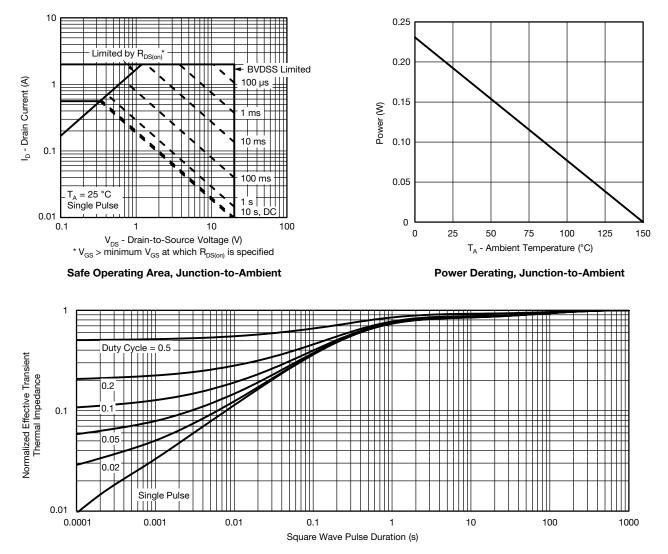


On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient





### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

Normalized Thermal Transient Impedance, Junction-to-Ambient



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