

RoHS

COMPLIANT HALOGEN

FREE

RYE002N05-VB Datasheet N-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS(min.)} (V)	R_{DS(on)} (Ω)	V _{GS(th)} (V)	I _D (mA)		
60	1.25 at V _{GS} = 10 V	1 to 2.5	330		

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FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- Trench Power MOSFETs
- Low On-Resistance: 1.25 Ω
- Low Threshold: 2.5 V
- Low Input Capacitance: 30 pF
- Fast Switching Speed: 25 ns
- Low Input and Output Leakage
- Miniature Package
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.
- Battery Operated Systems
- Solid State Relays

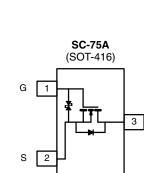
BENEFITS

- Low Offset Voltage
- Low-Voltage Operation
- High-Speed Circuits
- Low Error Voltage
- Small Board Area

ABSOLUTE MAXIMUM RATIN	GS (T _A = 25 °C, un	less otherwise n	oted)		
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	60	v	
Gate-Source Voltage		V _{GS}	± 20	V	
	T _A = 25 °C	1-	330		
Continuous Drain Current ^a	T _A = 85 °C	I _D	240	mA	
Pulsed Drain Current ^a		I _{DM}	650		
	T _A = 25 °C	P _D	250	mW	
Power Dissipation ^a	T _A = 85 °C	ГD	130	rnvv	
Thermal Resistance, Maximum Junction-to-Ambienta		R _{thJA}	500	°C/W	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	

Notes:

a. Surface mounted on FR4 board, power applied for t \leq 10 s.



SPECIFICATIONS ($T_J = 25 \ ^{\circ}C$, unless othe	rwise noted)					
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	-						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = 10 \mu A$	60			v	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 0.25 \text{ mA}$	1		2.5	v	
		$V_{DS} = 0 V, V_{GS} = \pm 10 V$			± 150		
Gate-Body Leakage	I _{GSS}	T _J = 85 °C			± 500		
		$V_{DS} = 0 V, V_{GS} = \pm 5 V$			± 20	nA	
		$V_{DS} = 50 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			10		
Zero Gate Voltage Drain Current	I _{DSS}	T _J = 85 °C			100		
		V _{DS} = 60 V, V _{GS} = 0 V			1	μA	
0	I _{D(on)}	V _{DS} = 10 V, V _{GS} = 4.5 V	500				
On-State Drain Current ^a		V _{DS} = 7.5 V, V _{GS} = 10 V	800			mA	
		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 200 \text{ mA}$		3.0			
		T _J = 125 °C		5.0			
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 500 mA		1.25		Ω	
		T _J = 125 °C		2.25			
Forward Transconductance ^a	9 _{fs}	V _{DS} = 10 V, I _D = 200 mA	100			mS	
Diode Forward Voltage ^a	V _{SD}	V _{GS} = 0 V, I _S = 200 mA			1.3	V	
Dynamic ^b							
Input Capacitance	C _{iss}			30			
Output Capacitance	C _{oss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz		6		pF	
Reverse Transfer Capacitance	C _{rss}			2.5			
Gate Charge	Qg	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 250 \text{ mA}, \text{ V}_{GS} = 4.5 \text{ V}$			0.6	nC	
Switching ^{b, c}		1	L				

Notes:

Turn-On Time

Turn-Off Time

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. For DESIGN AID ONLY, not subject to production testing.

c. Switching time is essentially independent of operating temperature.

t_(on)

t_(off)

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

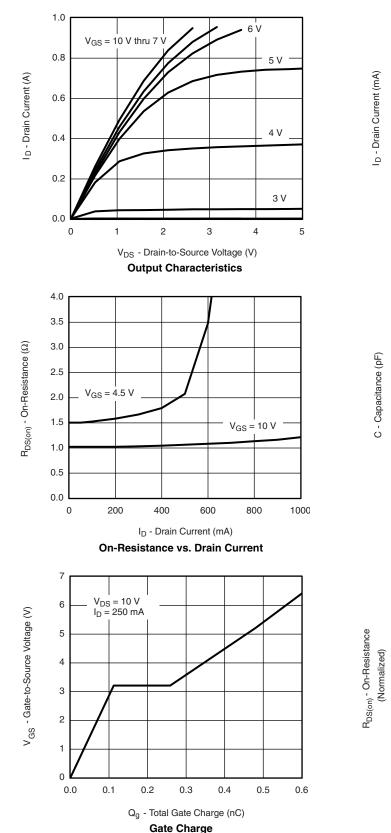
 V_{DD} = 30 V, R_L = 150 Ω, I_D = 200 mA, V_{GEN} = 10 V, R_g = 10 Ω

25

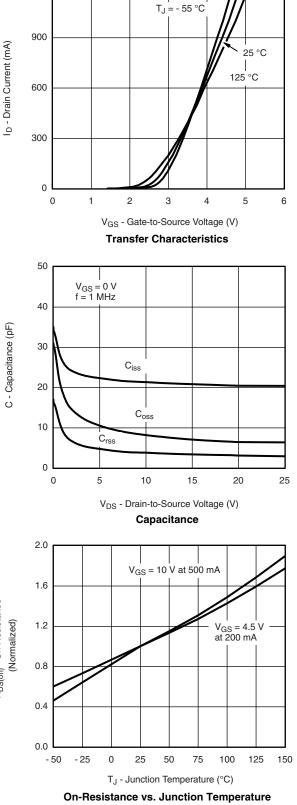
35

ns



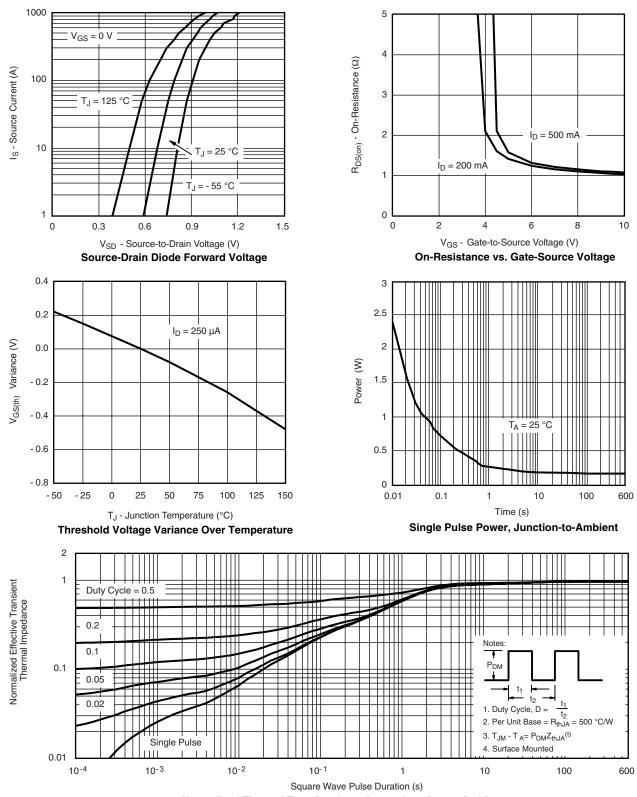


TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)

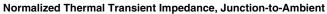


1200



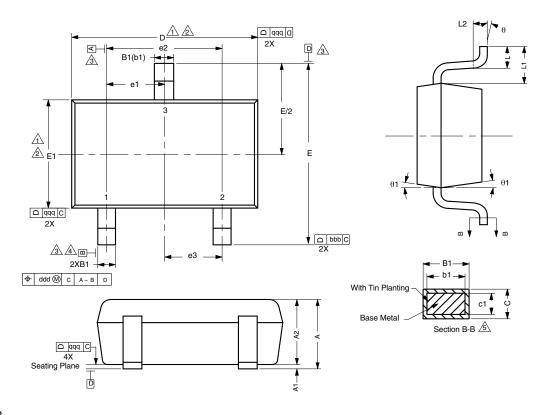


TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)





SC-75A: 3 Leads



DWG: 5868

Notes

Dimensions in millimeters will govern.

- Dimension D does not include mold flash, protrusions or gate burrs. Mold flash protrusions or gate burrs shall not exceed 0.10 mm per end. Dimension E1 does not include Interlead flash or protrusion. Interlead flash or protrusion shall not exceed 0.10 mm per side.
- Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, tie bar burrs, gate burrs and interlead flash, but including any mismatch between the top and bottom of the plastic body.
- A Datums A, B and D to be determined 0.10 mm from the lead tip.

A Terminal positions are shown for reference only.

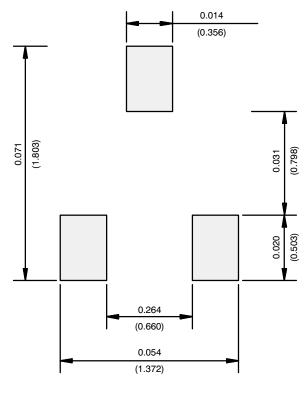
These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.

DIMENSIONS	TOLERANCES
aaa	0.10
bbb	0.10
ccc	0.10
ddd	0.10

DIM.	ľ	NOTE		
Divi.	MIN.	NOM.	MAX.	NOTE
А	-	-	0.80	
A1	0.00	-	0.10	
A2	0.65	0.70	0.80	
B1	0.19	-	0.24	5
b1	0.17	-	0.21	
с	0.13	-	0.15	5
c1	0.10	-	0.12	5
D	1.48	1.575	1.68	1, 2
E	1.50	1.60	1.70	
E1	0.66	0.76	0.86	1, 2
e1	0.50 BSC			
e2	1.00 BSC			
e3	0.50 BSC			
L	0.15	0.205	0.30	
L1	0.40 ref.			
L2	0.15 BSC			
q	0°	-	8°	
q1	4°	-	10°	



RECOMMENDED MINIMUM PADS FOR SC-75A: 3-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)



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