

NTE4151PT1G-VB Datasheet P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)	Q _g (TYP.) (nC)		
	0.450 at V _{GS} = -4.5 V	-0.55			
-20	0.500 at V _{GS} = -2.5 V	-0.50	1		
	0.600 at V _{GS} = -1.8 V	-0.38			

FEATURES

- Trench power MOSFET
- 100 % R tested
- Fast switching speed



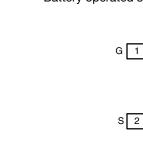
APPLICATIONS

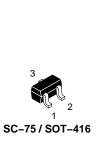
- Load / power switch for portable devices
- Drivers: relays, solenoids, displays

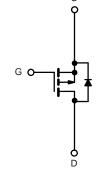
3 D

Top View

• Battery operated systems







P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V _{DS}	-20	V
Gate-Source Voltage		V _{GS}	± 12	V
0 11	T _A = 25 °C		-0.55 ^{b, c}	
Continuous Drain Current (T _J = 150 °C)	T _A = 70 °C	I _D	-0.45 ^{b, c}	
Pulsed Drain Current (t = 300 μs)		I _{DM}	-1.8	A
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	-0.16 ^{b, c}	
Maximum Davier Dissination	T _A = 25 °C	В	0.19 ^{b, c}	10/
Maximum Power Dissipation	T _A = 70 °C	P _D	0.12 ^{b, c}	W
Operating Junction and Storage Temperature Range		T _J , T _{stq}	-55 to +150	°C

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum Junction-to-Ambient ^{a, b}	t ≤ 5 s	R_{thJA}	440	530	°C/W
Maximum Junction-to-Ambient 3, 2	Steady State		540	650	

Notes

- a. Maximum under steady state conditions is 650 $^{\circ}\text{C/W}.$
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s.

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static				•			
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0$, $I_D = -250 \mu A$	-20	_	-	V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	ΔV _{DS} /T _J		-12	-	m\//°C	
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J	I _D = -250 μA	-	1.8	-	mV/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-0.4	-	-1	V	
Cata Sauraa Laakaga	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$	-	-	± 30		
Gate-Source Leakage		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$	-	-	± 1		
Zoro Goto Voltago Drain Current	l	V _{DS} = -20 V, V _{GS} = 0 V	-	-	-1	— μA —	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -20 V, V _{GS} = 0 V, T _J = 85 °C	-	-	-10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = \ge 5 \text{ V}, V_{GS} = -4.5 \text{ V}$	-1.5	-	-	Α	
		V _{GS} = -4.5 V, I _D = -0.4 A	-	0.450	-		
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -2.5 \text{ V}, I_D = -0.2 \text{ A}$	-	0.500	-	Ω	
		$V_{GS} = -1.8 \text{ V}, I_D = -0.1 \text{ A}$	-	0.600	-		
Forward Transconductance	9fs	$V_{DS} = -10 \text{ V}, I_D = 0.4 \text{ A}$	-	1	-	S	
Dynamic ^b							
Input Capacitance	C _{iss}		-	45	-		
Output Capacitance	C _{oss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	15	-	pF	
Reverse Transfer Capacitance	C _{rss}		-	10	ı		
Total Cata Chausa	Q _g	$V_{DS} =$ -10 V, $V_{GS} =$ -4.5 V, $I_D =$ -0.4 A	-	1.65	2.50	nC	
Total Gate Charge			-	1	2		
Gate-Source Charge	Q_{gs}	V_{DS} = -0 V, V_{GS} = -2.5 V, I_D = -0.4	-	0.2	ı	110	
Gate-Drain Charge	Q_{gd}		-	0.26	ı		
Gate Resistance	R_g	f = 1 MHz	2.4	12	24	Ω	
Turn-On Delay Time	t _{d(on)}		-	9	18		
Rise Time	t _r	V_{DD} = -10 V, R_L = 33.3 Ω	-	10	20		
Turn-Off DelayTime	t _{d(off)}	$I_D\cong$ -0.3 A, $V_{GEN}=$ -4.5 V, $R_g=$ 1 Ω	-	10	20		
Fall Time	t _f		-	8	16	ns	
Turn-On Delay Time	t _{d(on)}		-	1	2	113	
Rise Time	t _r	V_{DD} = -10 V, R_L = 33.3 Ω	-	8	16		
Turn-Off DelayTime	t _{d(off)}	$I_D\cong$ -0.3 A, $V_{GEN}=$ -8 V, $R_g=$ 1 Ω	-	9	18		
Fall Time	t _f		-	5	10		
Drain-Source Body Diode Characteris	tics						
Pulse Diode Forward Current ^a	I _{SM}		-	_	-1.5	Α	
Body Diode Voltage	V_{SD}	I _S = -0.3 A	-	-0.8	-1.2	V	
Body Diode Reverse Recovery Time	t _{rr}		-	16	24	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	L = 0.3 A dl/d+ = 100 A/vo	-	8	16	nC	
Reverse Recovery Fall Time	ta	I _F = -0.3 A, dl/dt = 100 A/μs	-	11	-		
Reverse Recovery Rise Time			_	5	-	ns	

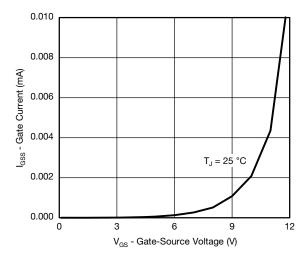
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

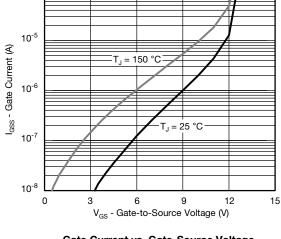
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

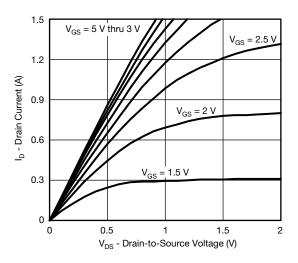


Gate Current vs. Gate-Source Voltage

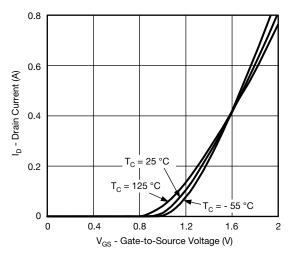


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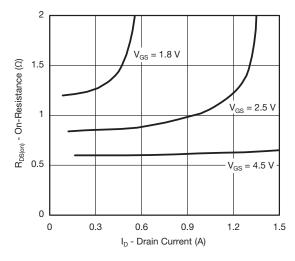
Gate Current vs. Gate-Source Voltage



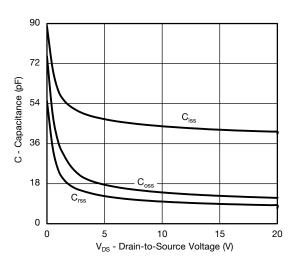
Output Characteristics



Transfer Characteristics



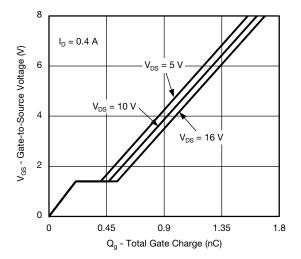
On-Resistance vs. Drain Current



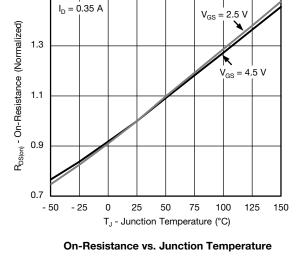
Capacitance

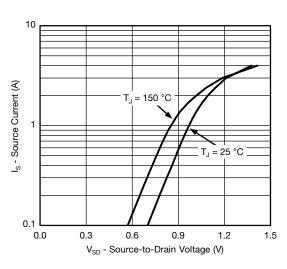


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

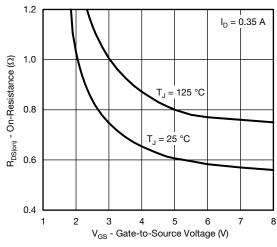


Gate Charge

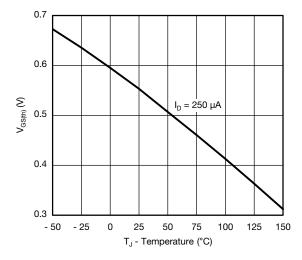




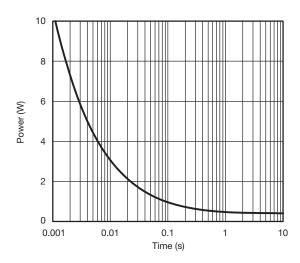
Source-Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



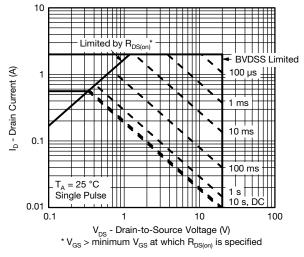
Threshold Voltage

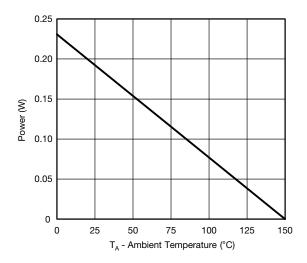


Single Pulse Power, Junction-to-Ambient



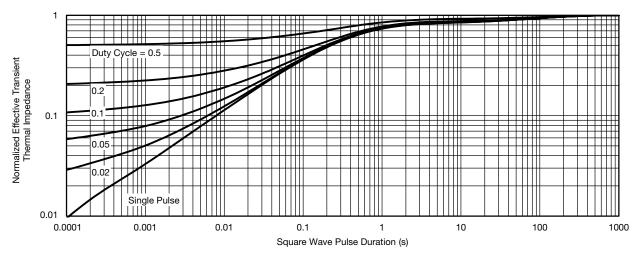
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





Safe Operating Area, Junction-to-Ambient

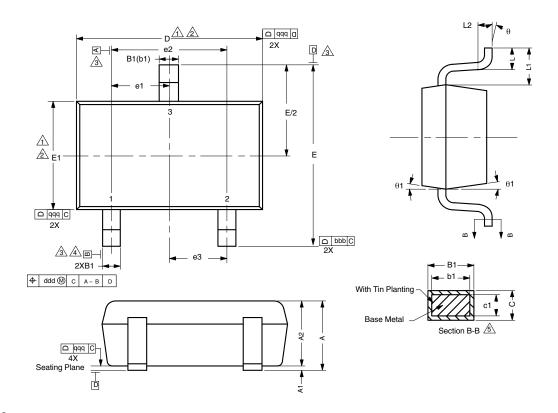
Power Derating, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient



SC-75: 3 Leads



DWG: 5868

Notes

Dimensions in millimeters will govern.

⚠Dimension D does not include mold flash, protrusions or gate burrs. Mold flash protrusions or gate burrs shall not exceed 0.10 mm per end. Dimension E1 does not include Interlead flash or protrusion. Interlead flash or protrusion shall not exceed 0.10 mm per side.

Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, tie bar burrs, gate burrs and interlead flash, but including any mismatch between the top and bottom of the plastic body.

Datums A, B and D to be determined 0.10 mm from the lead tip.

Terminal positions are shown for reference only.

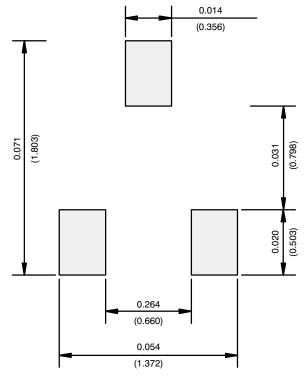
These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.

DIMENSIONS	TOLERANCES		
aaa	0.10		
bbb	0.10		
ccc	0.10		
ddd	0.10		

DIM.		NOTE		
	MIN.	NOM.	MAX.	NOTE
Α	-	-	0.80	
A1	0.00	-	0.10	
A2	0.65	0.70	0.80	
B1	0.19	-	0.24	5
b1	0.17	-	0.21	
С	0.13	-	0.15	5
c1	0.10	-	0.12	5
D	1.48	1.575	1.68	1, 2
E	1.50	1.60	1.70	
E1	0.66	0.76	0.86	1, 2
e1	0.50 BSC			
e2	1.00 BSC			
e3	0.50 BSC			
L	0.15	0.205	0.30	
L1	0.40 ref.			
L2	0.15 BSC			
q	0°	-	8°	
q1	4°	-	10°	



RECOMMENDED MINIMUM PADS FOR SC-75: 3-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

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