

APM2101SGC-TRL-VB Datasheet

P-Channel 20-V (D-S) MOSFET

Features

- ON-resistance $R_{DS(on)}$ 1 : 20m Ω (typ.)
- 1.8V drive
- Protection diode in
- Input Capacitance C_{iss} =1100pF(typ.)
- Halogen free compliance

Specifications

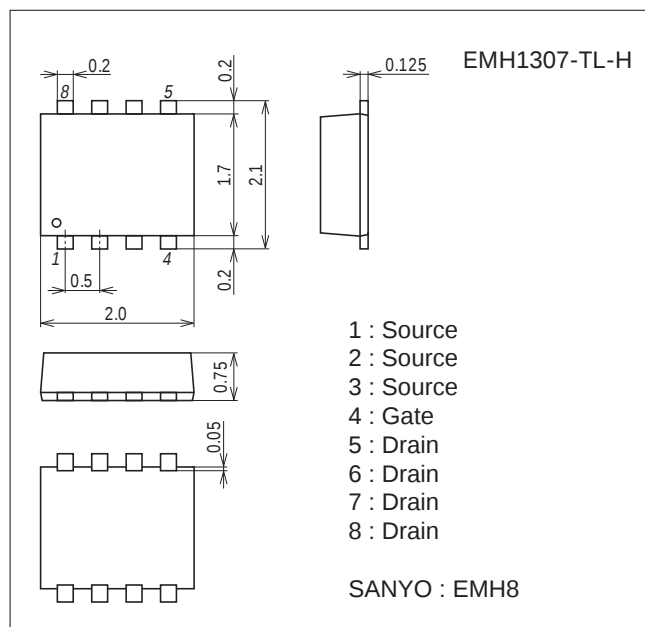
Absolute Maximum Ratings at $T_a=25^{\circ}\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DS}		-20	V
Gate-to-Source Voltage	V_{GS}		± 10	V
Drain Current (DC)	I_D		-6.5	A
Drain Current (Pulse)	I_{DP}	$PW \leq 10\mu\text{s}$, duty cycle $\leq 1\%$	-26	A
Allowable Power Dissipation	P_D	When mounted on ceramic substrate (1200mm ² ×0.8mm)	1.5	W
Channel Temperature	T_{ch}		150	$^{\circ}\text{C}$
Storage Temperature	T_{stg}		-55 to +150	$^{\circ}\text{C}$

Package Dimensions

unit : mm (typ)

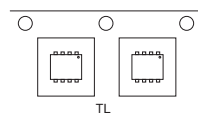
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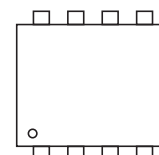
Product & Package Information

- Package : EMH8
- JEITA, JEDEC : -
- Minimum Packing Quantity : 3,000 pcs./reel

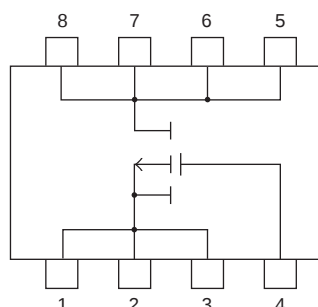
Taping Type : TL



Marking



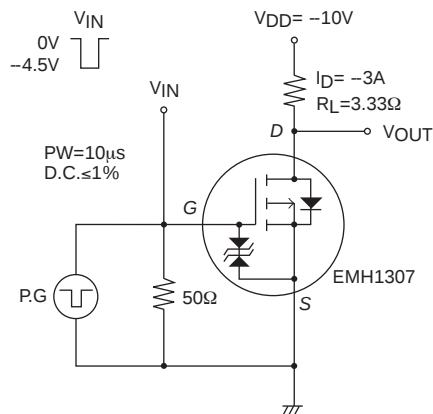
Electrical Connection

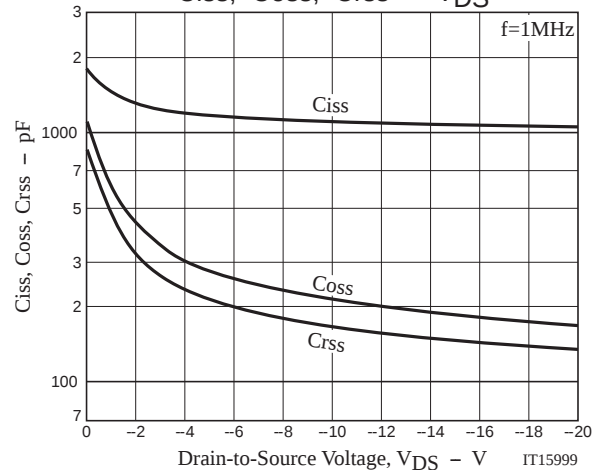
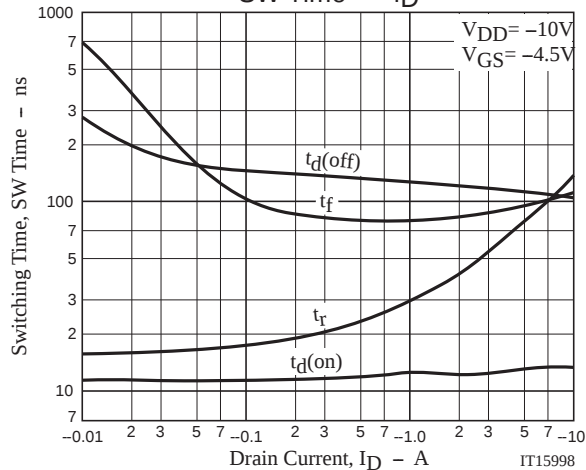
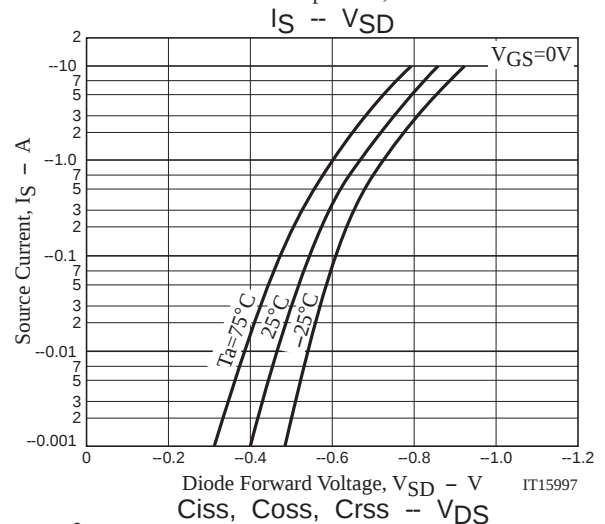
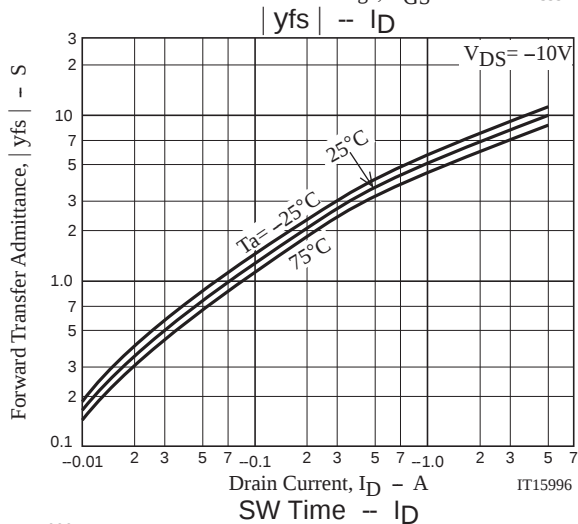
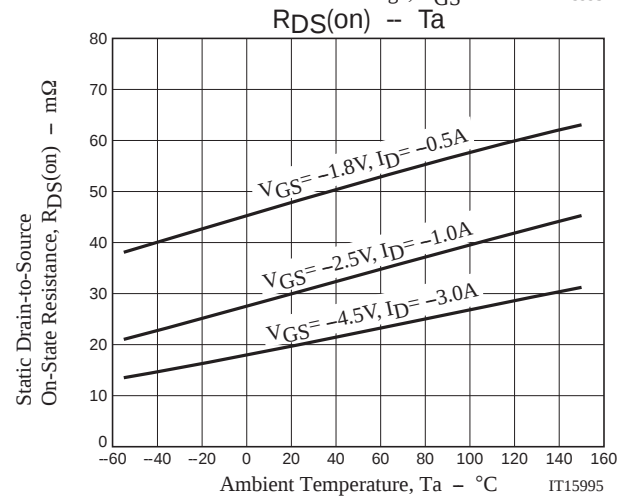
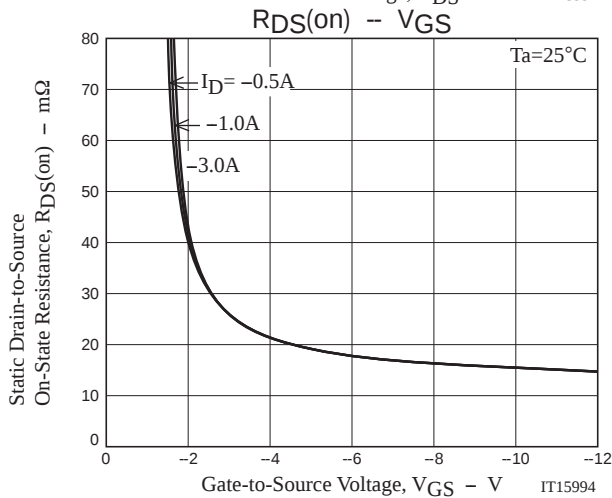
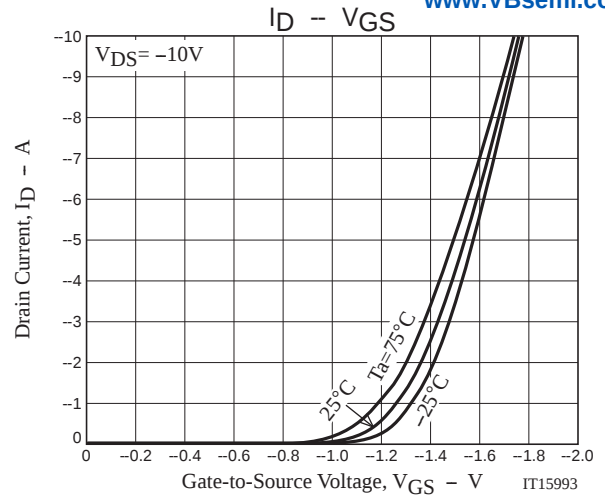
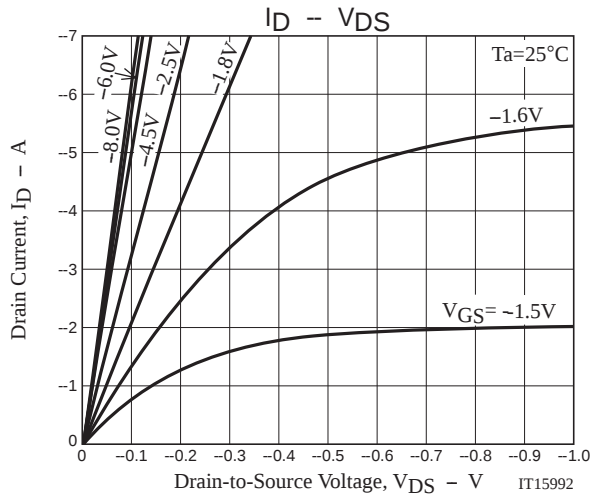


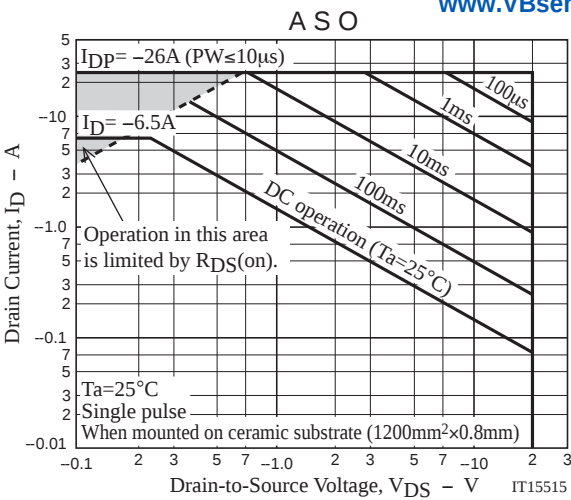
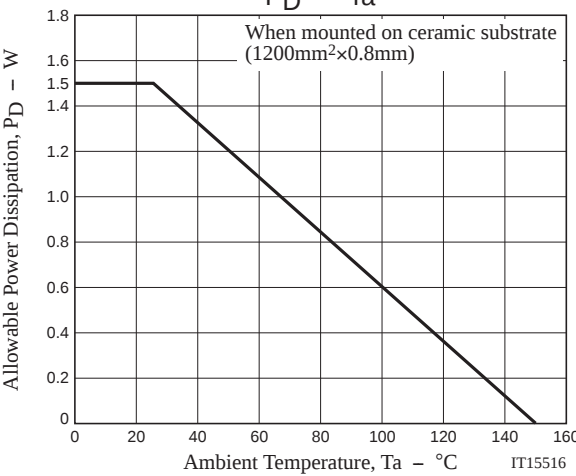
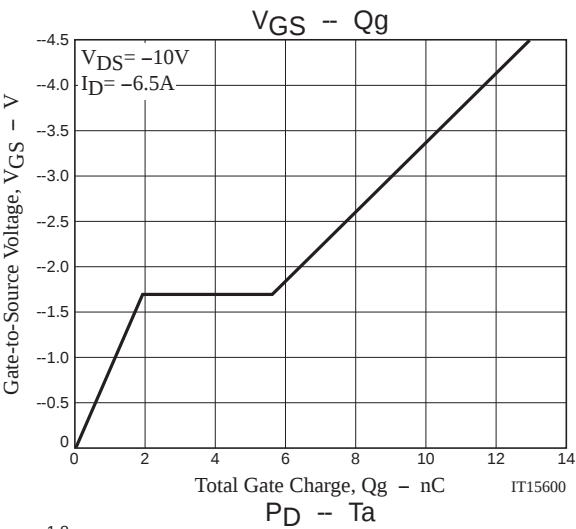
Electrical Characteristics at $T_a=25^{\circ}\text{C}$

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=-1\text{mA}$, $V_{GS}=0\text{V}$	-20			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-20\text{V}$, $V_{GS}=0\text{V}$			-1	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 8\text{V}$, $V_{DS}=0\text{V}$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS}=-10\text{V}$, $I_D=-1\text{mA}$	-0.4		-1.3	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=-10\text{V}$, $I_D=-3\text{A}$		8.2		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D=-3\text{A}$, $V_{GS}=-4.5\text{V}$		20	26	$\text{m}\Omega$
	$R_{DS(on)2}$	$I_D=-1.5\text{A}$, $V_{GS}=-2.5\text{V}$		31	44	$\text{m}\Omega$
	$R_{DS(on)3}$	$I_D=-0.5\text{A}$, $V_{GS}=-1.8\text{V}$		49	78	$\text{m}\Omega$
Input Capacitance	C_{iss}	$V_{DS}=-10\text{V}$, $f=1\text{MHz}$		1100		pF
Output Capacitance	C_{oss}			210		pF
Reverse Transfer Capacitance	C_{rss}			160		pF
Turn-ON Delay Time	$t_{d(on)}$			12.8		ns
Rise Time	t_r	See specified Test Circuit.		55		ns
Turn-OFF Delay Time	$t_{d(off)}$			120		ns
Fall Time	t_f			88		ns
Total Gate Charge	Q_g	$V_{DS}=-10\text{V}$, $V_{GS}=-4.5\text{V}$, $I_D=-6.5\text{A}$		13		nC
Gate-to-Source Charge	Q_{gs}			1.9		nC
Gate-to-Drain "Miller" Charge	Q_{gd}			3.7		nC
Diode Forward Voltage	V_{SD}	$I_S=-6.5\text{A}$, $V_{GS}=0\text{V}$		-0.8	-1.2	V

Switching Time Test Circuit

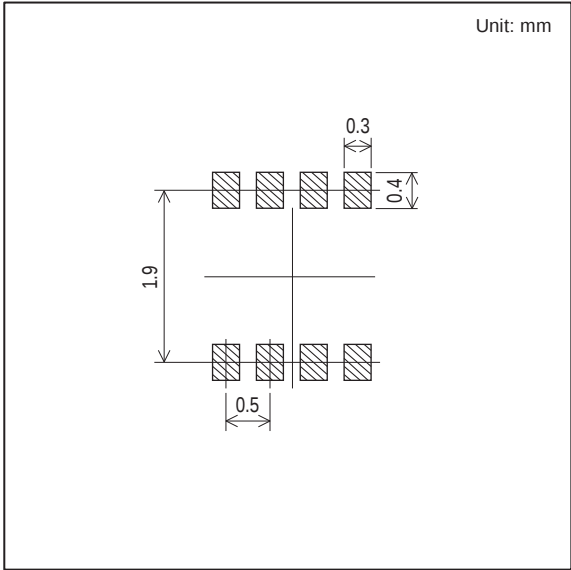
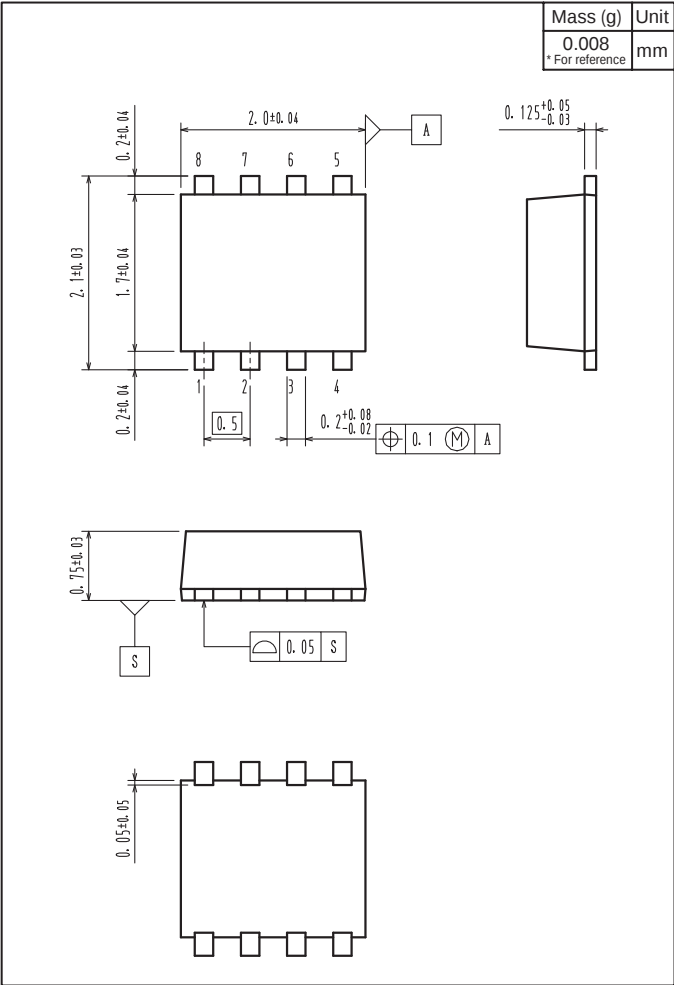






Outline Drawing
EMH1307-TL-H

Land Pattern Example



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