

## Si1427EDH-T1-GE3-VB Datasheet P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)			
	0.034 at V <sub>GS</sub> = - 4.5 V	- 4				
- 20	0.045 at V <sub>GS</sub> = - 2.5 V	- 4	12.5 nC			
	0.067 at V <sub>GS</sub> = - 1.8 V	- 4				

## **SOT-363** SC-70 (6-LEADS) D 1 6 D D 2 5 D G 3 4 S

#### FEATURES

- Halogen-free According to IEC 61249-2-21
  Definition
- Trench Power MOSFET
- 100 % Rg Tested
- Compliant to RoHS Directive 2002/95/EC

#### **APPLICATIONS**

- Load Switch for Portable Devices
  - Cellular Phone
  - DSC
  - Portable Game Console
  - MP3 - GPS
- G 3 4 S

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V <sub>DS</sub>	- 20	V	
Gate-Source Voltage		V <sub>GS</sub>	± 12	v	
	T <sub>C</sub> = 25 °C		- 4 <sup>a</sup>		
Continuous Drain Querent (T. 150 °C)	T <sub>C</sub> = 70 °C		- 4		
Continuous Drain Current ( $T_J = 150 \ ^{\circ}C$ )	T <sub>A</sub> = 25 °C	I <sub>D</sub>	- 4 <sup>a, b, c</sup>		
	T <sub>A</sub> = 70 °C		- 4 <sup>a, b, c</sup>	А	
Pulsed Drain Current (t = 300 µs)		I <sub>DM</sub>	- 25		
Quality of the During Divide Quarter	T <sub>C</sub> = 25 °C		- 2.3		
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	- 1.3 <sup>b, c</sup>		
	T <sub>C</sub> = 25 °C		2.8		
Maximum Power Dissinction	T <sub>C</sub> = 70 °C	В	1.8	w	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	1.6 <sup>b, c</sup>	vv	
	T <sub>A</sub> = 70 °C		1.0 <sup>b, c</sup>		
Operating Junction and Storage Temperature R	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C		
Soldering Recommendations (Peak Temperature)			260		

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient <sup>b, d</sup>	$t \le 5 s$	R <sub>thJA</sub>	60	80	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	34	45			

Notes: a. Package limited.

b. Surface mounted on 1" x 1" FR4 board.

c. t = 5 s.

d. Maximum under steady state conditions is 125 °C/W.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = -250 \mu A$	- 20			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	L 050		- 11		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = - 250 μA		2.6			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 0.4		- 1	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 10 V$			± 8	- μΑ	
		$V_{DS} = 0 V, V_{GS} = \pm 4.5 V$			± 1		
Zarra Casta Malta na Drain Ourreat		V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V			- 1		
Zero Gate Voltage Drain Current	IDSS	$V_{DS}$ = - 20 V, $V_{GS}$ = 0 V, $T_{J}$ = 55 °C			- 10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le$ - 5 V, $V_{GS}$ = - 10 V	- 15			Α	
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 5 A		0.034		Ω	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 4.4 A		0.045			
		V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 1 A		0.067			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 5 A		16		S	
Dynamic <sup>b</sup>							
Total Gate Charge	Qg	$V_{DS} = -10 \text{ V}, \text{ V}_{GS} = -8 \text{ V}, \text{ I}_{D} = -5 \text{ A}$		22	33	nC	
Cata Source Charge				12.5	19		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = - 10 V, $V_{GS}$ = - 4.5 V, $I_D$ = - 5 A		1.8			
Gate-Drain Charge	Q <sub>gd</sub>			3.3			
Gate Resistance	R <sub>g</sub>	f = 1 MHz	0.08	0.43	0.86	kΩ	
Turn-On Delay Time	t <sub>d(on)</sub>			150	225	- ns	
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 10 V, $R_L$ = 1.4 $\Omega$		300	450		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ - 4 Å, $V_{GEN}$ = - 4.5 V, $R_g$ = 1 $\Omega$		1620	2430		
Fall Time	t <sub>f</sub>			560	840		
Turn-On Delay Time	t <sub>d(on)</sub>			50	100		
Rise Time	t <sub>r</sub>	$V_{DD} = -10 \text{ V}, \text{ R}_{1} = 1.4 \Omega$		90	180		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong -4$ Å, $V_{GEN} = -10$ V, $R_g = 1 \Omega$		2500	3750		
Fall Time	t <sub>f</sub>			600	900		
Drain-Source Body Diode Characterist	cs						
Continuous Source-Drain Diode Current	۱ <sub>S</sub>	T <sub>C</sub> = 25 °C			- 2.3	A	
Pulse Diode Forward Current	I <sub>SM</sub>				- 25		
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 4 A, V <sub>GS</sub> = 0 V		- 0.85	- 1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			18	36	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			8	16	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = -4 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, \text{ T}_J = 25 ^\circ\text{C}$		18		- ns	
Reverse Recovery Rise Time	t <sub>b</sub>			10			

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

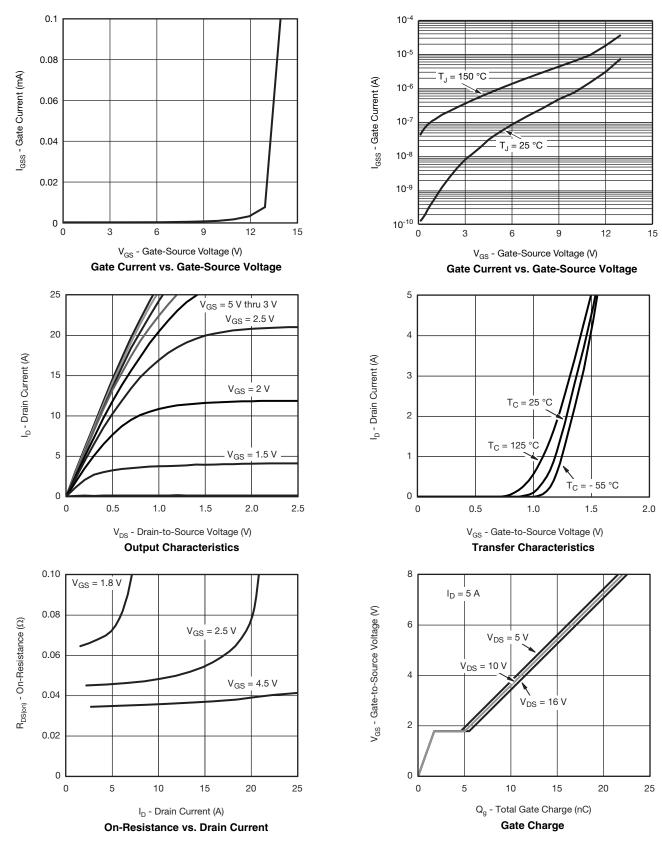
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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## Si1427EDH-T1-GE3-VB

#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

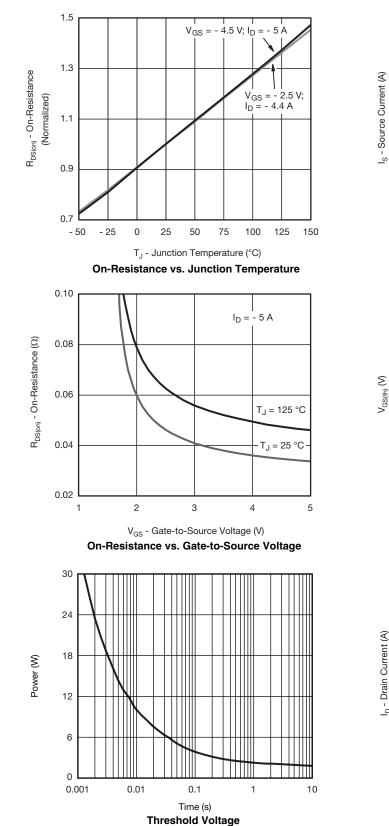


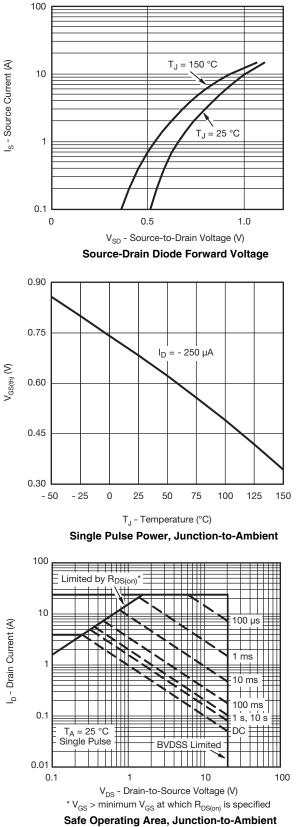
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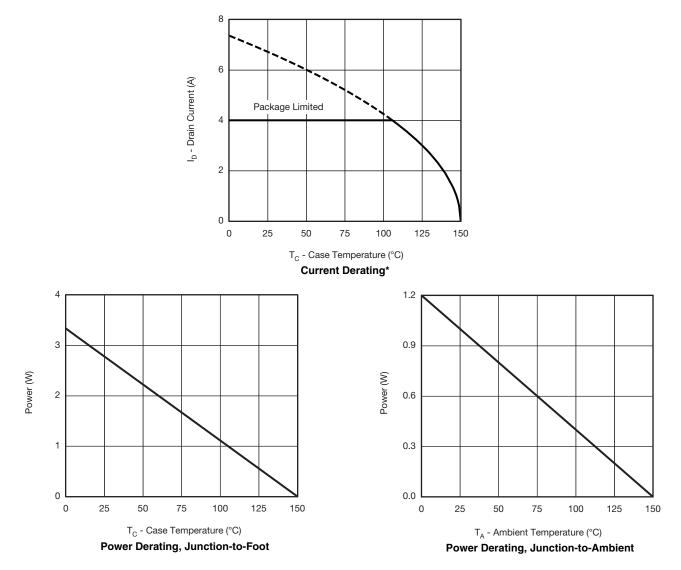
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\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

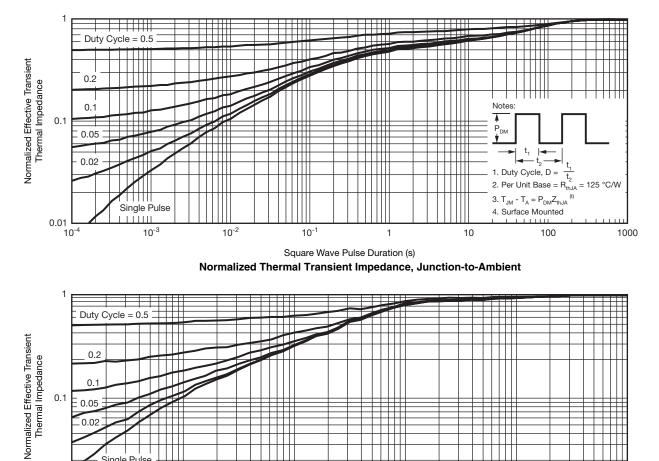
0.02

0.01 . 10<sup>-4</sup> Single Pulse

10<sup>-3</sup>



#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



10<sup>-2</sup>

Square Wave Pulse Duration (s) Normalized Thermal Transient Impedance, Junction-to-Foot

10<sup>-1</sup>

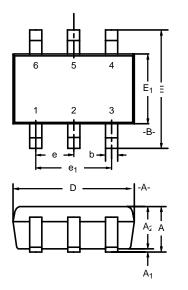
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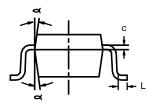
10

## Si1427EDH-T1-GE3-VB



#### SC-70: 6-LEADS



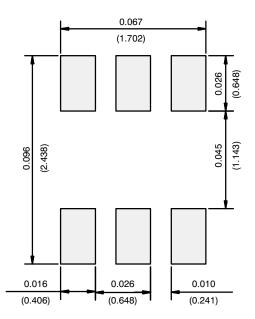


	MILLIMETERS			INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.90	-	1.10	0.035	-	0.043	
A <sub>1</sub>	-	-	0.10	-	-	0.004	
A <sub>2</sub>	0.80	-	1.00	0.031	-	0.039	
b	0.15	-	0.30	0.006	-	0.012	
С	0.10	-	0.25	0.004	-	0.010	
D	1.80	2.00	2.20	0.071	0.079	0.087	
Е	1.80	2.10	2.40	0.071	0.083	0.094	
E <sub>1</sub>	1.15	1.25	1.35	0.045	0.049	0.053	
е	0.65BSC			0.026BSC			
e <sub>1</sub>	1.20	1.30	1.40	0.047	0.051	0.055	
L	0.10	0.20	0.30	0.004	0.008	0.012	
٩	7°Nom			7°Nom			
ECN: S-03946—Rev. B, 09-Jul-01 DWG: 5550							

### Si1427EDH-T1-GE3-VB



#### **RECOMMENDED MINIMUM PADS FOR SC-70: 6-Lead**



Recommended Minimum Pads Dimensions in Inches/(mm)



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