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SI1499DH-T1-GE3-VB Datasheet P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ.)			
	0.034 at V _{GS} = - 4.5 V	- 4				
- 20	0.045 at V _{GS} = - 2.5 V	- 4	12.5 nC			
	0.067 at V _{GS} = - 1.8 V	- 4				

SOT-363 SC-70 (6-LEADS) D D 1 6 5 D D 2 G 3 4 S

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- Trench Power MOSFET
- 100 % Rg Tested
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- Load Switch for Portable Devices
 - Cellular Phone
 - DSC
 - Portable Game Console
 - MP3 - GPS

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V _{DS}	- 20	V		
Gate-Source Voltage		V _{GS}	± 12		
	T _C = 25 °C		- 4 ^a	A	
Continuous Drain Current (T 150 °C)	T _C = 70 °C		- 4		
Continuous Drain Current ($T_J = 150 \ ^{\circ}C$)	T _A = 25 °C	I _D	- 4 ^{a, b, c}		
	T _A = 70 °C		- 4 ^{a, b, c}		
Pulsed Drain Current (t = 300 µs)		I _{DM}	- 25		
Continuous Source-Drain Diode Current	T _C = 25 °C		- 2.3	_	
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	- 1.3 ^{b, c}		
	T _C = 25 °C		2.8		
Maximum Dawar Dissinction	T _C = 70 °C		1.8	w	
Maximum Power Dissipation	T _A = 25 °C	P _D	1.6 ^{b, c}		
	T _A = 70 °C		1.0 ^{b, c}		
Operating Junction and Storage Temperature R	T _J , T _{stg}	- 55 to 150	°C		
Soldering Recommendations (Peak Temperature		260			

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{b, d}	t ≤ 5 s	R _{thJA}	60	80	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	34	45	0/00		

Notes: a. Package limited.

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b. Surface mounted on 1" x 1" FR4 board.

c. t = 5 s.

d. Maximum under steady state conditions is 125 °C/W.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_{D} = -250 \mu A$	- 20			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	V _{DS} /T _J		- 11		m\//º	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = - 250 μΑ		2.6		mV/°	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 0.4		- 1	V	
	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 10 V$			± 8	- μΑ	
Gate-Source Leakage		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$			± 1		
Zero Gate Voltage Drain Current		$V_{DS} = -20 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1		
Zero Gale voltage Drain Current	IDSS	V_{DS} = - 20 V, V_{GS} = 0 V, T_{J} = 55 °C			- 10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS}{\leq}$ - 5 V, V_{GS} = - 10 V	- 15			Α	
		V_{GS} = - 4.5 V, I _D = - 5 A		0.034	0.034		
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 2.5 V, I _D = - 4.4 A		0.045		Ω	
		V _{GS} = - 1.8 V, I _D = - 1 A		0.067			
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 5 A		16		S	
Dynamic ^b							
Total Gate Charge	Q_{g} $V_{DS} = -10 V, V_{GS} = -8 V, I_{D} = -5 A$		22	33			
Cata Source Charge				12.5	19	nC	
Gate-Source Charge	Q _{gs}	V_{DS} = - 10 V, V_{GS} = - 4.5 V, I_D = - 5 A		1.8			
Gate-Drain Charge	Q _{gd}			3.3			
Gate Resistance	Rg	f = 1 MHz	0.08	0.43	0.86	kΩ	
Turn-On Delay Time	t _{d(on)}			150	225	- ns	
Rise Time	t _r	V_{DD} = - 10 V, R_L = 1.4 Ω		300	450		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ - 4 Å, V_{GEN} = - 4.5 V, R_g = 1 Ω		1620	2430		
Fall Time	t _f			560	840		
Turn-On Delay Time	t _{d(on)}			50	100		
Rise Time	t _r	V_{DD} = - 10 V, R_L = 1.4 Ω		90	180		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong -4$ Å, $V_{GEN} = -10$ V, $R_g = 1 \Omega$		2500	3750		
Fall Time	t _f			600	900		
Drain-Source Body Diode Characteristi	cs						
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			- 2.3	A	
Pulse Diode Forward Current	I _{SM}				- 25		
Body Diode Voltage	V _{SD}	$I_{S} = -4 \text{ A}, V_{GS} = 0 \text{ V}$		- 0.85	- 1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			18	36	ns	
Body Diode Reverse Recovery Charge	Q _{rr}				16	nC	
Reverse Recovery Fall Time	t _a	$I_F = -4 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, \text{ T}_J = 25 ^\circ\text{C}$		18		- ns	
Reverse Recovery Rise Time	t _b			10			

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

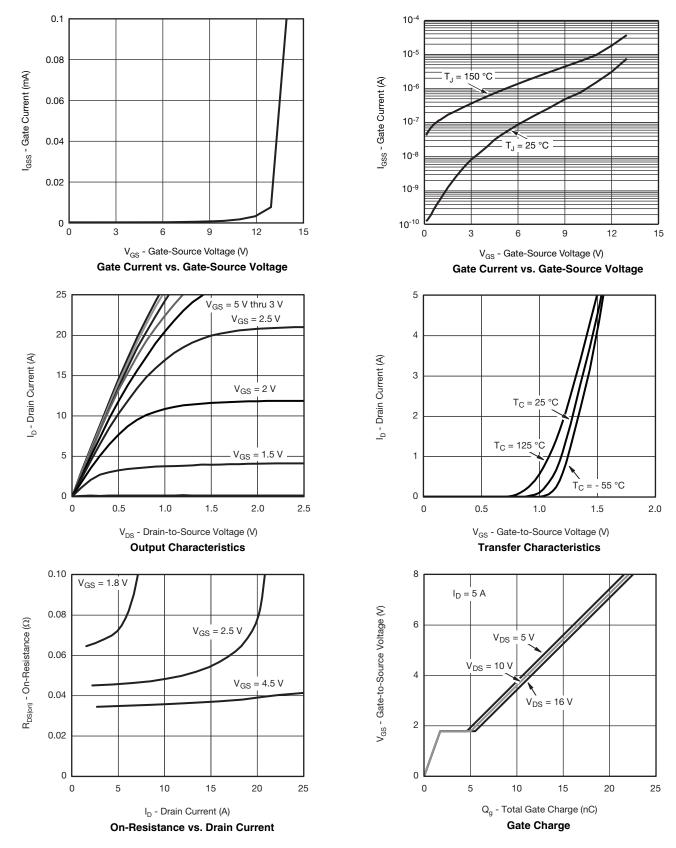
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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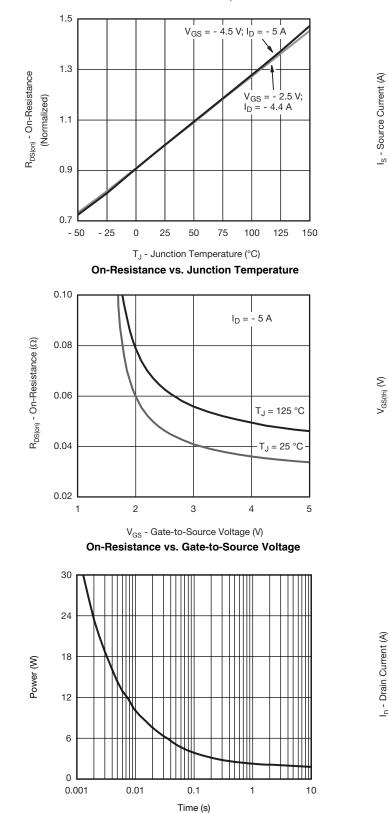


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

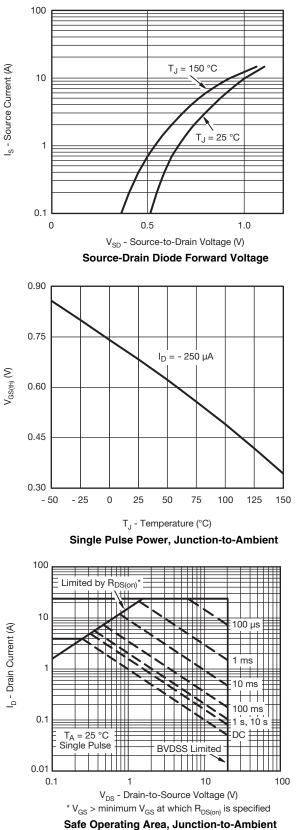




TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

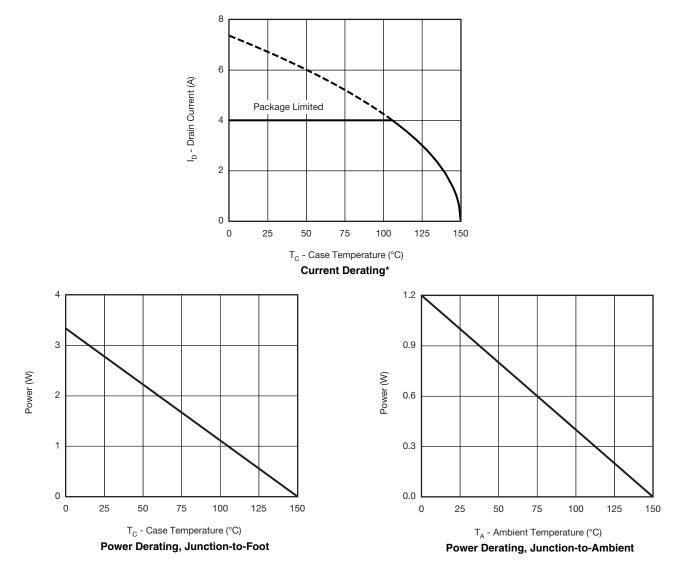


Threshold Voltage





TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

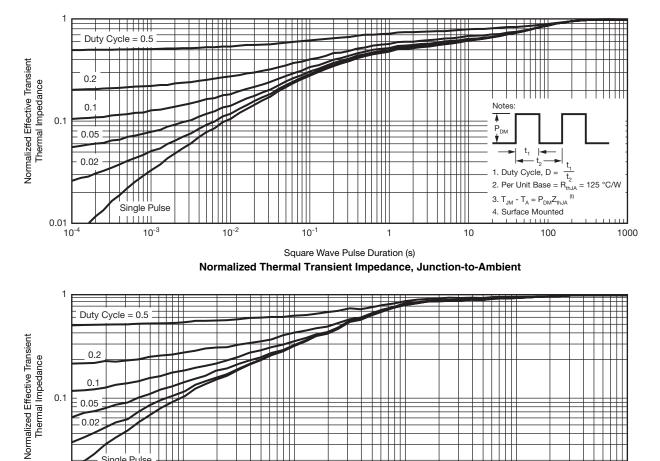
Single Pulse

10⁻³

0.01 . 10⁻⁴



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



10⁻²

Square Wave Pulse Duration (s) Normalized Thermal Transient Impedance, Junction-to-Foot

10⁻¹

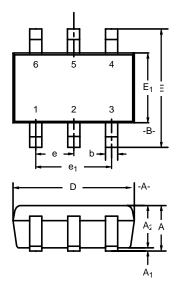
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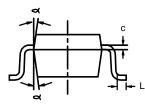
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SI1499DH-T1-GE3-VB



SC-70: 6-LEADS



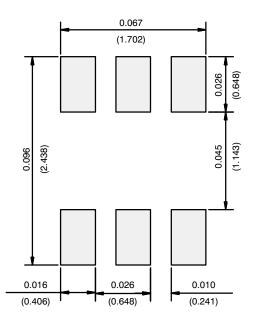


	MILLIMETERS			INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.90	-	1.10	0.035	-	0.043	
A ₁	-	-	0.10	-	-	0.004	
A ₂	0.80	-	1.00	0.031	-	0.039	
b	0.15	-	0.30	0.006	-	0.012	
С	0.10	-	0.25	0.004	-	0.010	
D	1.80	2.00	2.20	0.071	0.079	0.087	
Е	1.80	2.10	2.40	0.071	0.083	0.094	
E ₁	1.15	1.25	1.35	0.045	0.049	0.053	
е	0.65BSC			0.026BSC			
e ₁	1.20	1.30	1.40	0.047	0.051	0.055	
L	0.10	0.20	0.30	0.004	0.008	0.012	
م	7°Nom			7°Nom			
ECN: S-03946—Rev. B, 09-Jul-01 DWG: 5550							

SI1499DH-T1-GE3-VB



RECOMMENDED MINIMUM PADS FOR SC-70: 6-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)



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