

# SI1441EDH-T1-GE3-VB Datasheet P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	Q <sub>g</sub> (Typ.)			
	$0.034$ at $V_{GS} = -4.5 \text{ V}$	- 4			
- 20	0.045 at V <sub>GS</sub> = - 2.5 V	- 4	12.5 nC		
	0.067 at V <sub>GS</sub> = - 1.8 V	- 4			

# SOT-363 SC-70 (6-LEADS) D 1 5 D

#### **FEATURES**

 Halogen-free According to IEC 61249-2-21 Definition



Trench Power MOSFET

- 100 % R<sub>g</sub> Tested
- Compliant to RoHS Directive 2002/95/EC

#### RoHS COMPLIANT

## **APPLICATIONS**

- Load Switch for Portable Devices
  - Cellular Phone
  - DSC
  - Portable Game Console
  - MP3
  - GPS

ABSOLUTE MAXIMUM RATINGS	6 (T <sub>A</sub> = 25 °C, unle	ess otherwise n	oted)	
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	$V_{DS}$	- 20	V	
Gate-Source Voltage	$V_{GS}$	± 12	v	
	T <sub>C</sub> = 25 °C		- 4 <sup>a</sup>	
Continuous Proin Current (T = 150 °C)	T <sub>C</sub> = 70 °C	1	- 4	
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	- 4 <sup>a, b, c</sup>	
	T <sub>A</sub> = 70 °C		- 4 <sup>a, b, c</sup>	Α
Pulsed Drain Current (t = 300 μs)	I <sub>DM</sub>	- 25		
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	L	- 2.3	
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	- 1.3 <sup>b, c</sup>	
	T <sub>C</sub> = 25 °C		2.8	
Maximum Bower Dissipation	T <sub>C</sub> = 70 °C	ь	1.8	W
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	1.6 <sup>b, c</sup>	VV
	T <sub>A</sub> = 70 °C		1.0 <sup>b, c</sup>	
Operating Junction and Storage Temperature Ra	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	
Soldering Recommendations (Peak Temperature		260	C	

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Typical	Maximum	Unit			
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 5 s	R <sub>thJA</sub>	60	80	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	34	45	- 'C/VV		

#### Notes:

- a. Package limited.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s.
- d. Maximum under steady state conditions is 125 °C/W.

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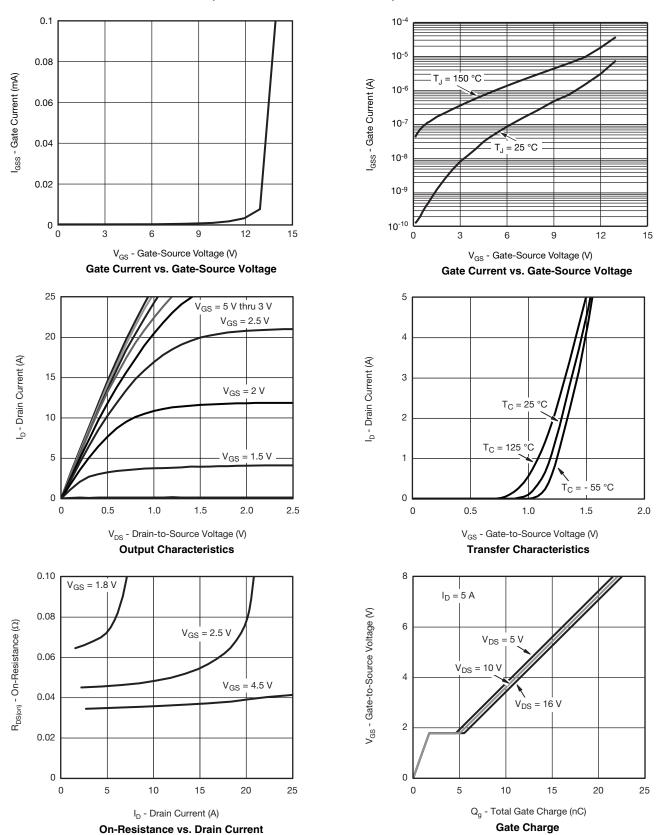
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 20			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	J 050 A		- 11		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = - 250 μA		2.6			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$	- 0.4		- 1	V	
Cata Sauraa Laakaga	_	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 10 \text{ V}$			± 8		
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$			± 1		
Zero Gate Voltage Drain Current	_	V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V			- 1	μΑ	
	I <sub>DSS</sub>	$V_{DS}$ = - 20 V, $V_{GS}$ = 0 V, $T_{J}$ = 55 °C			- 10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le$ - 5 V, $V_{GS} =$ - 10 V	- 15			Α	
		$V_{GS} = -4.5 \text{ V}, I_D = -5 \text{ A}$		0.034			
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = -2.5 \text{ V}, I_D = -4.4 \text{ A}$		0.045		Ω	
		$V_{GS} = -1.8 \text{ V}, I_D = -1 \text{ A}$		0.067			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = -10 \text{ V}, I_{D} = -5 \text{ A}$		16		S	
Dynamic <sup>b</sup>							
Total Gate Charge	Qg	$V_{DS} = -10 \text{ V}, V_{GS} = -8 \text{ V}, I_D = -5 \text{ A}$		22	33	nC	
Gate-Source Charge				12.5	19		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -5 \text{ A}$		1.8			
Gate-Drain Charge	$Q_{gd}$			3.3			
Gate Resistance	$R_g$	f = 1 MHz	0.08	0.43	0.86	kΩ	
Turn-On Delay Time	t <sub>d(on)</sub>			150	225		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 10 V, $R_L$ = 1.4 $\Omega$		300	450		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong$ - 4 A, $V_{GEN}$ = - 4.5 V, $R_g$ = 1 $\Omega$		1620	2430		
Fall Time	t <sub>f</sub>			560	840	ne	
Turn-On Delay Time	t <sub>d(on)</sub>			50	100	ns	
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 10 V, $R_L$ = 1.4 $\Omega$		90	180		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong -4 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$		2500	3750		
Fall Time	t <sub>f</sub>			600	900		
<b>Drain-Source Body Diode Characteris</b>	ics						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 2.3		
Pulse Diode Forward Current	I <sub>SM</sub>				- 25	Α	
Body Diode Voltage	$V_{SD}$	I <sub>S</sub> = -4 A, V <sub>GS</sub> = 0 V		- 0.85	- 1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			18	36	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = - 4 A, dI/dt = 100 A/μs, T <sub>.I</sub> = 25 °C		8	16	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	$_{1F} = -4 \text{ A}, \text{ ui/ut} = 100 \text{ A/}\mu\text{s}, \text{ I}_{J} = 25 ^{\circ}\text{C}$		18		ns	
Reverse Recovery Rise Time	t <sub>b</sub>			10			

#### Notes:

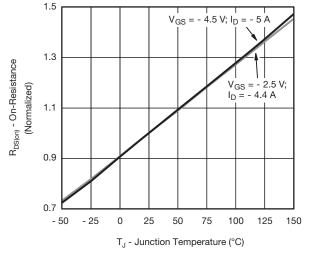
- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

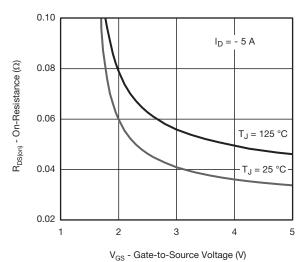




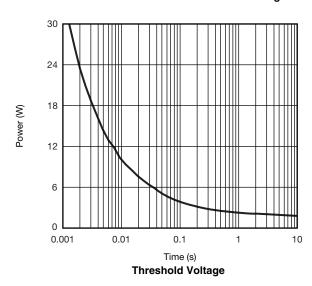


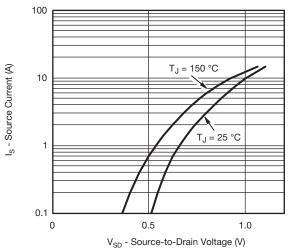


#### On-Resistance vs. Junction Temperature

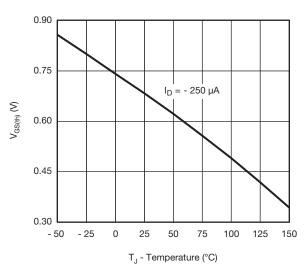


On-Resistance vs. Gate-to-Source Voltage

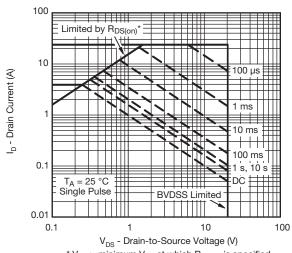




Source-Drain Diode Forward Voltage



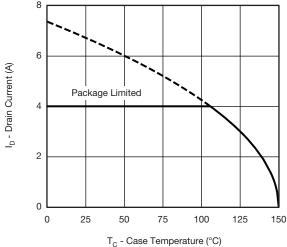
Single Pulse Power, Junction-to-Ambient



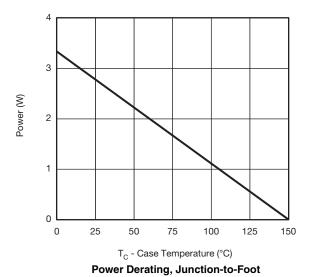
 $^{\star}$  V<sub>GS</sub> > minimum V<sub>GS</sub> at which R<sub>DS(on)</sub> is specified

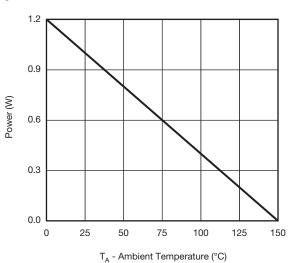
Safe Operating Area, Junction-to-Ambient





#### **Current Derating\***



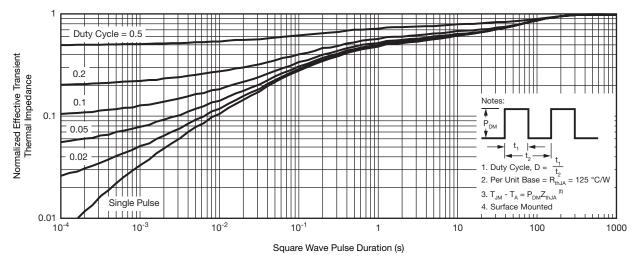


Power Derating, Junction-to-Ambient

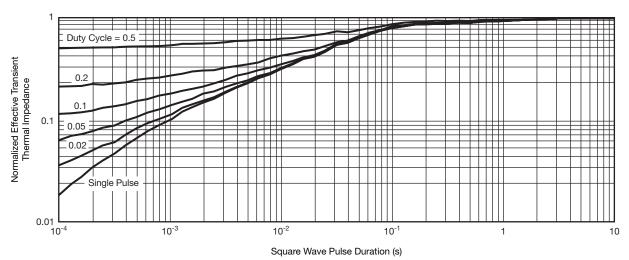
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<sup>\*</sup> The power dissipation PD is based on TJ(max) = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





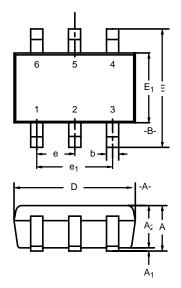
Normalized Thermal Transient Impedance, Junction-to-Ambient

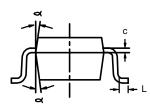


Normalized Thermal Transient Impedance, Junction-to-Foot



# SC-70: 6-LEADS



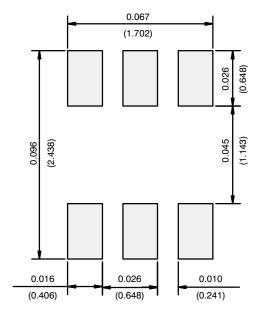


	MILLIMETERS			INCHES		
Dim	Min	Nom	Max	Min	Nom	Max
Α	0.90	-	1.10	0.035	_	0.043
<b>A</b> <sub>1</sub>	ı	-	0.10	-	_	0.004
A <sub>2</sub>	0.80	-	1.00	0.031	_	0.039
b	0.15	-	0.30	0.006	_	0.012
С	0.10	-	0.25	0.004	-	0.010
D	1.80	2.00	2.20	0.071	0.079	0.087
Е	1.80	2.10	2.40	0.071	0.083	0.094
E <sub>1</sub>	1.15	1.25	1.35	0.045	0.049	0.053
е	0.65BSC			0.026BSC		
e <sub>1</sub>	1.20	1.30	1.40	0.047	0.051	0.055
L	0.10	0.20	0.30	0.004	0.008	0.012
9	7°Nom			7°Nom		
ECN: S-03946—Rev. B, 09-Jul-01 DWG: 5550						

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## **RECOMMENDED MINIMUM PADS FOR SC-70: 6-Lead**



Recommended Minimum Pads Dimensions in Inches/(mm)

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