

#### AM1321P-T1-PF-VB Datasheet

# P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A) <sup>c</sup>	Q <sub>g</sub> (Typ.)		
- 20	$0.080 \text{ at V}_{GS} = -4.5 \text{ V}$	- 3.1	4.3 nC		
	0.100 at V <sub>GS</sub> = - 2.5 V	- 2.3	4.5110		

#### **FEATURES**

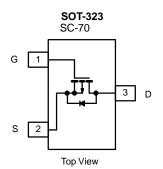
- Halogen-free According to IEC 61249-2-21 Definition
- Trench Power MOSFET
- 100 % R<sub>g</sub> Tested
- Compliant to RoHS Directive 2002/95/EC

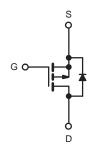


**HALOGEN** FREE

#### **APPLICATIONS**

- Load Switch
- DC/DC Converters





P-Channel MOSFET

**ABSOLUTE MAXIMUM RATINGS** (T<sub>A</sub> = 25 °C, unless otherwise noted) **Parameter** Symbol Limit Unit - 20 Drain-Source Voltage  $V_{DS}$ ٧  $V_{GS}$ Gate-Source Voltage ± 12  $T_C = 25 \, ^{\circ}C$ - 3.1 T<sub>C</sub> = 70 °C - 2.1 Continuous Drain Current (T<sub>J</sub> = 150 °C)  $I_D$ T<sub>A</sub> = 25 °C - 1.4<sup>a, b</sup>  $T_A = 70 \, ^{\circ}C$ - 1.1<sup>a, b</sup> Α  $I_{DM}$ **Pulsed Drain Current** - 6 T<sub>C</sub> = 25 °C - 0.4 Continuous Source-Drain Diode Current  $I_S$ T<sub>A</sub> = 25 °C - 0.3 T<sub>C</sub> = 25 °C 0.5 T<sub>C</sub> = 70 °C 0.3  $P_D$ Maximum Power Dissipation W T<sub>A</sub> = 25 °C 0.4<sup>a, b</sup> T<sub>A</sub> = 70 °C 0.3<sup>a, b</sup>  $T_J$ ,  $T_{stg}$ - 50 to 150 Operating Junction and Storage Temperature Range °С 260 Soldering Recommendations (Peak Temperature)

a. Surface mounted on 1" x 1" FR4 board.

b. t = 10 s.

c. Based on  $T_C = 25$  °C.



THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a, b</sup>	t ≤ 10 s	R <sub>thJA</sub>	250	300	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	225	270	5/ * *	

#### Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. Maximum under steady state conditions is 360 °C/W.

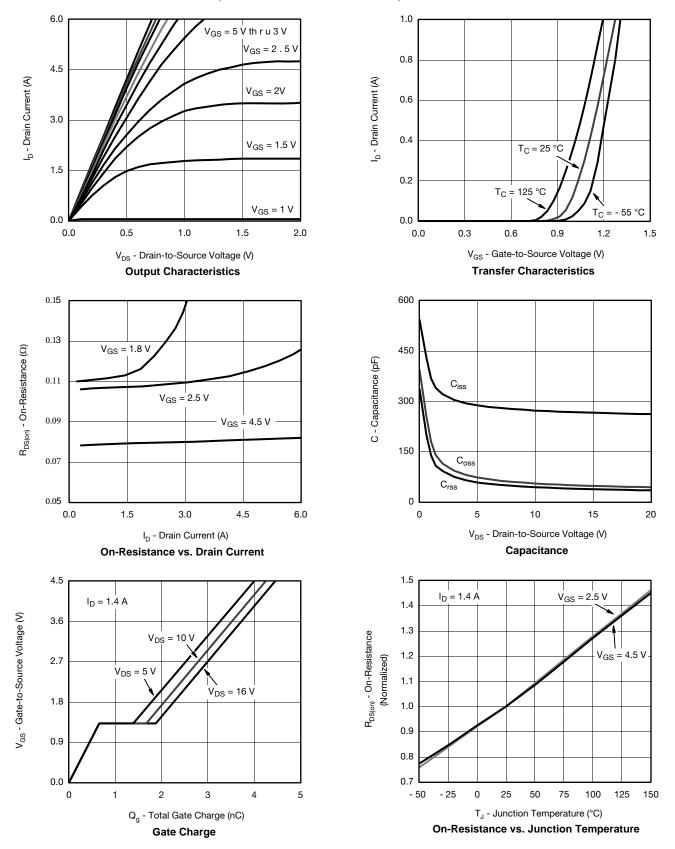
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$	- 20			V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = - 250 μA		- 14		mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = - 250 μA		2.4		mv/C
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 0.45		- 1.5	V
Gate-Source Leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current		$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}$			- 1	μA
Zeio Gate voltage Diain Current	I <sub>DSS</sub>	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			- 10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 2			Α
		$V_{GS} = -4.5 \text{ V}, I_D = -1.4 \text{ A}$		0.080		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = -2.5 \text{ V}, I_D = -1.2 \text{ A}$		0.100		Ω
		V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 0.3 A		0.140		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = -5 \text{ V}, I_{D} = -1.4 \text{ A}$		5		S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>			272		pF
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		55		
Reverse Transfer Capacitance	C <sub>rss</sub>			44		
Total Gate Charge		$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -1.4 \text{ A}$		4.3	6.5	nC
Total Gate Charge				2.7	4.1	
Gate-Source Charge			0.7		lic	
Gate-Drain Charge	$Q_{gd}$			1.0		
Gate Resistance	$R_g$	f = 1 MHz	1.4	7	14	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			12	20	
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 10 V, $R_L$ = 9.1 $\Omega$		20	30	
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D \cong -1.1 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$		23	35	
Fall Time	t <sub>f</sub>			9	18	ns
Turn-On Delay Time	t <sub>d(on)</sub>			5	10	115
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 10 V, $R_L$ = 9.1 $\Omega$		10	20	
Turn-Off DelayTime	t <sub>d(off)</sub>	$t_{d(off)}$ $I_D \cong -1.1 \text{ A}, V_{GEN} = -8 \text{ V}, R_g = 1 \Omega$		18	27	
Fall Time	t <sub>f</sub>			7	14	
<b>Drain-Source Body Diode Characterist</b>	ics					
Continuous Source-Drain Diode Current	I <sub>S</sub>	$I_S$ $T_C = 25  ^{\circ}C$			- 2.4	А
Pulse Diode Forward Current <sup>a</sup> I <sub>SM</sub>					- 6	
Body Diode Voltage	V <sub>SD</sub>	I <sub>F</sub> = - 0.7 A		- 0.8	- 1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>			18	27	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = - 0.7 A, dl/dt = 100 A/μs, T <sub>.I</sub> = 25 °C		7	14	nC
Reverse Recovery Fall Time	t <sub>a</sub>	1F = -0.7 A, αι/αι = 100 A/μS, 1J = 25 °C		7		ns
Reverse Recovery Rise Time	t <sub>b</sub>			11		

#### Notes:

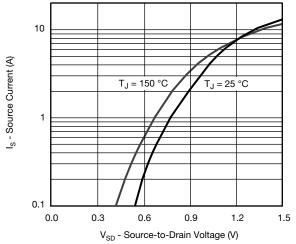
- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

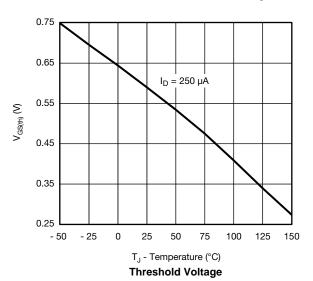








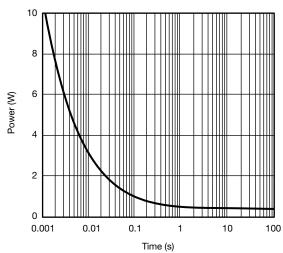
Source-Drain Diode Forward Voltage



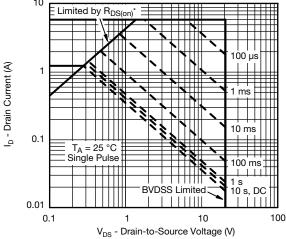
 $C_{\text{O}}^{\text{C}}$  0.24  $C_{\text{O}}^{\text{C}}$  0.16  $C_{\text{O}}^{\text{C}}$  0.08  $C_{\text{O}}^{\text{C}}$  0.08  $C_{\text{O}}^{\text{C}}$  0.32  $C_{\text{O}}^{\text{C}}$  0.16  $C_{\text{O}}^{\text{C}}$  0.32  $C_{\text{O}}^{\text{C}}$  0.4  $C_{\text{O}}^{\text{C}}$  0.7  $C_{\text{O}}^{\text{C}}$  0.7  $C_{\text{O}}^{\text{C}}$  0.7  $C_{\text{O}}^{\text{C}}$  0.8  $C_{\text{O}}^{\text{C}}$  0.9  $C_{\text{O}}^{\text{C$ 

V<sub>GS</sub> - Gate-to-Source Voltage (V)

On-Resistance vs. Gate-to-Source Voltage



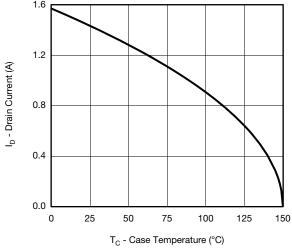
Single Pulse Power, Junction-to-Ambient



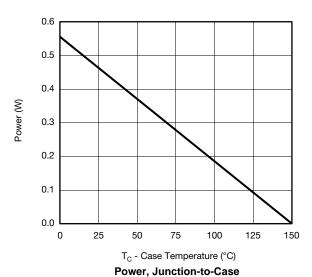
 $^{\star}$  V<sub>GS</sub> > minimum V<sub>GS</sub> at which R<sub>DS(on)</sub> is specified

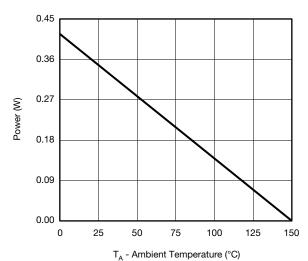
Safe Operating Area, Junction-to-Ambient







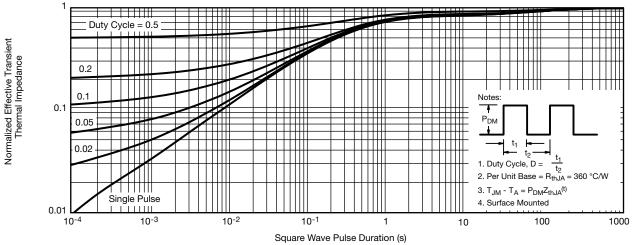




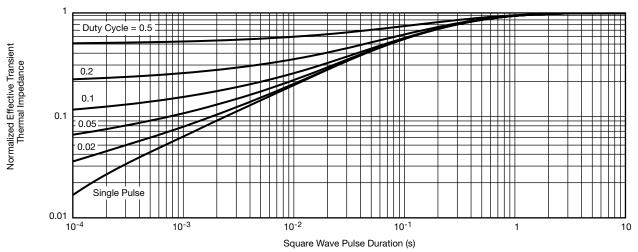
Power, Junction-to-Ambient

<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit





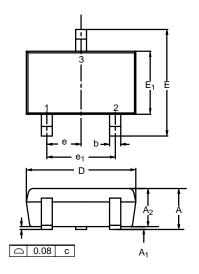
Normalized Thermal Transient Impedance, Junction-to-Ambient

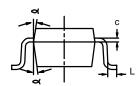


Normalized Thermal Transient Impedance, Junction-to-Foot



### SC-70: 3-LEADS

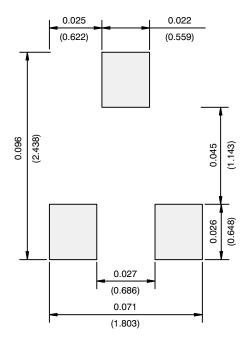




	MILLIMETERS			INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.90	-	1.10	0.035	_	0.043	
A <sub>1</sub>	-	-	0.10	-	-	0.004	
$A_2$	0.80	-	1.00	0.031	_	0.039	
b	0.25	-	0.40	0.010	_	0.016	
С	0.10	-	0.25	0.004	-	0.010	
D	1.80	2.00	2.20	0.071	0.079	0.087	
Ε	1.80	2.10	2.40	0.071	0.083	0.094	
E <sub>1</sub>	1.15	1.25	1.35	0.045	0.049	0.053	
е	0.65BSC			0.026BSC			
e <sub>1</sub>	1.20	1.30	1.40	0.047	0.051	0.055	
L	0.10	0.20	0.30	0.004	0.008	0.012	
9	7°Nom			7°Nom			



#### **RECOMMENDED MINIMUM PADS FOR SC-70: 3-Lead**



Recommended Minimum Pads Dimensions in Inches/(mm)



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