

# VSP030P10MS-VB Datasheet P-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)			
- 100	$0.032 \text{ at V}_{GS} = -10 \text{ V}$	- 28	7.6 nC			
- 100	0.036 at V <sub>GS</sub> = - 4.5 V	- 25	7.6110			

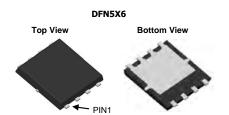
### **FEATURES**

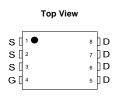
- Trench Power MOSFET
- 100 % UIS Tested

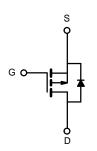
#### **APPLICATIONS**

Load Switch









P-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b>	<b>S</b> (T <sub>A</sub> = 25 °C, unle	ess otherwise not	ted)		
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V <sub>DS</sub>	- 100	V		
Gate-Source Voltage	V <sub>GS</sub>	± 20	V		
	T <sub>C</sub> = 25 °C		- 28 <sup>a</sup>		
Continuous Drain Current /T 450 °C)	T <sub>C</sub> = 70 °C		- 27		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	22.2 <sup>b</sup>	A	
	T <sub>A</sub> = 70 °C		- 26.1 <sup>b</sup>	^	
Pulsed Drain Current	I <sub>DM</sub>	- 15	7		
Avalanche Current Pulse	L = 0.1 mH	I <sub>AS</sub>	- 4.5		
Single Pulse Avalanche Energy	L = 0.1 IIII	E <sub>AS</sub>	10.1	mJ	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	L	6.9 <sup>a</sup>	А	
Continuous Source-Diairi Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	2.1 <sup>b</sup>	^	
	T <sub>C</sub> = 25 °C		10.4 <sup>a</sup>		
Maximum Power Dissipation	T <sub>C</sub> = 70 °C	P <sub>D</sub>	6.6 <sup>a</sup>	w	
	T <sub>A</sub> = 25 °C	' D	1.1 <sup>b</sup>	VV	
	T <sub>A</sub> = 70 °C		2 <sup>b</sup>	7	
Operating Junction and Storage Temperature R	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient <sup>b</sup>	Steady State	$R_{thJA}$	33	40	°C/W		
Maximum Junction-to-Case	Steady State	R <sub>thJC</sub>	0.98	1.2	C/VV		

#### Notes:

- a. Based on  $T_C$  = 25 °C.
- b. Surface mounted on 1" x 1" FR4 board.



arameter Symbol Test Conditions		Min.	Тур.	Max.	Unit	
Static						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 100			V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = - 250 μA		68		mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	i <sub>D</sub> = - 250 μA		- 5.2		mv/°C
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.8		- 3	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zara Cata Valtaga Drain Current	1	V <sub>DS</sub> = - 100 V, V <sub>GS</sub> = 0 V			- 1	
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = - 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 10	μA
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = - 5 V, V <sub>GS</sub> = - 10 V	- 120			Α
D : 0	В	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 3 A		0.032		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = -4.5 \text{ V}, I_D = -2 \text{ A}$		0.036		Ω
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 5 A	20			S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>			3500		pF
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		390		
Reverse Transfer Capacitance	C <sub>rss</sub>			290		
Total Cata Charge	Q <sub>g</sub>	$V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -5 \text{ A}$		76	115	nC
Total Gate Charge				38	60	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = -30 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -5 \text{ A}$		16		
Gate-Drain Charge	$Q_{gd}$			19		
Gate Resistance	$R_g$	f = 1 MHz		5.2		Ω
Turn-On Delay Time	t <sub>d(on)</sub>			10	15	
Rise Time	t <sub>r</sub>	$V_{DD} = -2 V$ , $R_L = 2 \Omega$		7	15	ns
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ - 5 A, $V_{GEN}$ = - 10 V, $R_g$ = 1 $\Omega$		70	110	
Fall Time	t <sub>f</sub>			40	60	
<b>Drain-Source Body Diode Characteristics</b>	s					
Continuous Source-Drain Diode Current	IS	$T_C = 25  ^{\circ}C$			- 6.9	Α
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				- 15	^
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 3 A		- 1	- 1.5	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>			45	68	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	L = 5 A di/dt = 10 A/vo T = 25 °C		59	120	nC
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = -5 \text{ A, di/dt} = 10 \text{ A/}\mu\text{s, T}_J = 25 \text{ °C}$		29		
Reverse Recovery Rise Time	t <sub>b</sub>			16		ns

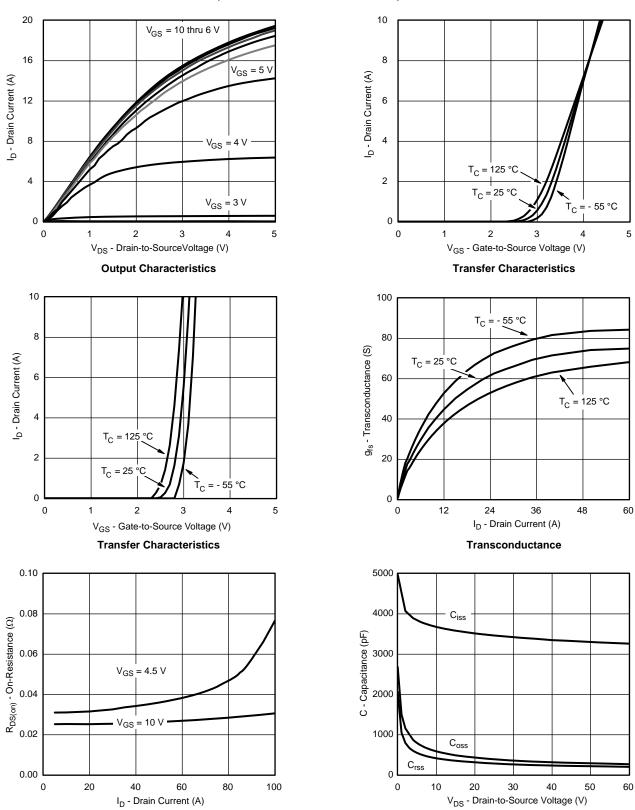
### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



# TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



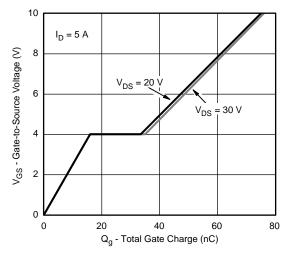
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On-Resistance vs. Drain Current

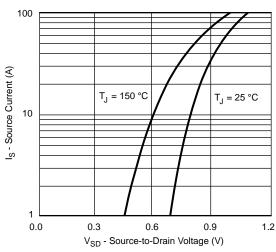
Capacitance



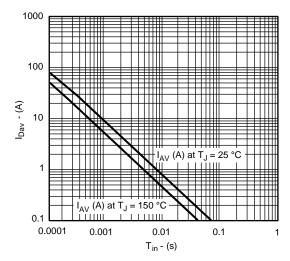
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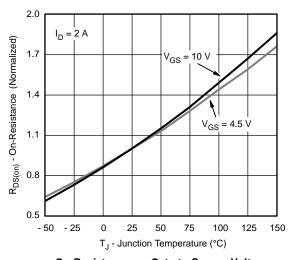




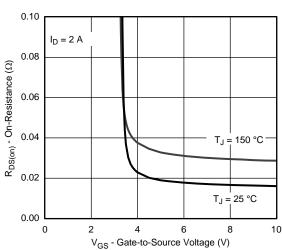
Source-Drain Diode Forward Voltage



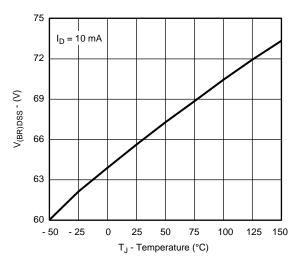
Single Pulse Avalanche Current Capability vs. Time



On-Resistance vs. Gate-to-Source Voltage



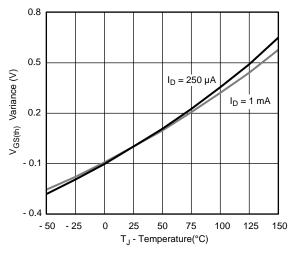
On-Resistance vs. Gate-to-Source Voltage

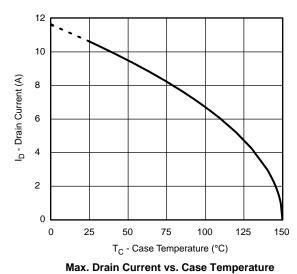


Drain-Source Breakdown Voltage vs. Junction Temperature

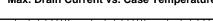


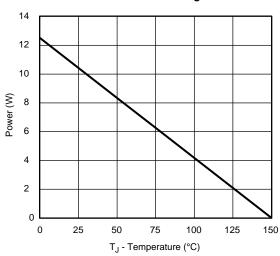
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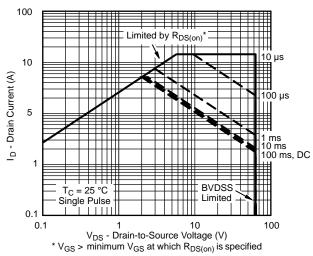




Threshold Voltage

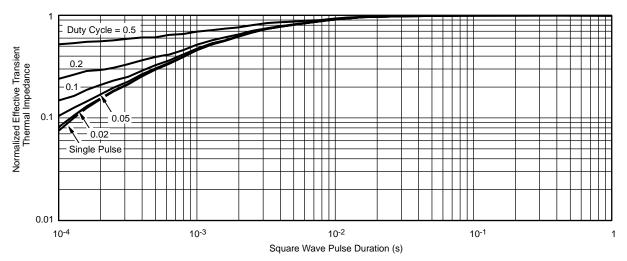






# Power Derating, Junction-to-Case

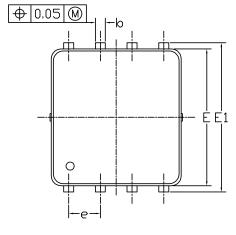
Safe Operating Area, Junction-to-Case

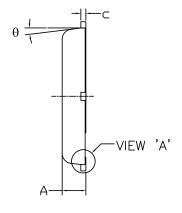


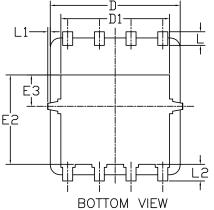
Normalized Thermal Transient Impedance, Junction-to-Case

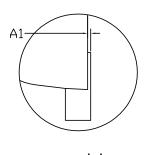


DFN5x6\_8L\_EP1\_P PACKAGE OUTLIN



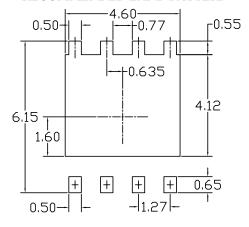






<u>VIEW 'A'</u> (SCALE 5:1)

#### RECOMMENDED LAND PATTERN



SYMBOLS DIMENSIONS IN MILLIMETERS			METERS	DIMENSIONS IN INCHES			
3 I MBOLS	MIN	NOM	MAX	MIN	NOM	MAX	
A	0.85	0. 95	1.00	0.033	0.037	0.039	
Al	0.00		0.05	0.000		0.002	
b	0.30	0.40	0.50	0.012	0.016	0.020	
С	0. 15	0. 20	0. 25	0.006	0.008	0.010	
D	5. 10	5. 20	5. 30	0. 201	0. 205	0. 209	
D1	4. 25	4. 35	4. 45	0. 167	0.171	0. 175	
Е	5. 45	5. 55	5. 65	0. 215	0.219	0. 222	
E1	5. 95	6.05	6. 15	0. 234	0. 238	0. 242	
E2	3. 525	3.625	3. 725	0.139	0. 143	0.147	
E3	1. 175	1. 275	1. 375	0.046	0.050	0.054	
e	1. 27 BSC			0.050 BSC			
L	0.45	0. 55	0.65	0.018	0.022	0.026	
L1	0		0. 15	0		0.006	
L2	0.68 REF			0.027 REF			
θ	0°		10°	0°		10°	

#### **NOTE**

- UNIT: mm
- 1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS. MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH.
- 2. CONTROLLING DIMENSION IS MILLIMETER. CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.



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