

SIR432DP-T1-GE3-VB Datasheet N-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{(BR)DSS} (V)	r _{DS(on)} (Ω)	I _D (A)		
100	0.017 at V _{GS} = 10 V	30		

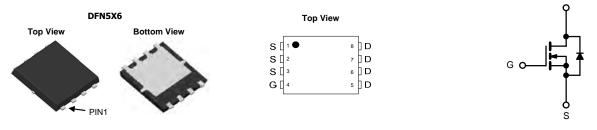
FEATURES

- Trench Power MOSFET
- 175 °C Junction Temperature
- Low Thermal Resistance Package
- 100 % R_g Tested

APPLICATIONS

• Isolated DC/DC Converters





N-Channel MOSFET

D

PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V _{DS}	100	V	
Gate-source voltage		V _{GS}	± 20		
	T _C = 25 °C		30		
Constitution of the summer (T 150 °C)	T _C = 70 °C		19		
Continuous drain current ($T_J = 150 \ ^{\circ}C$)	T _A = 25 °C	I _D	10 ^{b, c}		
	T _A = 70 °C	1	8.5 ^{b, c}	Α	
Pulsed drain current (t = 100 µs)		I _{DM}	75		
	T _C = 25 °C		56		
Continuous source-drain diode current	T _A = 25 °C	I _S	4.5 ^{b, c}		
Single pulse avalanche current		I _{AS}	20		
Single pulse avalanche energy	L = 0.1 mH	E _{AS}	20	mJ	
	T _C = 25 °C		60		
	T _C = 70 °C		40	14/	
Maximum power dissipation	T _A = 25 °C	PD	5 b, c	W	
	T _A = 70 °C	1	3.2 ^{b, c}		
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +150	•0	
Soldering recommendations (peak tempera		260	°C		

THERMAL RESISTANCE RATINGS							
	SYMBOL	TYPICAL	MAXIMUM	UNIT			
t ≤ 10 s	R _{thJA}	20	25	°C/W			
Steady state	R _{thJC}	1.6	2	0/10			
	t ≤ 10 s	SYMBOL t ≤ 10 s R _{thJA}	SYMBOL TYPICAL t ≤ 10 s R _{thJA} 20	SYMBOL TYPICAL MAXIMUM t ≤ 10 s R _{thJA} 20 25			

Notes

a. Package limitedb. Surface mounted on 1" x 1" FR4 board

c. t = 10 s

SPECIFICATIONS ($T_J = 25 \ ^{\circ}C$,	SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)							
PARAMETER	RAMETER SYMBOL TEST CONDITIONS				MAX.	UNIT		
Static								
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \ \mu\text{A}$	100	-	-	V		
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$	$_{\rm S}/{\rm T_J}$ I _D = 10 mA		81	-			
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA	-	-7.5	-	mV/°C		
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	3	-	5	V		
Gate-source leakage	I _{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V	-	-	100	nA		
Zero gate voltage drain current	1	$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	-	1	μA		
Zelo gate voltage drain current	I _{DSS}	V_{DS} = 100 V, V_{GS} = 0 V, T_{J} = 70 $^{\circ}C$	-	-	15			
On-state drain current ^a	I _{D(on)}	$V_{DS} \geq 10 \ V, \ V_{GS} = 10 \ V$	40	-	-	А		
Drain-source on-state resistance ^a	P	V_{GS} =10 V, I _D = 10 A	-	0.0170	-			
Drain-source on-state resistance -	R _{DS(on)}	$V_{GS} = 7.5 \text{ V}, \text{ I}_{D} = 10 \text{ A}$	-	0.0200	-	Ω		
Forward transconductance ^a	g _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 10 \text{ A}$	-	46	-	S		
Dynamic ^b								
Input capacitance	C _{iss}		-	1470	-			
Output capacitance	C _{oss}	V _{DS} = 50 V, V _{GS} = 0 V, f = 1 MHz		132	-	pF		
Reverse transfer capacitance	C _{rss}		-	11.2	-	1		
Total gate charge		$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$	-	20	-	nC		
	Qg		-	15	-			
Gate-source charge	Q _{gs}	V_{DS} = 50 V, V_{GS} = 7.5 V, I_D = 10 A	-	6.45	-			
Gate-drain charge	Q _{gd}		-	3.5	-			
Output charge	Q _{oss}	$V_{DS} = 50 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	22	-			
Gate resistance	Rg	f = 1 MHz	0.2	0.76	1.4	Ω		
Turn-on delay time	t _{d(on)}		-	12	24			
Rise time	t _r	$V_{DD} = 50 \text{ V}, \text{ R}_{L} = 5 \Omega, \text{ I}_{D} \cong 10 \text{ A},$	-	5	10			
Turn-off delay time	t _{d(off)}	V_{GEN} = 10 V, R_g = 1 Ω	-	19	38			
Fall time	t _f		-	5	10			
Turn-on delay time	t _{d(on)}		-	15	30	ns		
Rise time	t _r	V_{DD} = 50 V, R_L = 5 Ω , $I_D \cong$ 10 A,	-	6	12	-		
Turn-off delay time	t _{d(off)}	V_{GEN} = 7.5 V, R_g = 1 Ω	-	19	38			
Fall time	t _f		-	5	10			
Drain-Source Body Diode Characterist	ics		•		•	•		
Continuous source-drain diode current	I _S	$T_{C} = 25 \ ^{\circ}C$	-	-	56.8	^		
Pulse diode forward current	I _{SM}		-	-	80	A		
Body diode voltage	V _{SD}	$I_{\rm S} = 5$ A, $V_{\rm GS} = 0$ V	-	0.78	1.1	V		
Body diode reverse recovery time	t _{rr}		-	43	86	ns		
Body diode reverse recovery charge	Q _{rr}		-	72	144	nC		
Reverse recovery fall time	t _a	$I_F = 10 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^\circ\text{C}$	-	33	-			
Reverse recovery rise time	t _b		-	10	-	ns		

Notes

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %

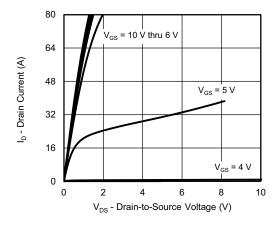
b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

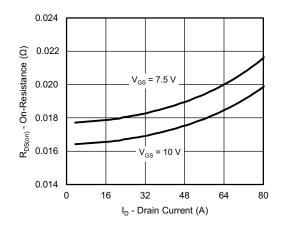
VBsemi VBsemi.com

SIR432DP-T1-GE3-VB

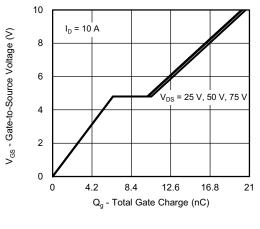
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



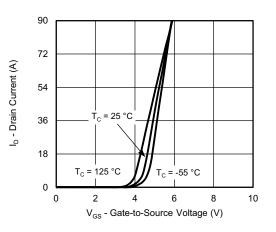
Output Characteristics



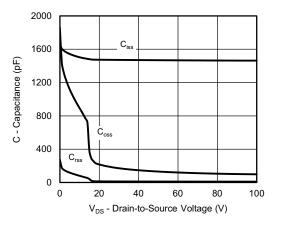
On-Resistance vs. Drain Current and Gate Voltage



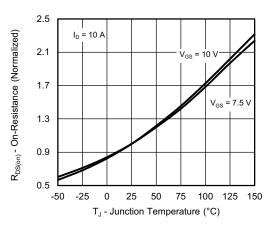
Gate Charge



Transfer Characteristics



Capacitance

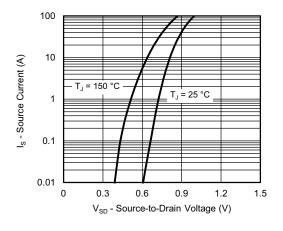


On-Resistance vs. Junction Temperature

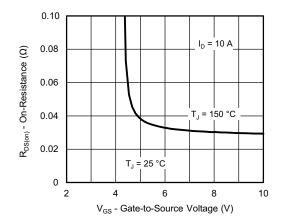
SIR432DP-T1-GE3-VB



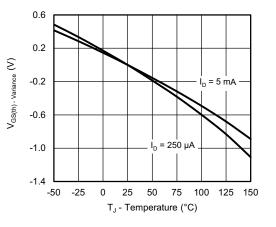
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



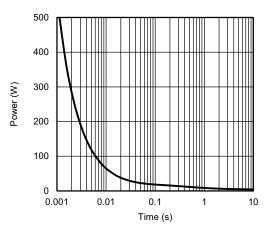
Source-Drain Diode Forward Voltage



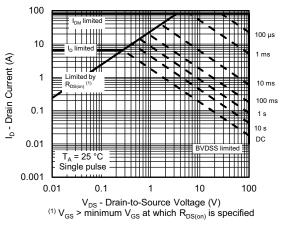
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



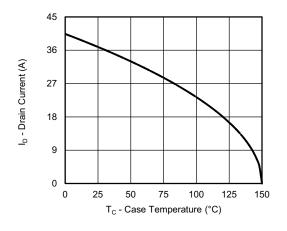
Single Pulse Power, Junction-to-Ambient



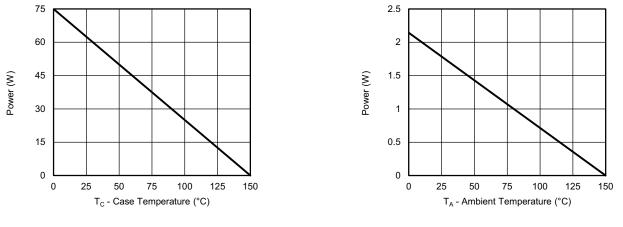
Safe Operating Area, Junction-to-Ambient



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating ^a



Power, Junction-to-Case

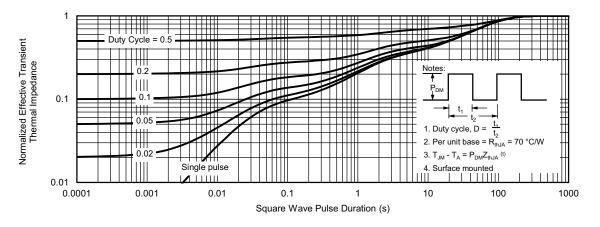
Power, Junction-to-Ambient

Note

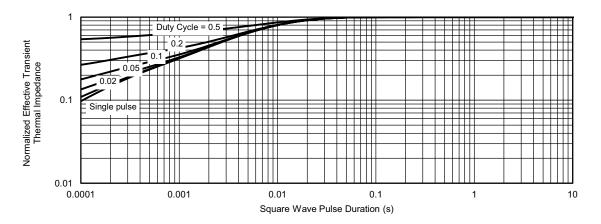
a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

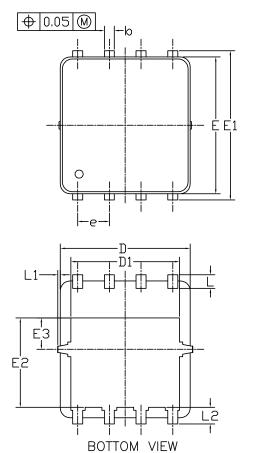


Normalized Thermal Transient Impedance, Junction-to-Ambient

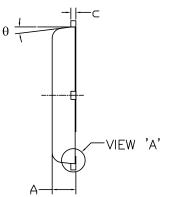


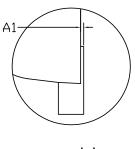
Normalized Thermal Transient Impedance, Junction-to-Case





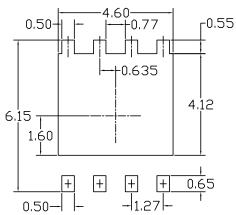
DFN5x6_8L_EP1_P PACKAGE OUTLIN





<u>VIEW 'A'</u> (SCALE 5:1)

RECOMMENDED LAND PATTERN



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES			
SIMBOLS	MIN	NOM	MAX	MIN	NOM	MAX	
A	0.85	0.95	1.00	0.033	0.037	0.039	
Al	0.00		0.05	0.000		0.002	
b	0.30	0.40	0.50	0.012	0.016	0.020	
c	0.15	0.20	0.25	0.006	0.008	0.010	
D	5.10	5.20	5.30	0.201	0.205	0.209	
D1	4.25	4.35	4.45	0.167	0.171	0.175	
E	5.45	5.55	5.65	0.215	0.219	0.222	
E1	5.95	6.05	6.15	0.234	0.238	0.242	
E2	3.525	3.625	3.725	0.139	0.143	0.147	
E3	1.175	1.275	1.375	0.046	0.050	0.054	
e	1.27 BSC				0.050 BSC		
L	0.45	0.55	0.65	0.018	0.022	0.026	
L1	0		0.15	0		0.006	
L2	0.68 REF			0.027 REF			
θ	0°		10°	0°		10°	

UNIT: mm

NOTE 1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS. MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH. 2. CONTROLLING DIMENSION IS MILLIMETER.

CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.



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