

# SIR432DP-T1-GE3-VB Datasheet N-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>(BR)DSS</sub> (V)	r <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)		
100	0.017 at V <sub>GS</sub> = 10 V	30		

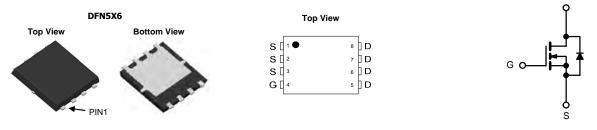
#### **FEATURES**

- Trench Power MOSFET
- 175 °C Junction Temperature
- Low Thermal Resistance Package
- 100 % R<sub>g</sub> Tested

#### **APPLICATIONS**

• Isolated DC/DC Converters





N-Channel MOSFET

D

PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V <sub>DS</sub>	100	V	
Gate-source voltage		V <sub>GS</sub>	± 20		
	T <sub>C</sub> = 25 °C		30		
Constitution of the summer (T 150 °C)	T <sub>C</sub> = 70 °C		19		
Continuous drain current ( $T_J = 150 \ ^{\circ}C$ )	T <sub>A</sub> = 25 °C	I <sub>D</sub>	10 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C	1	8.5 <sup>b, c</sup>	Α	
Pulsed drain current (t = 100 µs)		I <sub>DM</sub>	75		
	T <sub>C</sub> = 25 °C		56		
Continuous source-drain diode current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	4.5 <sup>b, c</sup>		
Single pulse avalanche current		I <sub>AS</sub>	20		
Single pulse avalanche energy	L = 0.1 mH	E <sub>AS</sub>	20	mJ	
	T <sub>C</sub> = 25 °C		60		
	T <sub>C</sub> = 70 °C		40	14/	
Maximum power dissipation	T <sub>A</sub> = 25 °C	PD	5 b, c	W	
	T <sub>A</sub> = 70 °C	1	3.2 <sup>b, c</sup>		
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	•0	
Soldering recommendations (peak tempera		260	°C		

THERMAL RESISTANCE RATINGS							
	SYMBOL	TYPICAL	MAXIMUM	UNIT			
t ≤ 10 s	R <sub>thJA</sub>	20	25	°C/W			
Steady state	R <sub>thJC</sub>	1.6	2	0/10			
	t ≤ 10 s	SYMBOL   t ≤ 10 s R <sub>thJA</sub>	SYMBOL TYPICAL   t ≤ 10 s R <sub>thJA</sub> 20	SYMBOL TYPICAL MAXIMUM   t ≤ 10 s R <sub>thJA</sub> 20 25			

Notes

a. Package limitedb. Surface mounted on 1" x 1" FR4 board

c. t = 10 s

<b>SPECIFICATIONS</b> ( $T_J = 25 \ ^{\circ}C$ ,	<b>SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C, unless otherwise noted)							
PARAMETER	RAMETER SYMBOL TEST CONDITIONS				MAX.	UNIT		
Static								
Drain-source breakdown voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \ \mu\text{A}$	100	-	-	V		
V <sub>DS</sub> temperature coefficient	$\Delta V_{DS}/T_{J}$	$_{\rm S}/{\rm T_J}$ I <sub>D</sub> = 10 mA		81	-			
V <sub>GS(th)</sub> temperature coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA	-	-7.5	-	mV/°C		
Gate-source threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	3	-	5	V		
Gate-source leakage	I <sub>GSS</sub>	$V_{DS}$ = 0 V, $V_{GS}$ = ± 20 V	-	-	100	nA		
Zero gate voltage drain current	1	$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	-	1	μA		
Zelo gate voltage drain current	I <sub>DSS</sub>	$V_{DS}$ = 100 V, $V_{GS}$ = 0 V, $T_{J}$ = 70 $^{\circ}C$	-	-	15			
On-state drain current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \geq 10 \ V, \ V_{GS} = 10 \ V$	40	-	-	А		
Drain-source on-state resistance <sup>a</sup>	P	$V_{GS}$ =10 V, I <sub>D</sub> = 10 A	-	0.0170	-			
Drain-source on-state resistance -	R <sub>DS(on)</sub>	$V_{GS} = 7.5 \text{ V}, \text{ I}_{D} = 10 \text{ A}$	-	0.0200	-	Ω		
Forward transconductance <sup>a</sup>	g <sub>fs</sub>	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 10 \text{ A}$	-	46	-	S		
Dynamic <sup>b</sup>								
Input capacitance	C <sub>iss</sub>		-	1470	-			
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 0 V, f = 1 MHz		132	-	pF		
Reverse transfer capacitance	C <sub>rss</sub>		-	11.2	-	1		
Total gate charge		$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$	-	20	-	nC		
	Qg		-	15	-			
Gate-source charge	Q <sub>gs</sub>	$V_{DS}$ = 50 V, $V_{GS}$ = 7.5 V, $I_D$ = 10 A	-	6.45	-			
Gate-drain charge	Q <sub>gd</sub>		-	3.5	-			
Output charge	Q <sub>oss</sub>	$V_{DS} = 50 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	22	-			
Gate resistance	Rg	f = 1 MHz	0.2	0.76	1.4	Ω		
Turn-on delay time	t <sub>d(on)</sub>		-	12	24			
Rise time	t <sub>r</sub>	$V_{DD} = 50 \text{ V}, \text{ R}_{L} = 5 \Omega, \text{ I}_{D} \cong 10 \text{ A},$	-	5	10			
Turn-off delay time	t <sub>d(off)</sub>	$V_{GEN}$ = 10 V, $R_g$ = 1 $\Omega$	-	19	38			
Fall time	t <sub>f</sub>		-	5	10			
Turn-on delay time	t <sub>d(on)</sub>		-	15	30	ns		
Rise time	t <sub>r</sub>	$V_{DD}$ = 50 V, $R_L$ = 5 $\Omega$ , $I_D \cong$ 10 A,	-	6	12	-		
Turn-off delay time	t <sub>d(off)</sub>	$V_{GEN}$ = 7.5 V, $R_g$ = 1 $\Omega$	-	19	38			
Fall time	t <sub>f</sub>		-	5	10			
Drain-Source Body Diode Characterist	ics		•		•	•		
Continuous source-drain diode current	I <sub>S</sub>	$T_{C} = 25 \ ^{\circ}C$	-	-	56.8	^		
Pulse diode forward current	I <sub>SM</sub>		-	-	80	A		
Body diode voltage	V <sub>SD</sub>	$I_{\rm S} = 5$ A, $V_{\rm GS} = 0$ V	-	0.78	1.1	V		
Body diode reverse recovery time	t <sub>rr</sub>		-	43	86	ns		
Body diode reverse recovery charge	Q <sub>rr</sub>		-	72	144	nC		
Reverse recovery fall time	t <sub>a</sub>	$I_F = 10 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^\circ\text{C}$	-	33	-			
Reverse recovery rise time	t <sub>b</sub>		-	10	-	ns		

Notes

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %

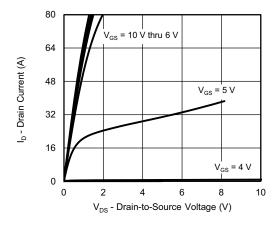
b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

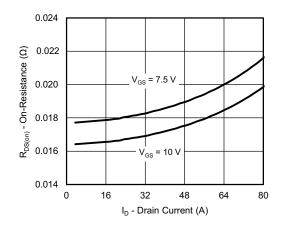
VBsemi VBsemi.com

## SIR432DP-T1-GE3-VB

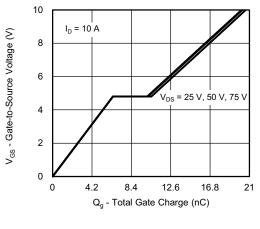
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



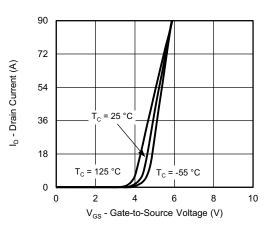
#### **Output Characteristics**



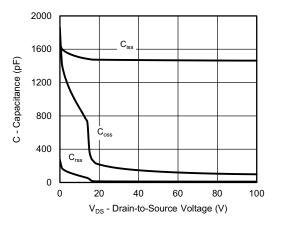
**On-Resistance vs. Drain Current and Gate Voltage** 



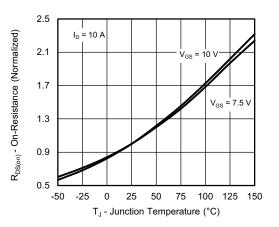
Gate Charge



**Transfer Characteristics** 



Capacitance

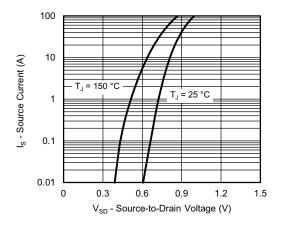


**On-Resistance vs. Junction Temperature** 

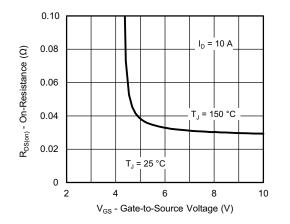
### SIR432DP-T1-GE3-VB



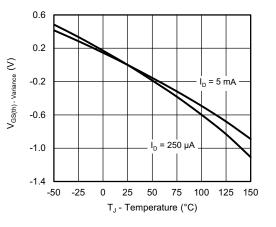
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



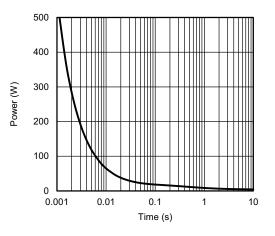
Source-Drain Diode Forward Voltage



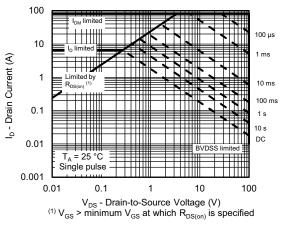
On-Resistance vs. Gate-to-Source Voltage



**Threshold Voltage** 



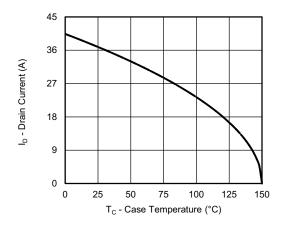
Single Pulse Power, Junction-to-Ambient



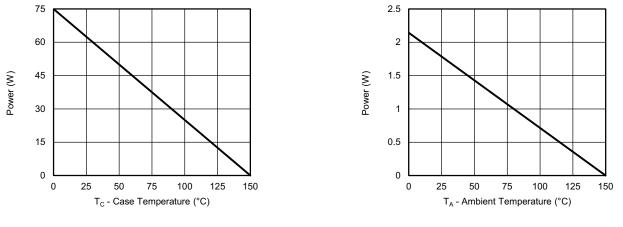
Safe Operating Area, Junction-to-Ambient



#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating <sup>a</sup>



Power, Junction-to-Case

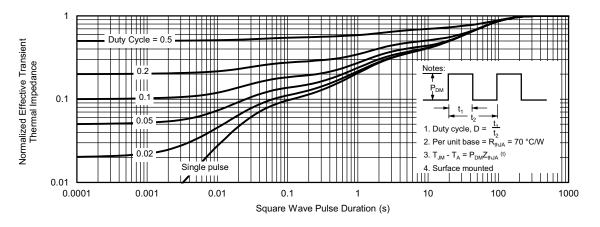
Power, Junction-to-Ambient

#### Note

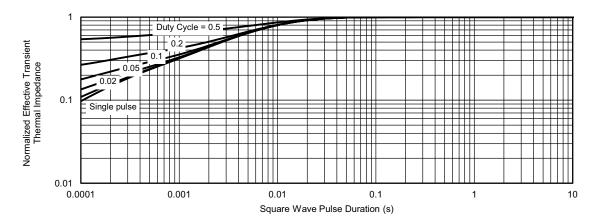
a. The power dissipation P<sub>D</sub> is based on T<sub>J</sub> max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit



#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

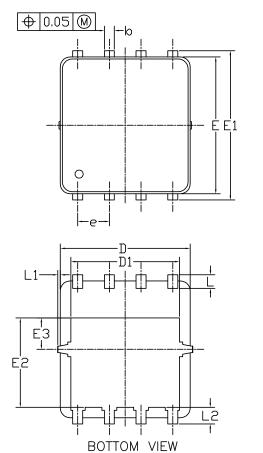


Normalized Thermal Transient Impedance, Junction-to-Ambient

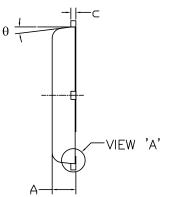


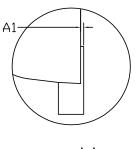
Normalized Thermal Transient Impedance, Junction-to-Case





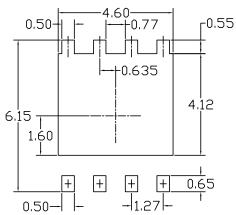
DFN5x6\_8L\_EP1\_P PACKAGE OUTLIN





<u>VIEW 'A'</u> (SCALE 5:1)

**RECOMMENDED LAND PATTERN** 



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES			
SIMBOLS	MIN	NOM	MAX	MIN	NOM	MAX	
A	0.85	0.95	1.00	0.033	0.037	0.039	
Al	0.00		0.05	0.000		0.002	
b	0.30	0.40	0.50	0.012	0.016	0.020	
c	0.15	0.20	0.25	0.006	0.008	0.010	
D	5.10	5.20	5.30	0.201	0.205	0.209	
D1	4.25	4.35	4.45	0.167	0.171	0.175	
E	5.45	5.55	5.65	0.215	0.219	0.222	
E1	5.95	6.05	6.15	0.234	0.238	0.242	
E2	3.525	3.625	3.725	0.139	0.143	0.147	
E3	1.175	1.275	1.375	0.046	0.050	0.054	
e	1.27 BSC				0.050 BSC		
L	0.45	0.55	0.65	0.018	0.022	0.026	
L1	0		0.15	0		0.006	
L2	0.68 REF			0.027 REF			
θ	0°		10°	0°		10°	

#### UNIT: mm

NOTE 1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS. MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH. 2. CONTROLLING DIMENSION IS MILLIMETER.

CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.



# Disclaimer

All products due to improve reliability, function or design or for other reasons, product specifications and data are subject to change without notice.

Taiwan VBsemi Electronics Co., Ltd., branches, agents, employees, and all persons acting on its or their representatives (collectively, the "Taiwan VBsemi"), assumes no responsibility for any errors, inaccuracies or incomplete data contained in the table or any other any disclosure of any information related to the product.(www.VBsemi.com)

Taiwan VBsemi makes no guarantee, representation or warranty on the product for any particular purpose of any goods or continuous production. To the maximum extent permitted by applicable law on Taiwan VBsemi relinquished: (1) any application and all liability arising out of or use of any products; (2) any and all liability, including but not limited to special, consequential damages or incidental; (3) any and all implied warranties, including a particular purpose, non-infringement and merchantability guarantee.

Statement on certain types of applications are based on knowledge of the product is often used in a typical application of the general product VBsemi Taiwan demand that the Taiwan VBsemi of. Statement on whether the product is suitable for a particular application is non-binding. It is the customer's responsibility to verify specific product features in the products described in the specification is appropriate for use in a particular application. Parameter data sheets and technical specifications can be provided may vary depending on the application and performance over time. All operating parameters, including typical parameters must be made by customer's technical experts validated for each customer application. Product specifications do not expand or modify Taiwan VBsemi purchasing terms and conditions, including but not limited to warranty herein.

Unless expressly stated in writing, Taiwan VBsemi products are not intended for use in medical, life saving, or life sustaining applications or any other application. Wherein VBsemi product failure could lead to personal injury or death, use or sale of products used in Taiwan VBsemi such applications using client did not express their own risk. Contact your authorized Taiwan VBsemi people who are related to product design applications and other terms and conditions in writing.

The information provided in this document and the company's products without a license, express or implied, by estoppel or otherwise, to any intellectual property rights granted to the VBsemi act or document. Product names and trademarks referred to herein are trademarks of their respective representatives will be all.

# **Material Category Policy**

Taiwan VBsemi Electronics Co., Ltd., hereby certify that all of the products are determined to be RoHS compliant and meets the definition of restrictions under Directive of the European Parliament 2011/65 / EU, 2011 Nian. 6. 8 Ri Yue restrict the use of certain hazardous substances in electrical and electronic equipment (EEE) - modification, unless otherwise specified as inconsistent.(www.VBsemi.com)

Please note that some documents may still refer to Taiwan VBsemi RoHS Directive 2002/95 / EC. We confirm that all products identified as consistent with the Directive 2002/95 / EC European Directive 2011/65 /.

Taiwan VBsemi Electronics Co., Ltd. hereby certify that all of its products comply identified as halogen-free halogen-free standards required by the JEDEC JS709A. Please note that some Taiwanese VBsemi documents still refer to the definition of IEC 61249-2-21, and we are sure that all products conform to confirm compliance with IEC 61249-2-21 standard level JS709A.