

AON6450-VB Datasheet

N-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{(BR)DSS} (V)	r _{DS(on)} (Ω)	I _D (A)		
100	0.009 at V _{GS} = 10 V	65		

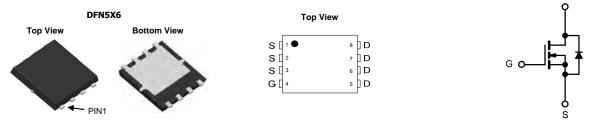
FEATURES

- Trench Power MOSFET
- 175 °C Junction Temperature
- Low Thermal Resistance Package
- 100 % R_g Tested

APPLICATIONS

• Isolated DC/DC Converters





N-Channel MOSFET

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PARAMETER Drain-source voltage Gate-source voltage		SYMBOL	LIMIT	UNIT	
		V _{DS}	100 ± 20		
		V _{GS}			
	T _C = 25 °C		65		
	T _C = 70 °C	1 . 🗖	60		
Continuous drain current (T _J = 150 °C)	T _A = 25 °C	I _D	20 ^{b, c}		
	T _A = 70 °C	1	18.5 ^{b, c}	A	
Pulsed drain current (t = 100 µs)		I _{DM}	180		
Continuous source-drain diode current	T _C = 25 °C		60		
	T _A = 25 °C	I _S	4.8 ^{b, c}		
Single pulse avalanche current		I _{AS}	30		
Single pulse avalanche energy	L = 0.1 mH	E _{AS}	40	mJ	
	T _C = 25 °C		80		
	T _C = 70 °C		50	w	
Maximum power dissipation	T _A = 25 °C	PD	5 ^{b, c}		
	T _A = 70 °C	1	3.2 ^{b, c}		
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +150	<u></u>	
Soldering recommendations (peak temperature) ^c		Ĭ	260		

THERMAL RESISTANCE RATINGS							
	SYMBOL	TYPICAL	MAXIMUM	UNIT			
t ≤ 10 s	R _{thJA}	20	25	°C/W			
Steady state	R _{thJC}	1.6	2	- 0/00			
	t ≤ 10 s	SYMBOL t ≤ 10 s R _{thJA}	SYMBOL TYPICAL t ≤ 10 s R _{thJA} 20	SYMBOL TYPICAL MAXIMUM t ≤ 10 s R _{thJA} 20 25			

Notes

a. Package limitedb. Surface mounted on 1" x 1" FR4 board

c. t = 10 s

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static			•			
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 V, I_D = 250 \mu A$	100	-	-	V
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$	l _D = 10 mA	-	81	-	
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA	-	-7.5	-	mV/°C
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	3	-	5	V
Gate-source leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$	-	-	100	nA
		$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	-	1	μA
Zero gate voltage drain current	IDSS	V_{DS} = 100 V, V_{GS} = 0 V, T_{J} = 70 °C	-	-	15	
On-state drain current ^a	I _{D(on)}	$V_{DS} \ge 10$ V, V_{GS} =10 V	40	-	-	А
	_	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 10 \text{ A}$	-	0.009	-	Ω
Drain-source on-state resistance ^a	R _{DS(on)}	$V_{GS} = 7.5 \text{ V}, \text{ I}_{D} = 10 \text{ A}$	-	0.012	-	
Forward transconductance ^a	g _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 10 \text{ A}$	-	46	-	S
Dynamic ^b			•			
Input capacitance	C _{iss}		-	3970	-	
Output capacitance	C _{oss}	$V_{DS} = 50 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$	-	132	-	pF
Reverse transfer capacitance	C _{rss}		-	11.2	-	1
	0	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 10 \text{ A}$	-	20	-	
Total gate charge	Q _g	-	15	-	1	
Gate-source charge	Q _{gs}	$V_{DS} = 50 \text{ V}, V_{GS} = 7.5 \text{ V}, I_D = 10 \text{ A}$	-	6.45	-	nC
Gate-drain charge	Q _{gd}		-	3.5	-	
Output charge	Q _{oss}	$V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}$	-	22	-	1
Gate resistance	R _q	f = 1 MHz	0.2	0.76	1.4	Ω
Turn-on delay time	t _{d(on)}		-	12	24	
Rise time	t _r	$V_{DD} = 50 \text{ V}, \text{ R}_{\text{I}} = 5 \Omega, \text{ I}_{\text{D}} \cong 10 \text{ A},$	-	5	10	
Turn-off delay time	t _{d(off)}	$V_{\text{GEN}} = 10 \text{ V}, \text{ R}_{\text{g}} = 1 \Omega$	-	19	38	1
Fall time	t _f		-	5	10	1
Turn-on delay time	t _{d(on)}		-	15	30	ns
Rise time	t _r	$V_{DD} = 50 \text{ V}, \text{ R}_{L} = 5 \Omega, \text{ I}_{D} \cong 10 \text{ A},$	-	6	12	-
Turn-off delay time	t _{d(off)}	$V_{\text{GEN}} = 7.5 \text{ V}, \text{ R}_{\text{g}} = 1 \Omega$	-	19	38	
Fall time	t _f		-	5	10	1
Drain-Source Body Diode Characteristi	cs		I		•	<u> </u>
Continuous source-drain diode current	I _S	T _C = 25 °C	-	-	60	Γ.
Pulse diode forward current	I _{SM}		-	-	80	A
Body diode voltage	V _{SD}	I _S = 5 A, V _{GS} = 0 V	-	0.78	1.1	V
Body diode reverse recovery time	t _{rr}		-	43	86	ns
Body diode reverse recovery charge	Q _{rr}		-	72	144	nC
Reverse recovery fall time	ta	$I_F = 10 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^\circ\text{C}$	-	33	-	
Reverse recovery rise time	t _b		-	10	-	ns

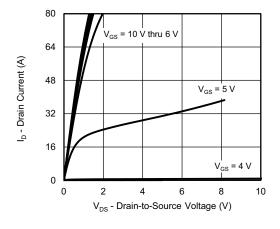
Notes

a. Pulse test; pulse width $\leq 300~\mu\text{s},$ duty cycle $\leq 2~\%$

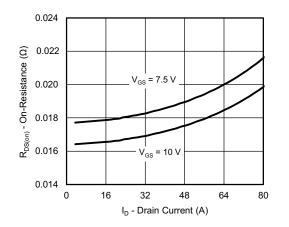
b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

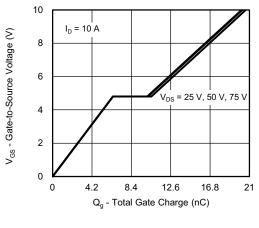
VBsemi VBsemi.com



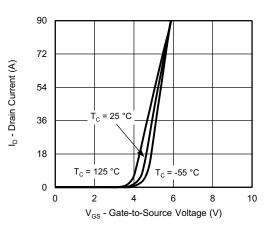
Output Characteristics



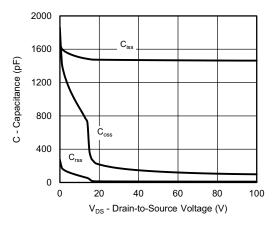
On-Resistance vs. Drain Current and Gate Voltage



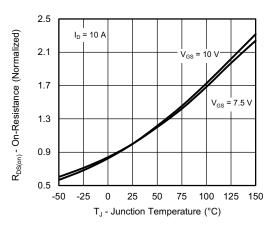
Gate Charge



Transfer Characteristics

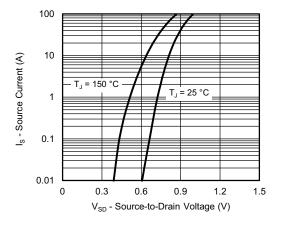


Capacitance

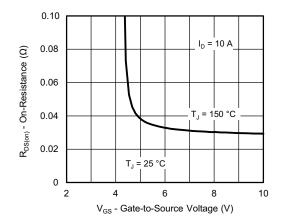


On-Resistance vs. Junction Temperature

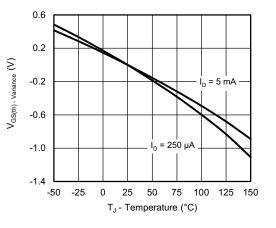




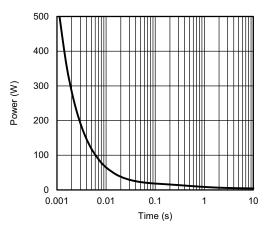
Source-Drain Diode Forward Voltage



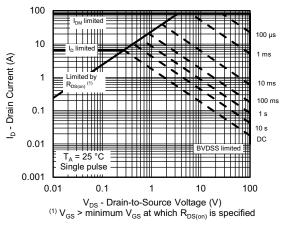
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

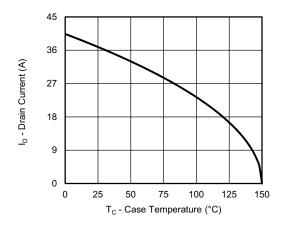


Single Pulse Power, Junction-to-Ambient

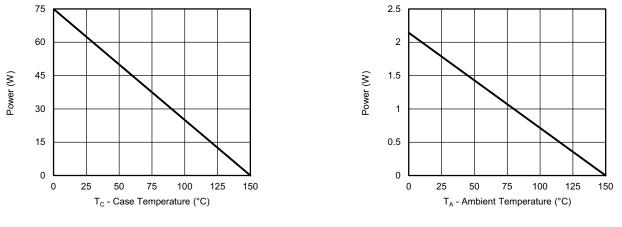


Safe Operating Area, Junction-to-Ambient





Current Derating ^a



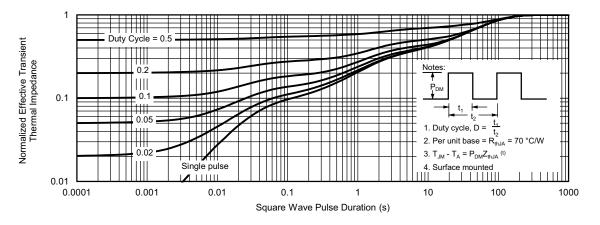
Power, Junction-to-Case

Power, Junction-to-Ambient

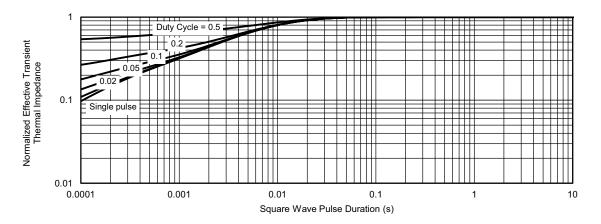
Note

a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit



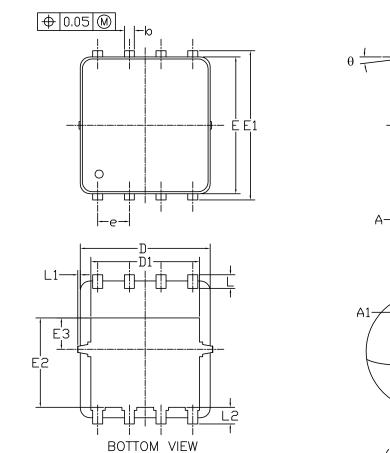


Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case





DFN5x6_8L_EP1_P PACKAGE OUTLIN

A1

С

VIEW 'A'

(SCALE 5:1)

RECOMMENDED LAND PATTERN .60 -0.55 0.50 -0.77 -0.635 4.12 6.15 -1.60 + 0.65 +|+| + t -11.27-0.50-

SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
SYMBOLS	MIN	NOM	MAX	MIN	NOM	MAX
A	0.85	0.95	1.00	0.033	0.037	0.039
Al	0.00		0.05	0.000		0.002
b	0.30	0.40	0.50	0.012	0.016	0.020
с	0.15	0.20	0.25	0.006	0.008	0.010
D	5.10	5.20	5.30	0.201	0.205	0.209
D1	4.25	4.35	4.45	0.167	0.171	0.175
E	5.45	5.55	5.65	0.215	0.219	0.222
E1	5.95	6.05	6.15	0.234	0.238	0.242
E2	3.525	3.625	3.725	0.139	0.143	0.147
E3	1.175	1.275	1.375	0.046	0.050	0.054
е	1.27 BSC				0.050 BSC	
L	0.45	0.55	0.65	0.018	0.022	0.026
L1	0		0.15	0		0.006
L2	0.68 REF			0.027 REF		
θ	0°		10°	0°		10°

NOTE

UNIT: mm

 PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS. MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH.
 CONTROLLING DIMENSION IS MILLIMETER.

CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.



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