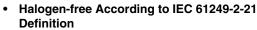


AON6240-VB Datasheet N-Channel 40-V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A) ^a	Q _g (Typ.)		
40	0.0025 at V _{GS} = 10 V	120	38 nC		
	0.0028 at V _{GS} = 6.5 V	105	30 110		

FEATURES

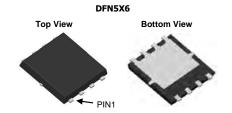


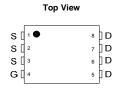


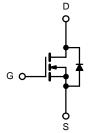
- Trench Power MOSFET
- 100 % R_g Tested
- 100 % UIS Tested

APPLICATIONS

- Synchronous Rectification
- Secondary Side DC/DC







N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	$T_A = 25 ^{\circ}C$, unles	ss otherwise not	ed	
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V_{DS}	40	V	
Gate-Source Voltage		V_{GS}	± 20	v
Continuous Drain Current (T _J = 150 °C) Pulsed Drain Current	$T_{C} = 25 ^{\circ}\text{C}$ $T_{C} = 70 ^{\circ}\text{C}$ $T_{A} = 25 ^{\circ}\text{C}$ $T_{A} = 70 ^{\circ}\text{C}$ $T_{C} = 25 ^{\circ}\text{C}$	I _D	120 80 33 ^{b, c} 26 ^{b, c} 360 100	A
Continuous Source-Drain Diode Current Single Pulse Avalanche Current	T _A = 25 °C	I _S	4.9 ^{b, c} 40	
Single Pulse Avalanche Energy		E _{AS}	80	mJ
Maximum Power Dissipation	$T_{C} = 25 °C$ $T_{C} = 70 °C$ $T_{A} = 25 °C$ $T_{A} = 70 °C$	P _D	83 53 5.4 ^{b, c} 3.4 ^{b, c}	W
Operating Junction and Storage Temperature Ra	T _J , T _{stg}	- 55 to 150		
Soldering Recommendations (Peak Temperature		260	- °C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, f}	t ≤ 10 s	R_{thJA}	18	23	°C/W	
Maximum Junction-to-Case (Drain)	Steady State	R_{thJC}	1.0	1.5		

- a. Based on T_C = 25 °C.
 b. Surface mounted on 1" x 1" FR4 board.
- d. Maximum under steady state conditions is 90 °C/W.
- e. Calculated based on maximum junction temperature. Package limitation current is 80 A.

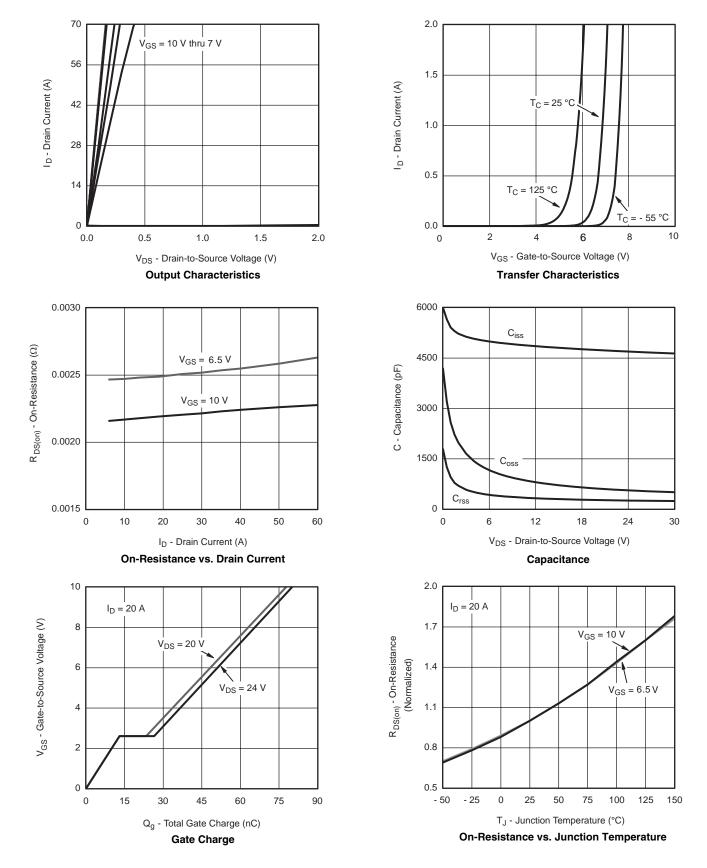
SPECIFICATIONS $T_J = 25$

Notes

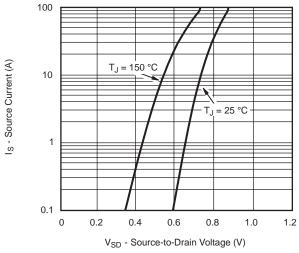
- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

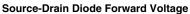
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

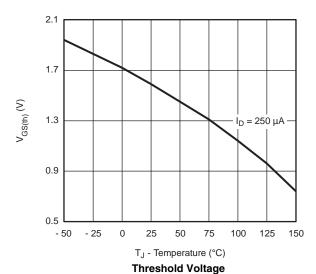


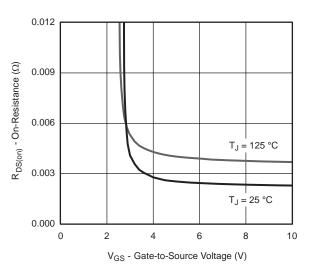




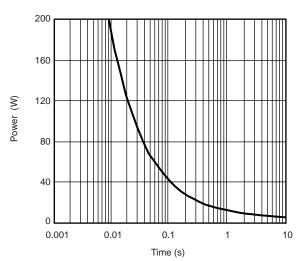




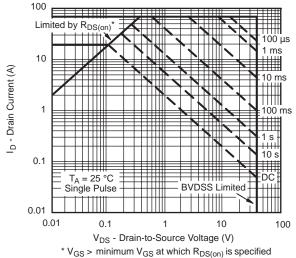




On-Resistance vs. Gate-to-Source Voltage

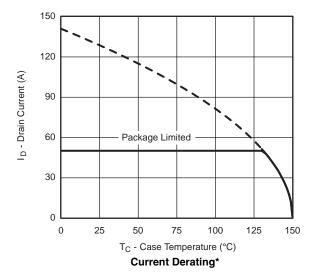


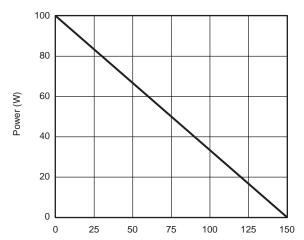
Single Pulse Power, Junction-to-Ambient



Safe Operating Area, Junction-to-Ambient

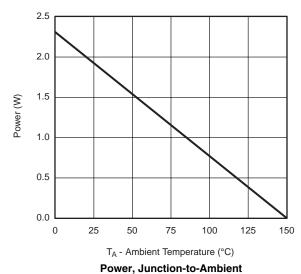






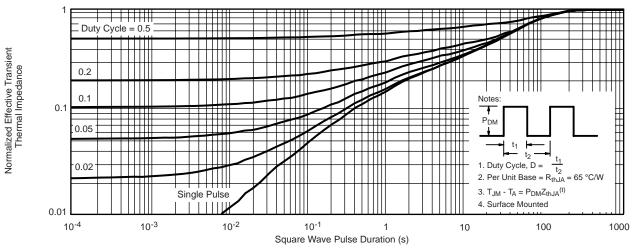
T_C - Case Temperature (°C)

Power, Junction-to-Case

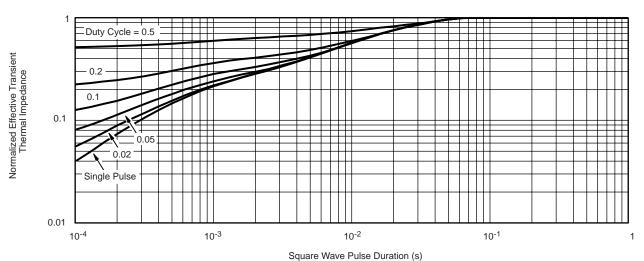


^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

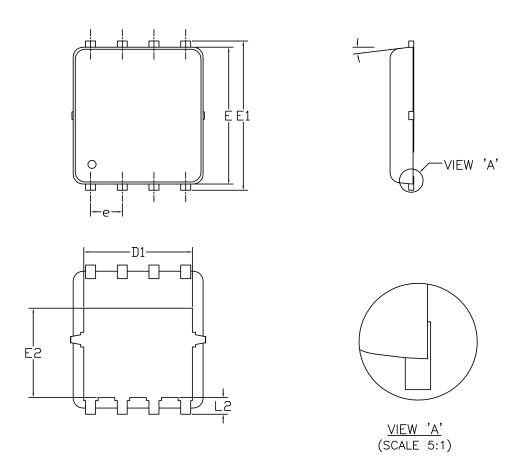


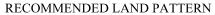


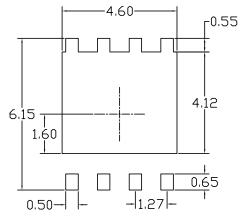
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case







				DIMENSIONS IN INCHES		CHES
		NOM	MAX	MIN	NOM	MAX
	0.85	0. 95	1.00	0.033	0.037	0.039
	0.00		0.05	0.000		0.002
	0.30		0.50	0.012	0.016	0.020
c	0.15	0. 20	0. 25	0.006	0.008	0.010
					0. 205	
D1		4. 35			0. 171	
		5. 55			0. 219	
		6.05			0. 238	
E2		3. 625			0. 143	
e		1. 27 BSC		0. 050 BSC		
L	0.45	0. 55	0.65	0.018	0.022	0.026
L1	0		0. 15	0		0.006
L2		0.68 REF		0. 027 REF		
-	0°		10°	0°		10°

NOTE

- UNIT: mm
- 1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS. MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH.
- 2. CONTROLLING DIMENSION IS MILLIMETER. CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.



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