

AM7430N-T1-PF-VB Datasheet
N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A) ^{a, e}	Q_g (Typ)
30	0.007 at $V_{GS} = 10$ V	80	31 nC
	0.009 at $V_{GS} = 4.5$ V	60	

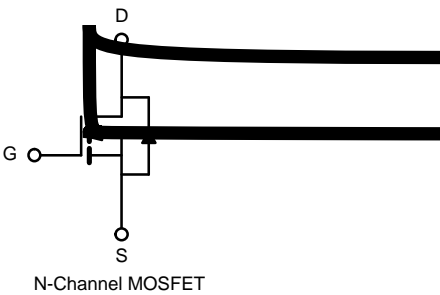
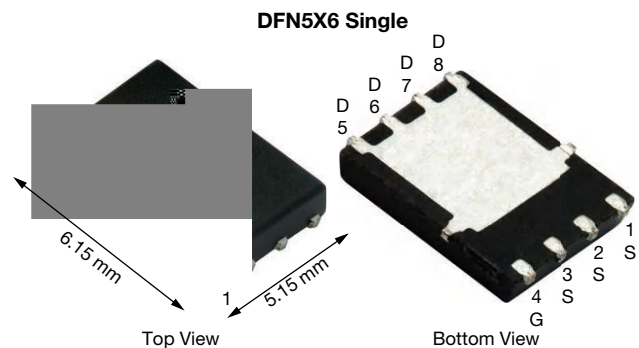
FEATURES

- Trench Power MOSFET
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2011/65/EU



APPLICATIONS

- OR-ing
- Server
- DC/DC



ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C, unless otherwise noted)

Parameter		Symbol	Limit		Unit
Drain-Source Voltage		V _{DS}	30		V
Gate-Source Voltage		V _{GS}	± 20		
Continuous Drain Current (T _J = 175 °C)	T _C = 25 °C	I _D	80		A
	T _C = 70 °C		60		
	T _A = 25 °C		50 ^{b, c}		
	T _A = 70 °C		45 ^{b, c}		
Pulsed Drain Current		I _{DM}	210		
Avalanche Current Pulse	L = 0.1 mH	I _{AS}	60		
Single Pulse Avalanche Energy		E _{AS}	95		mJ
Continuous Source-Drain Diode Current	T _C = 25 °C	I _S	80		A
	T _A = 25 °C				
Maximum Power Dissipation	T _C = 25 °C	P _D	155		W
	T _C = 70 °C		105		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175		°C
THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typ.	Max.	Unit
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 sec	R _{thJA}	32	40	°C/W
Maximum Junction-to-Case	Steady State	R _{thJC}	0.5	0.6	

C/W.
ature. Package limitation current is 90 A.

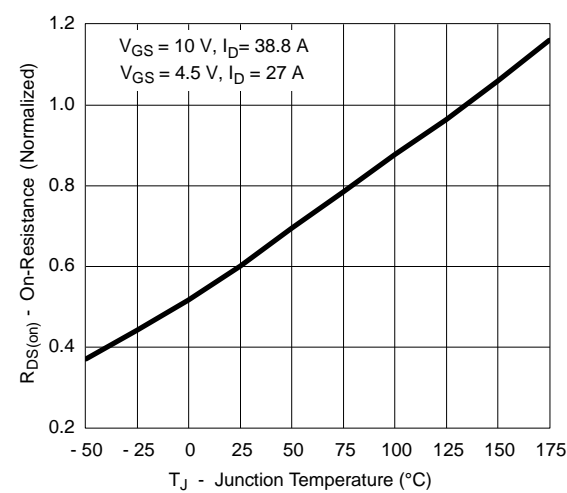
SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	30			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = 250 μA		35		mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J			- 7.5		
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	0.5		2.0	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V			1	μA
		V _{DS} = 30 V, V _{GS} = 0 V, T _J = 55 °C			10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	90			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 30.8 A		0.007		Ω
		V _{GS} = 4.5 V, I _D = 27 A		0.009		
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 30.8 A		160		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz			1180	pF
Output Capacitance	C _{oss}				425	
Reverse Transfer Capacitance	C _{rss}				170	
Total Gate Charge	Q _g	V _{DS} = 15 V, V _{GS} = 10 V, I _D = 30.8 A			61	nC
		V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 27.8 A			31.5	
Gate-Source Charge	Q _{gs}				10	
Gate-Drain Charge	Q _{gd}				6	
Gate Resistance	R _g	f = 1 MHz		1.4	2.1	Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 0.625 Ω I _D ≅ 24 A, V _{GEN} = 10 V, R _g = 1 Ω		18	27	ns
Rise Time	t _r			11	17	
Turn-Off Delay Time	t _{d(off)}			70	105	
Fall Time	t _f			10	15	
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 0.67 Ω I _D ≅ 22.5 A, V _{GEN} = 4.5 V, R _g = 1 Ω		55	83	
Rise Time	t _r			180	270	
Turn-Off Delay Time	t _{d(off)}			55	83	
Fall Time	t _f			12	18	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C		60		A
Pulse Diode Forward Current ^a	I _{SM}			210		
Body Diode Voltage	V _{SD}	I _S = 22 A		0.8	1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = 20 A, di/dt = 100 A/μs, T _J = 25 °C		52	78	ns
Body Diode Reverse Recovery Charge	Q _{rr}			70.2	105	nC
Reverse Recovery Fall Time	t _a			27		ns
Reverse Recovery Rise Time	t _b			25		

Notes:

- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
 b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

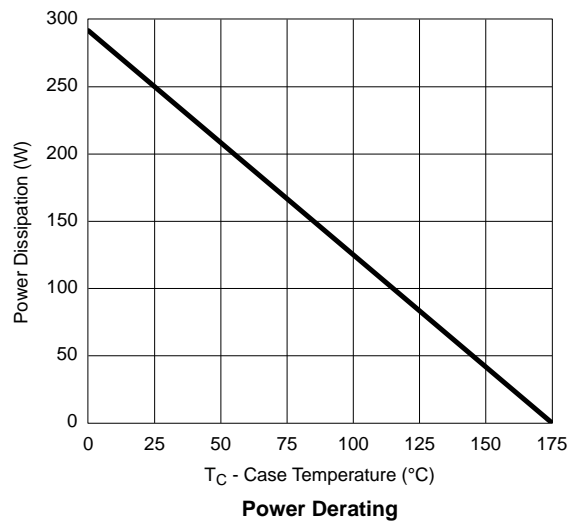
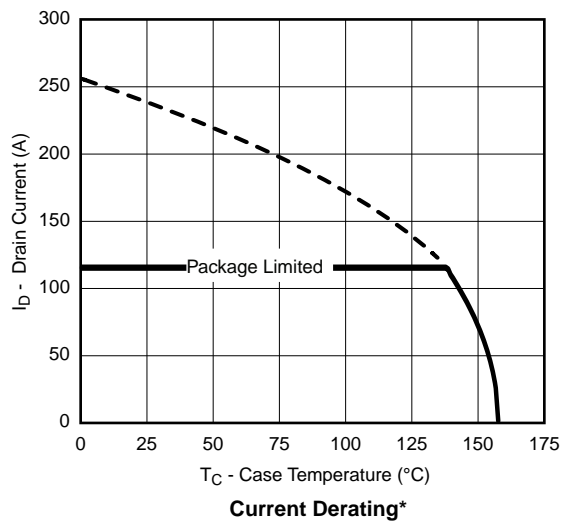
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



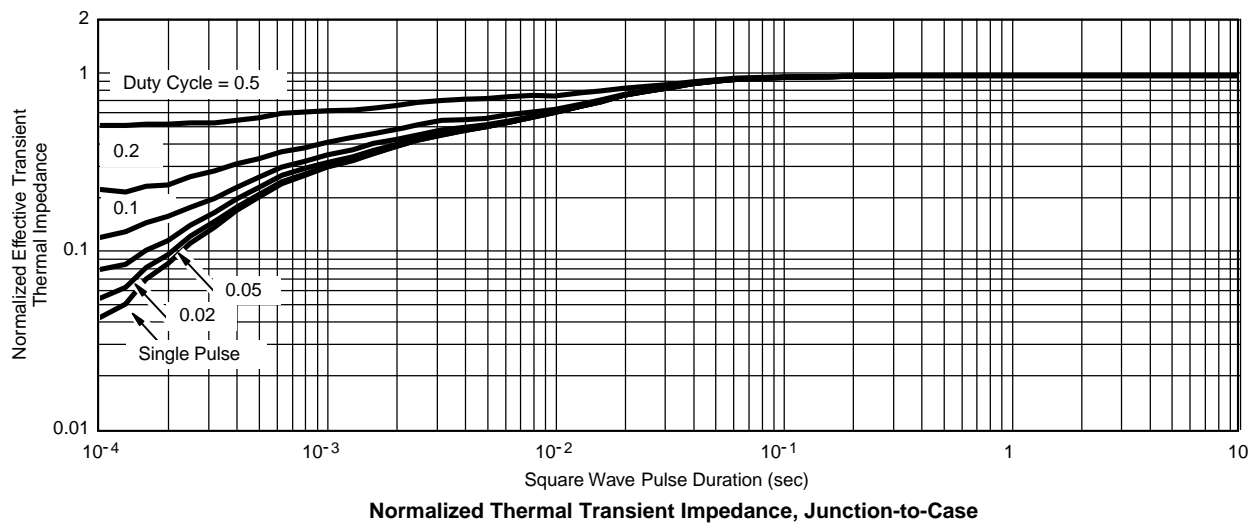
On-Resistance vs. Junction Temperature

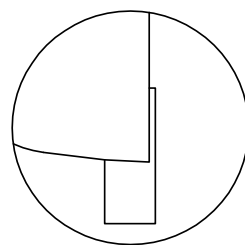
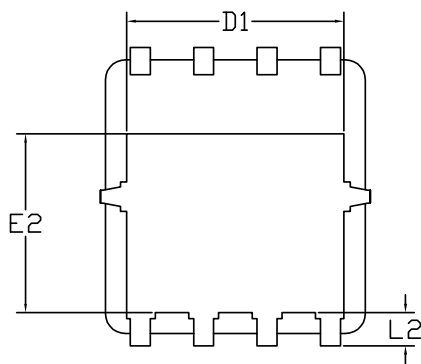
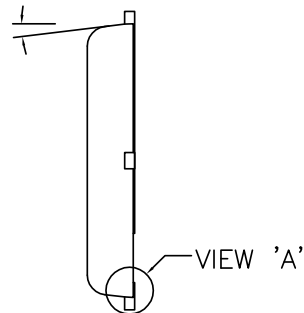
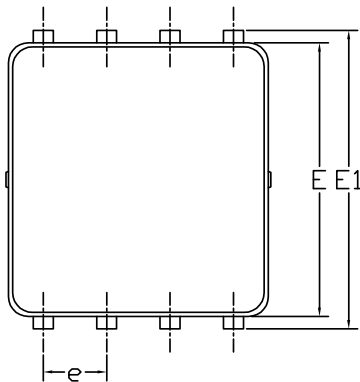
$R_{DS(on)}$ vs. V_{GS} vs. Temperature

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

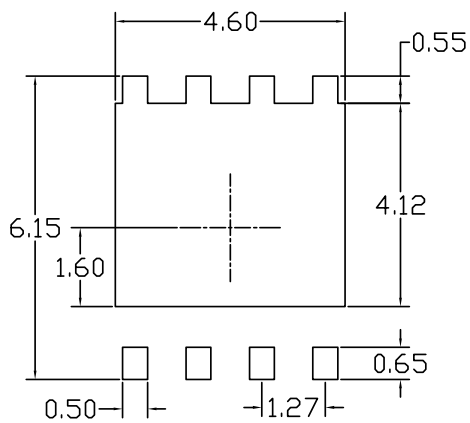


*The power dissipation P_D is based on $T_{J(max)} = 175$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





VIEW 'A'
(SCALE 5:1)



D1	4.35	0.171
E2	3.625	0.143
L2	0.68 REF	0.027 REF

UNIT: mm

NOTE

1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH.
2. CONT \

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