## 0908NS-VB Datasheet N-Channel 30-V (D-S) MOSFET

#### **FEATURES**

- Trench Power MOSFET
- 100 % R<sub>g</sub> and UIS Tested
- Compliant to RoHS Directive 2011/65/EU

#### **APPLICATIONS**

- OR-ing
- Server
- DC/DC

Notes: a. Based on  $T_C = 25$  °C.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static					L		
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	J 250 \		35		1//00	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA		- 7.5		mV/°C	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	0.5		2.0	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V		1			
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10	μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	90			Α	
		$V_{GS} = 10 \text{ V}, I_D = 30.8 \text{ A}$	0.007		<u> </u>		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 27 \text{ A}$		0.009		Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_D = 30.8 \text{ A}$		160		S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>				1180	pF	
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$			425		
Reverse Transfer Capacitance	C <sub>rss</sub>				170		
Total Gate Charge	Qg	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 30.8 \text{ A}$			61	nC	
					31.5		
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 27.8 \text{ A}$			10		
Gate-Drain Charge	$Q_{gd}$				6		
Gate Resistance	R <sub>g</sub>	f = 1 MHz		1.4	2.1	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			18	27	- ns	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 0.625 $\Omega$		11	17		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong 24$ A, $V_{GEN}=10$ V, $R_g=1$ $\Omega$		70	105		
Fall Time	t <sub>f</sub>			10	15		
Turn-On Delay Time	t <sub>d(on)</sub>			55	83		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 0.67 $\Omega$		180	270		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 22.5 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		55	83		
Fall Time	t <sub>f</sub>			12	18		
Drain-Source Body Diode Characteristic	cs						
Continuous Source-Drain Diode Current	I <sub>S</sub>	$T_C = 25  ^{\circ}C$		60		Δ	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>			210		A	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 22 A		0.8	1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			52	78	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 20 A, di/dt = 100 A/μs, T <sub>.I</sub> = 25 °C		70.2	105	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = 20 \text{ A}, \text{ u/u} = 100 \text{ A/}\mu\text{s}, I_J = 25 \text{ C}$		27		ns	
Reverse Recovery Rise Time	t <sub>b</sub>			25			

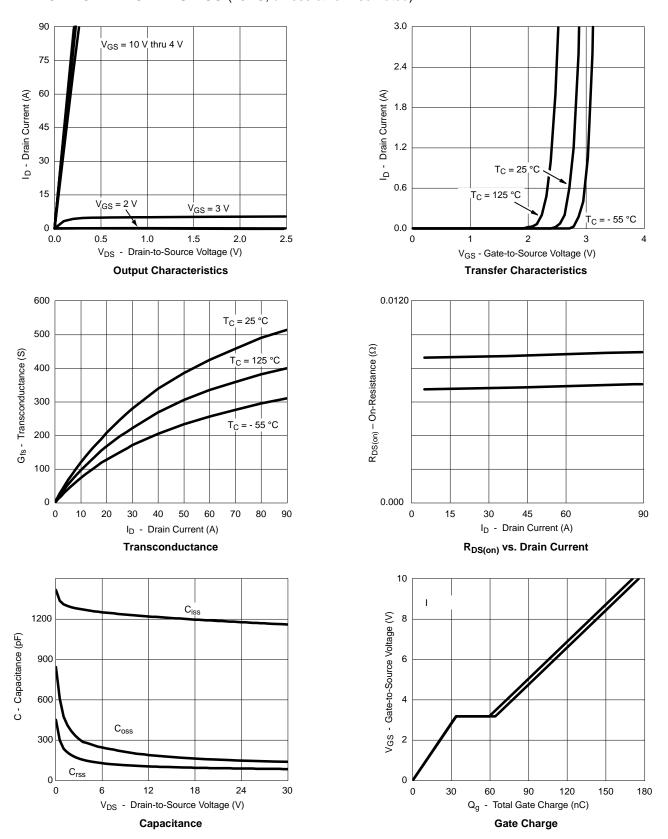
#### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

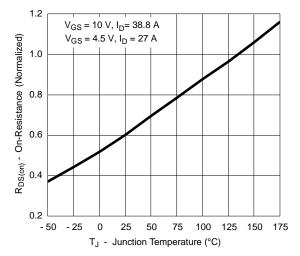
服务热线:400-655-8788

### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

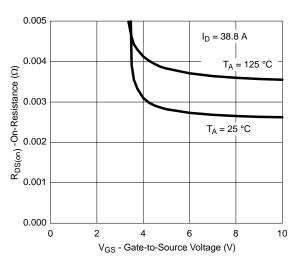




#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



On-Resistance vs. Junction Temperature



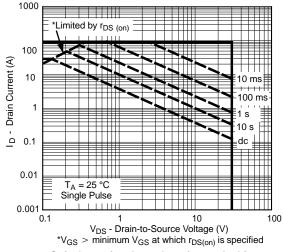
 $R_{DS(on)}\, vs.\, V_{GS}\, vs.\, Temperature$ 



Forward Diode Voltage vs. Temperature



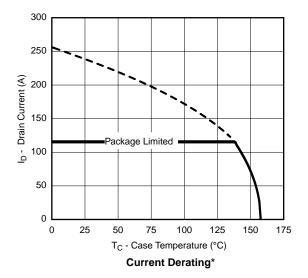
Threshold Voltage



Safe Operating Area, Junction-to-Ambient

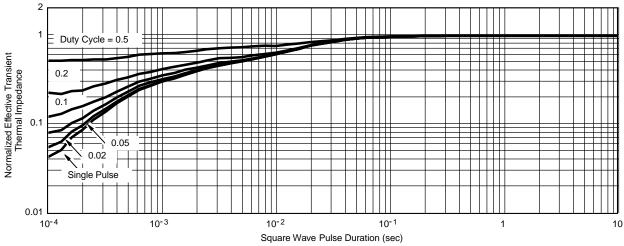


#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





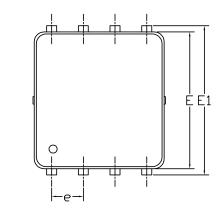
\*The power dissipation  $P_D$  is based on  $T_{J(max)}$  = 175 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

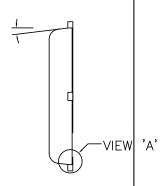


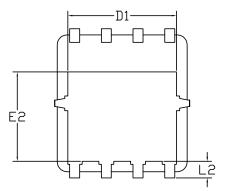
Normalized Thermal Transient Impedance, Junction-to-Case

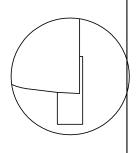
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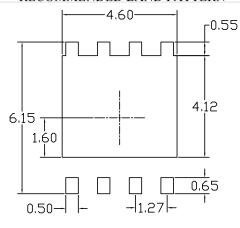






<u>VIEW 'A'</u> (SCALE 5:1)

#### RECOMMENDED LAND PATTERN



	0. 85 0. 00 0. 30		
c	0. 15	0. 20	
D1		4. 35	
		5. 55 6. 05	
E2		3. 625	
e		1. 27 BSC	
L	0.45	0.55	
L1	0		
L2		0.68 REF	
	0°		

E2		3. 625	
e		1. 27 BSC	
L	0.45	0. 55	
L1	0		
L2		0.68 REF	
	0°		

	0.000
	0.002
	0.020
	0.010
c'	
	0.026
	0.006
7	
	10°

CHES

MAX

0.039

#### NOTE

- UNIT: mm 1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS
- MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6
- 2. CONTROLLING DIMENSION IS MILLIMETER. CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.



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