

060P03NS3E-VB Datasheet

P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A) ^a	Q _g (Typ.)			
- 30	0.004 at V _{GS} = - 10 V	- 120	130 nC			
	0.006 at $V_{GS} = -4.5 \text{ V}$	- 100	130110			

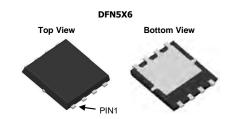
FEATURES

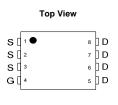
- Halogen-free
- Trench Power MOSFET
- 100 %Rg Tested

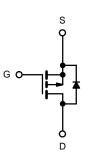


APPLICATIONS

- Notebook
 - Load Switch







P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unle	ss otherwise no	ted		
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V _{DS}	- 30	V		
Gate-Source Voltage	V _{GS}	± 20	٧		
Continuous Drain Current (T _J = 150 °C)	$T_{C} = 25 \text{ °C}$ $T_{C} = 70 \text{ °C}$ $T_{A} = 25 \text{ °C}$ $T_{A} = 70 \text{ °C}$	I _D	- 120 ^a - 100 ^a - 31.6 ^{b, c} - 25.3 ^{b, c}	A	
Pulsed Drain Current		I _{DM}	- 280		
Continuous Source-Drain Diode Current	T _C = 25 °C T _A = 25 °C	I _S	- 80 ^a - 56 ^{b, c}		
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	- 60		
Single Pulse Avalanche Energy	L = 0.1 IIII1	E _{AS}	160	mJ	
Maximum Power Dissipation	$T_{C} = 25 \text{ °C}$ $T_{C} = 70 \text{ °C}$ $T_{A} = 25 \text{ °C}$ $T_{A} = 70 \text{ °C}$	P _D	110 83 6.95 ^{b, c} 5.0 ^{b, c}	W	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C		
Soldering Recommendations (Peak Temperature)		260	C		

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Typical	Maximum	Unit			
Maximum Junction-to-Ambient ^{b, f}	t ≤ 10 s	R_{thJA}	15	20	°C/W		
Maximum Junction-to-Case (Drain)	Steady State	R_{thJC}	0.9	1.2	C/VV		

Notes:

- a. Package limited.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s.
- d. The DFN5x6 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- e. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.
- f. Maximum under Steady State conditions is 54 °C/W.

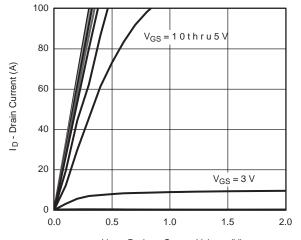


Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static					L	
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 30			V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	J 250A		- 31		mV/°C
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = - 250 μA		6.5		
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$	- 1.0		- 3.0	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
7 0		$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$			- 1	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 30 V, V _{GS} = 0 V, T _J = 55 °C			- 10	μA
On-State Drain Current ^a	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 10 V	- 30			Α
_		V _{GS} = - 10 V, I _D = - 20 A		0.004		Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 15 A		0.006		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 20 A		97		S
Dynamic ^b				1		
Input Capacitance	C _{iss}			7050		
Output Capacitance	C _{oss}	$V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		1375		pF
Reverse Transfer Capacitance	C _{rss}			1215		
Total Gate Charge	Qg	V _{DS} = - 15 V, V _{GS} = - 10 V, I _D = - 20 A		130	250	nC
				78	130	
Gate-Source Charge	Q_{gs}	$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -20 \text{ A}$		29		
Gate-Drain Charge	Q_{gd}			37		
Gate Resistance	R _g	f = 1 MHz		1.9		Ω
Turn-On Delay Time	t _{d(on)}			25	40	
Rise Time	t _r	V_{DD} = - 15 V, R_L = 15 Ω		15	30	ns
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ - 1.0 A, V_{GEN} = - 10 V, R_g = 1 Ω		110	170	
Fall Time	t _f			30	50	
Turn-On Delay Time	t _{d(on)}			110	170	
Rise Time	t _r	V_{DD} = - 15 V, R_L = 15 Ω		100	150	
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ - 1.0 A, V_{GEN} = - 4.5 V, R_g = 1 Ω		100	150	
Fall Time	t _f			50	75	
Drain-Source Body Diode Characteristi	cs			1	<u>I</u>	
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			100	۸
Pulse Diode Forward Current ^a	I _{SM}				120	A
Body Diode Voltage	V _{SD}	I _S = - 5 A		- 0.54	- 1.1	V
Body Diode Reverse Recovery Time	t _{rr}			50	100	ns
Body Diode Reverse Recovery Charge	Q _{rr}	1 25 A dl/dt 400 A/ T 05 00		65	130	nC
Reverse Recovery Fall Time	t _a	$I_F = 3.5 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		26		
Reverse Recovery Rise Time	th	t _b		24		ns

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$ b. Guaranteed by design, not subject to production testing.

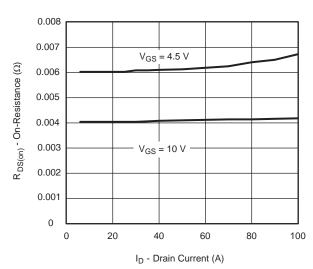
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



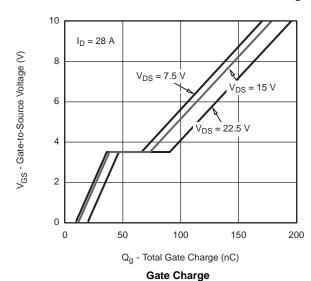


 V_{DS} - Drain-to-Source Voltage (V)

Output Characteristics



On-Resistance vs. Drain Current and Gate Voltage



0.0

0.0

0.5 1.0 1.5 2.0 2.5

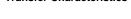
V_{DS} - Drain-to-Source Voltage (V)

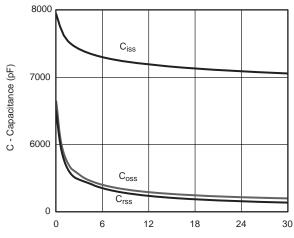
3.0

 $T_C = -55 \, ^{\circ}C$

T_C = 25 °C

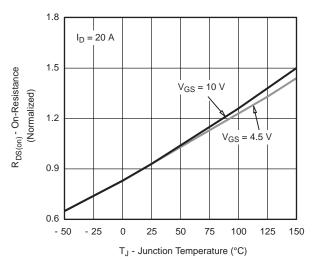
Transfer Characteristics





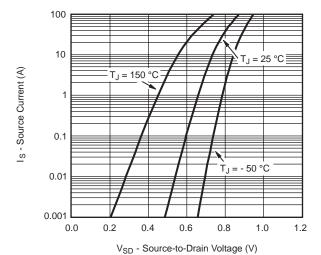
V_{DS} - Drain-to-Source Voltage (V)

Capacitance

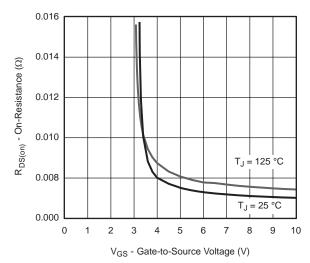


On-Resistance vs. Junction Temperature

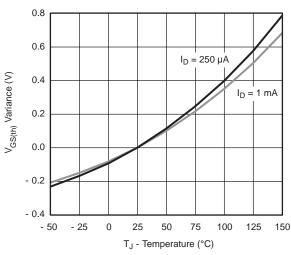




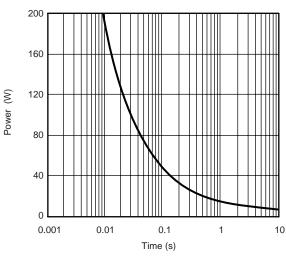
Source-Drain Diode Forward Voltage



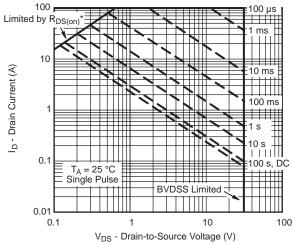
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



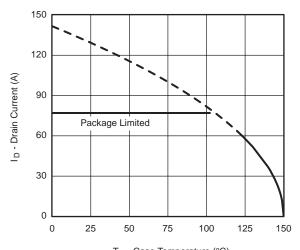
Single Pulse Power, Junction-to-Ambient



* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

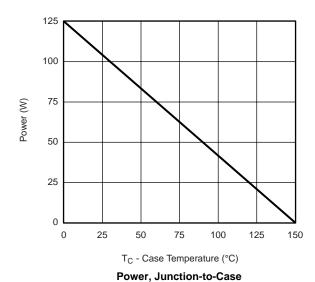
Safe Operating Area, Junction-to-Ambient

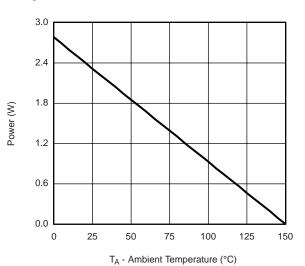




T_C - Case Temperature (°C)

Current Derating*

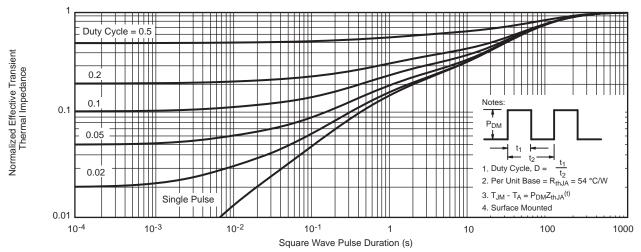




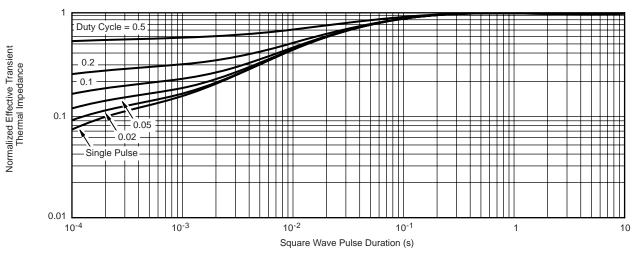
Power, Junction-to-Ambient

^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





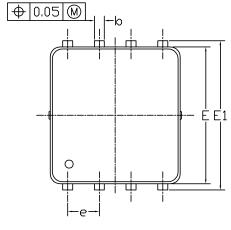
Normalized Thermal Transient Impedance, Junction-to-Ambient

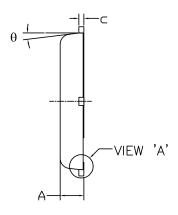


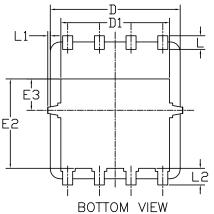
Normalized Thermal Transient Impedance, Junction-to-Case

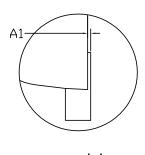


DFN5x6_8L_EP1_P PACKAGE OUTLIN



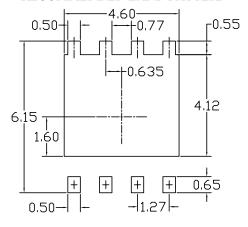






<u>VIEW 'A'</u> (SCALE 5:1)

RECOMMENDED LAND PATTERN



SYMBOLS DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES				
21 MBOLS	MIN	NOM	MAX	MIN	NOM	MAX	
A	0.85	0. 95	1.00	0.033	0.037	0.039	
Al	0.00		0.05	0.000		0.002	
b	0.30	0.40	0.50	0.012	0.016	0.020	
c	0. 15	0. 20	0. 25	0.006	0.008	0.010	
D	5. 10	5. 20	5. 30	0. 201	0. 205	0. 209	
D1	4. 25	4. 35	4. 45	0. 167	0.171	0. 175	
Е	5. 45	5. 55	5. 65	0. 215	0. 219	0. 222	
E1	5. 95	6.05	6. 15	0. 234	0. 238	0. 242	
E2	3. 525	3.625	3. 725	0.139	0.143	0. 147	
E3	1. 175	1.275	1.375	0.046	0.050	0.054	
e	1. 27 BSC			0. 050 BSC			
L	0.45	0. 55	0.65	0.018	0.022	0.026	
L1	0		0. 15	0		0.006	
L2	0.68 REF			0. 027 REF			
θ	0°		10°	0°		10°	

NOTE

- UNIT: mm
- 1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS. MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH.
- 2. CONTROLLING DIMENSION IS MILLIMETER. CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.



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